



# SPEC® CFP2006 Result

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**NEC Corporation**

**SPECfp®2006 = 73.4**

Express5800/R120e-1E (Intel Xeon E5-2420 v2)

**SPECfp\_base2006 = 70.9**

CPU2006 license: 9006

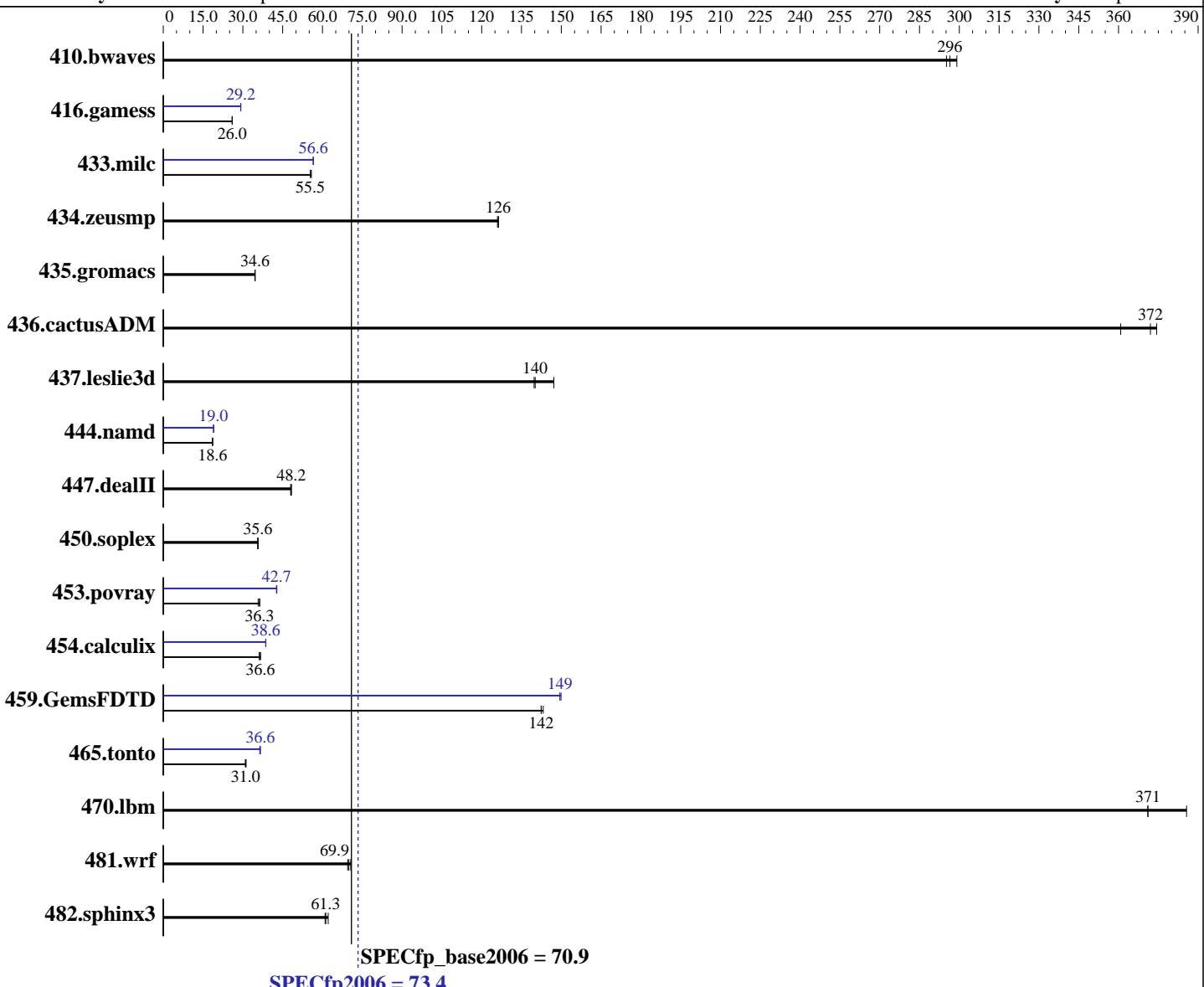
Test date: Jan-2014

Test sponsor: NEC Corporation

Hardware Availability: Jan-2014

Tested by: NEC Corporation

Software Availability: Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2420 v2  
CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz  
CPU MHz: 2200  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
Compiler: Kernel 2.6.32-358.18.1.el6.x86\_64  
C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>45.8</b>	<b>296</b>	46.0	295	45.4	299	<b>45.8</b>	<b>296</b>	46.0	295	45.4	299
416.gamess	752	26.0	<b>754</b>	<b>26.0</b>	755	25.9	<b>671</b>	<b>29.2</b>	671	29.2	669	29.3
433.milc	164	55.8	<b>165</b>	<b>55.5</b>	166	55.5	<b>163</b>	<b>56.5</b>	<b>162</b>	<b>56.6</b>	162	56.6
434.zeusmp	72.0	126	72.3	126	<b>72.1</b>	<b>126</b>	72.0	126	72.3	126	<b>72.1</b>	<b>126</b>
435.gromacs	<b>206</b>	<b>34.6</b>	207	34.5	206	34.6	<b>206</b>	<b>34.6</b>	207	34.5	206	34.6
436.cactusADM	33.1	361	<b>32.1</b>	<b>372</b>	31.9	374	33.1	361	<b>32.1</b>	<b>372</b>	31.9	374
437.leslie3d	67.3	140	<b>67.1</b>	<b>140</b>	63.9	147	<b>67.3</b>	140	<b>67.1</b>	<b>140</b>	63.9	147
444.namd	431	18.6	432	18.6	<b>432</b>	<b>18.6</b>	423	19.0	<b>423</b>	<b>19.0</b>	423	19.0
447.dealII	238	48.0	<b>237</b>	<b>48.2</b>	237	48.3	238	48.0	<b>237</b>	<b>48.2</b>	237	48.3
450.soplex	233	35.7	<b>234</b>	<b>35.6</b>	234	35.6	<b>233</b>	35.7	<b>234</b>	<b>35.6</b>	234	35.6
453.povray	<b>147</b>	<b>36.3</b>	149	35.8	146	36.4	124	42.8	<b>124</b>	<b>42.7</b>	125	42.7
454.calculix	228	36.1	225	36.6	<b>225</b>	<b>36.6</b>	214	38.6	213	38.7	<b>214</b>	<b>38.6</b>
459.GemsFDTD	74.1	143	<b>74.5</b>	<b>142</b>	74.5	142	70.8	150	<b>71.0</b>	<b>149</b>	71.0	149
465.tonto	315	31.2	<b>317</b>	<b>31.0</b>	318	31.0	269	36.6	<b>269</b>	<b>36.6</b>	271	36.4
470.lbm	35.6	386	37.0	371	<b>37.0</b>	<b>371</b>	35.6	386	37.0	371	<b>37.0</b>	<b>371</b>
481.wrf	<b>160</b>	<b>69.9</b>	158	70.7	161	69.5	<b>160</b>	<b>69.9</b>	158	70.7	161	69.5
482.sphinx3	320	61.0	314	62.2	<b>318</b>	<b>61.3</b>	320	61.0	314	62.2	<b>318</b>	<b>61.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Energy Performance: Performance

Memory Voltage: 1.5 V

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

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## General Notes (Continued)

OMP\_NUM\_THREADS = "12"

The Express5800/R120e-1E and  
the Express5800/R120e-2E models are electronically equivalent.  
The results have been measured on the Express5800/R120e-2E model.

Added glibc-static-2.12-1.107.el6.x86\_64.rpm  
to enable static linking  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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