SPEC® CFP2006 Result

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NEC Corporation

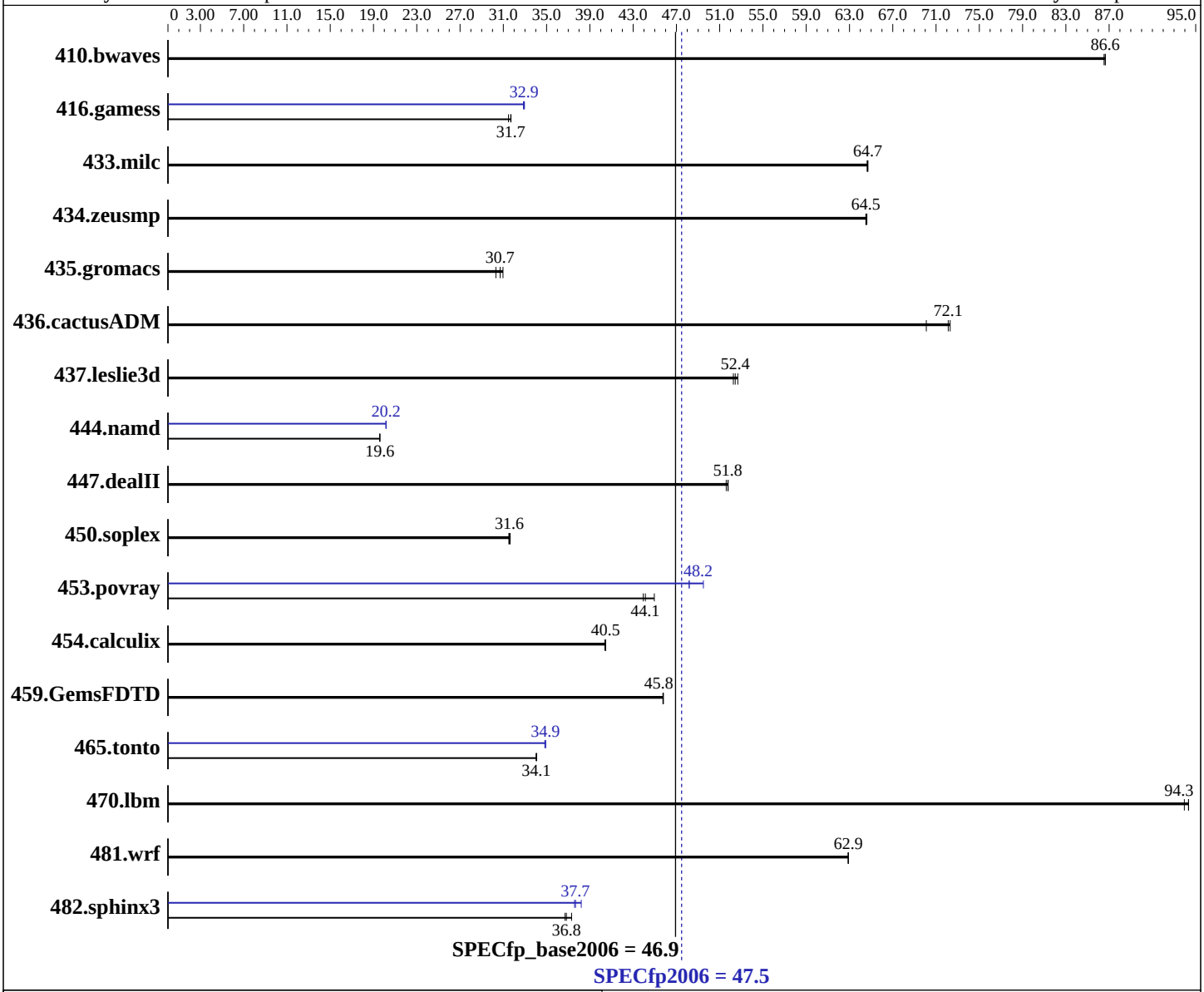
Express5800/T110f-E (Intel Celeron G1820)

SPECfp®2006 = 47.5

SPECfp_base2006 = 46.9

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jan-2014
Hardware Availability: Jan-2014
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Celeron G1820
CPU Characteristics:	
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) Kernel 2.6.32-358.18.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**157**	**86.6**	157	86.6	157	86.5	**157**	**86.6**	157	86.6	157	86.5
416.gamess	**618**	**31.7**	622	31.5	617	31.7	596	32.9	**595**	**32.9**	594	33.0
433.milc	142	64.7	142	64.6	**142**	**64.7**	142	64.7	142	64.6	**142**	**64.7**
434.zeusmp	**141**	**64.5**	141	64.6	141	64.5	**141**	**64.5**	141	64.6	141	64.5
435.gromacs	**233**	**30.7**	231	31.0	236	30.3	**233**	**30.7**	231	31.0	236	30.3
436.cactusADM	165	72.3	**166**	**72.1**	170	70.1	165	72.3	**166**	**72.1**	170	70.1
437.leslie3d	**179**	**52.4**	178	52.7	180	52.3	**179**	**52.4**	178	52.7	180	52.3
444.namd	**409**	**19.6**	410	19.6	409	19.6	**398**	**20.2**	398	20.2	398	20.2
447.dealII	**221**	**51.8**	222	51.6	221	51.8	**221**	**51.8**	222	51.6	221	51.8
450.soplex	264	31.6	265	31.5	**264**	**31.6**	264	31.6	265	31.5	**264**	**31.6**
453.povray	121	43.9	**121**	**44.1**	118	45.0	107	49.5	110	48.2	**110**	**48.2**
454.calculix	204	40.5	204	40.4	**204**	**40.5**	204	40.5	204	40.4	**204**	**40.5**
459.GemsFDTD	232	45.8	**232**	**45.8**	232	45.8	232	45.8	**232**	**45.8**	232	45.8
465.tonto	289	34.0	**289**	**34.1**	289	34.1	282	34.8	282	34.9	**282**	**34.9**
470.lbm	**146**	**94.3**	146	94.0	146	94.3	**146**	**94.3**	146	94.0	146	94.3
481.wrf	178	62.9	**178**	**62.9**	178	62.9	178	62.9	**178**	**62.9**	178	62.9
482.sphinx3	531	36.7	**529**	**36.8**	522	37.3	519	37.6	**517**	**37.7**	510	38.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "2"
```

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General Notes (Continued)

```
Added glibc-static-2.12-1.107.el6.x86_64.rpm
to enable static linking
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120-RevB.xml

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For other inquiries, please contact webmaster@spec.org.

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