SPEC® CINT2006 Result

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IBM Corporation

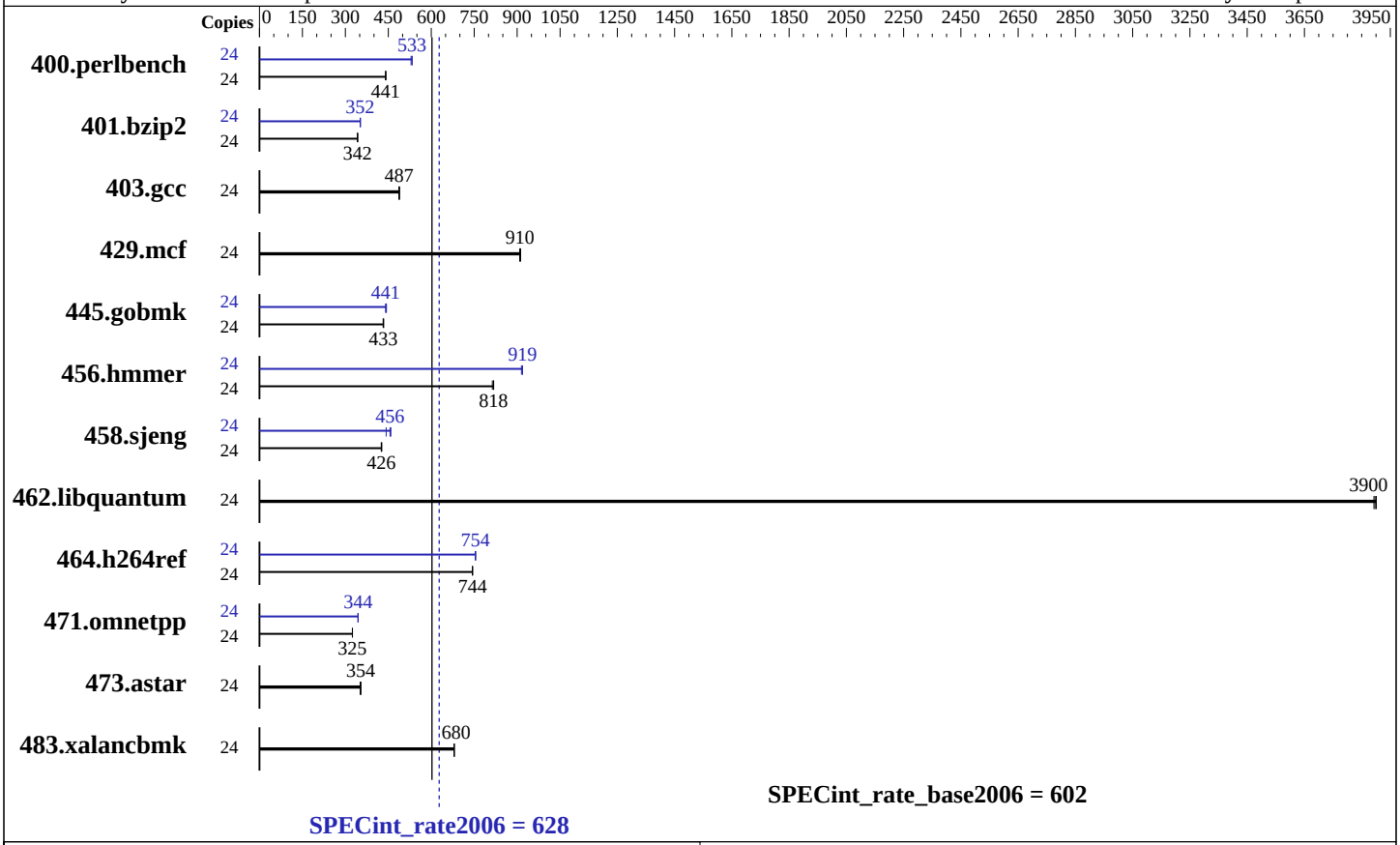
IBM BladeCenter HS23
(Intel Xeon E5-2643 v2, 3.50 GHz)

SPECint®_rate2006 = 628

SPECint_rate_base2006 = 602

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2014
Hardware Availability: Dec-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2643 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	1 x 300 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	532	441	532	441	**532**	**441**	24	**440**	**533**	440	533	443	529
401.bzip2	24	677	342	675	343	**676**	**342**	24	658	352	657	352	**658**	**352**
403.gcc	24	395	489	**396**	**487**	396	487	24	395	489	**396**	**487**	396	487
429.mcf	24	**240**	**910**	240	912	241	909	24	**240**	**910**	240	912	241	909
445.gobmk	24	580	434	583	432	**581**	**433**	24	568	443	**571**	**441**	571	441
456.hmmer	24	274	818	275	813	**274**	**818**	24	244	919	**244**	**919**	245	915
458.sjeng	24	683	425	**682**	**426**	680	427	24	656	443	**637**	**456**	633	459
462.libquantum	24	127	3900	**128**	**3900**	128	3890	24	127	3900	**128**	**3900**	128	3890
464.h264ref	24	714	744	714	744	**714**	**744**	24	703	755	705	754	**704**	**754**
471.omnetpp	24	462	325	462	324	**462**	**325**	24	**436**	**344**	435	345	436	344
473.astar	24	479	352	476	354	**476**	**354**	24	479	352	476	354	**476**	**354**
483.xalancbmk	24	243	681	244	680	**244**	**680**	24	243	681	244	680	**244**	**680**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Operating Mode set to Maximum Perfomance in BIOS
Sysinfo program /cpu2006.1.2_14.0_updated/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on HS23-IVB Fri Jan 17 15:15:34 2014

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2643 v2 @ 3.50GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
```

Continued on next page

Platform Notes (Continued)

```
        siblings  : 12
        physical 0: cores 2 3 4 8 9 10
        physical 1: cores 2 3 4 8 9 10
     cache size : 25600 KB

  From /proc/meminfo
     MemTotal:       132131472 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.4 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux HS23-IVB 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jan 17 14:48

  SPEC is set to: /cpu2006.1.2_14.0_updated
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_hs23ivb-lv_root
                    ext4     50G  6.9G   40G  15% /

  Additional information from dmidecode:
    BIOS IBM -[TKE135FUS-1.50]- 08/14/2013
    Memory:
     8x Micron 36JDZS2G72PZ-1G9E1 16 GB 1867 MHz 2 rank
     8x Not Specified Not Specified

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/cpu2006.1.2_14.0_updated/libs/32:/cpu2006.1.2_14.0_updated/libs/64:/cpu2006.1.2_14.0_updated/sh"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RedHat EL 6.4
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
  echo 1>       /proc/sys/vm/drop_caches
  runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html`
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`

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For other inquiries, please contact webmaster@spec.org.

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