# SPEC® CINT2006 Result

**Sugon**

I620-G15 (Intel Xeon E5-2697 v2, 2.70 GHz)

<table>
<thead>
<tr>
<th>SPECint® rate2006</th>
<th>965</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006</td>
<td>933</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 9046  
**Test date:** Nov-2013  
**Test sponsor:** Sugon  
**Hardware Availability:** Nov-2013  
**Tested by:** Sugon  
**Software Availability:** Nov-2013

## Hardware

<table>
<thead>
<tr>
<th>Description</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon E5-2697 v2</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.50 GHz</td>
</tr>
<tr>
<td>CPU MHZ:</td>
<td>2700</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>24 cores, 2 chips, 12 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>30 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 3 TB SATA, 7200RPM</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

## Software

<table>
<thead>
<tr>
<th>Description</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System:</td>
<td>SUSE Linux Enterprise Server 11 (x86_64) 3.0.76-0.11-default</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 14.0.0.0.80 of Intel C++ Studio XE for Linux;</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>ext3</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (Full multiuser with network)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software:</td>
<td>Microquill SmartHeap V10.0</td>
</tr>
</tbody>
</table>

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
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CPU2006 license: 9046
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>48</td>
<td>644</td>
<td>728</td>
<td>645</td>
<td>727</td>
<td>645</td>
<td>727</td>
<td>48</td>
<td>534</td>
<td>879</td>
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<tr>
<td>401.bzip2</td>
<td>48</td>
<td>917</td>
<td>505</td>
<td>920</td>
<td>504</td>
<td>919</td>
<td>504</td>
<td>48</td>
<td>901</td>
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<td>403.gcc</td>
<td>48</td>
<td>529</td>
<td>731</td>
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<td>730</td>
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<td>729</td>
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<td>532</td>
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<td>429.mcf</td>
<td>48</td>
<td>324</td>
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<tr>
<td>445.gobmk</td>
<td>48</td>
<td>702</td>
<td>717</td>
<td>703</td>
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<td>686</td>
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<td>456.hmmer</td>
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<td>1260</td>
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<td>1270</td>
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<td>1270</td>
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<td>327</td>
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<td>458.sjeng</td>
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<td>462.libquantum</td>
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<td>871</td>
<td>1220</td>
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<td>1210</td>
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<td>1210</td>
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<td>832</td>
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<td>471.omnetpp</td>
<td>48</td>
<td>613</td>
<td>489</td>
<td>615</td>
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<td>489</td>
<td>48</td>
<td>588</td>
<td>510</td>
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<tr>
<td>473.astar</td>
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<td>654</td>
<td>516</td>
<td>656</td>
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<td>511</td>
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<tr>
<td>483.xalancbmk</td>
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<td>344</td>
<td>962</td>
<td>343</td>
<td>965</td>
<td>344</td>
<td>963</td>
<td>48</td>
<td>344</td>
<td>962</td>
<td>343</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Intel Virtualization technology set to disabled
Power Technology set to performance
Turbo boost set to enabled
DDR Speed set to force 1866
Sysinfo program /root/cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux-e494 Tue Nov 12 01:39:52 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
2 "physical id"s (chips)
48 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

  cpu cores : 12
  siblings : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  cache size : 30720 KB

From /proc/meminfo
  MemTotal: 264519660 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
    VERSION = 11
    PATCHLEVEL = 3

  uname -a:
    Linux linux-e494 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
    (ccab990) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Nov 12 01:31 last=S

SPEC is set to: /root/cpu2006

  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda3 ext3 2.7T 338G 2.4T 13% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. V8.100A 10/31/2013
  Memory:
    16x 16 GB
    16x Samsung M393B2G70QH0-CMA 16 GB 1866 MHz

(End of data from sysinfo program)
There is a error in sysinfo output. There are only 16 DIMMs in this system. The cause of this error is the sysinfo itself. The sysinfo of revision 6818 can't identify the correct memory information.
The memory information should be:
  Memory:
    16x Samsung M393B2G70QH0-CMA 16 GB 1866 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

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General Notes (Continued)

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
   icc  -m32

C++ benchmarks:
   icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
   -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
   -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
   -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
   403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
   icc  -m32

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Peak Compiler Invocation (Continued)

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prep-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prep-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prep-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prep-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prep-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
            -L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.html
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.xml
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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