# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

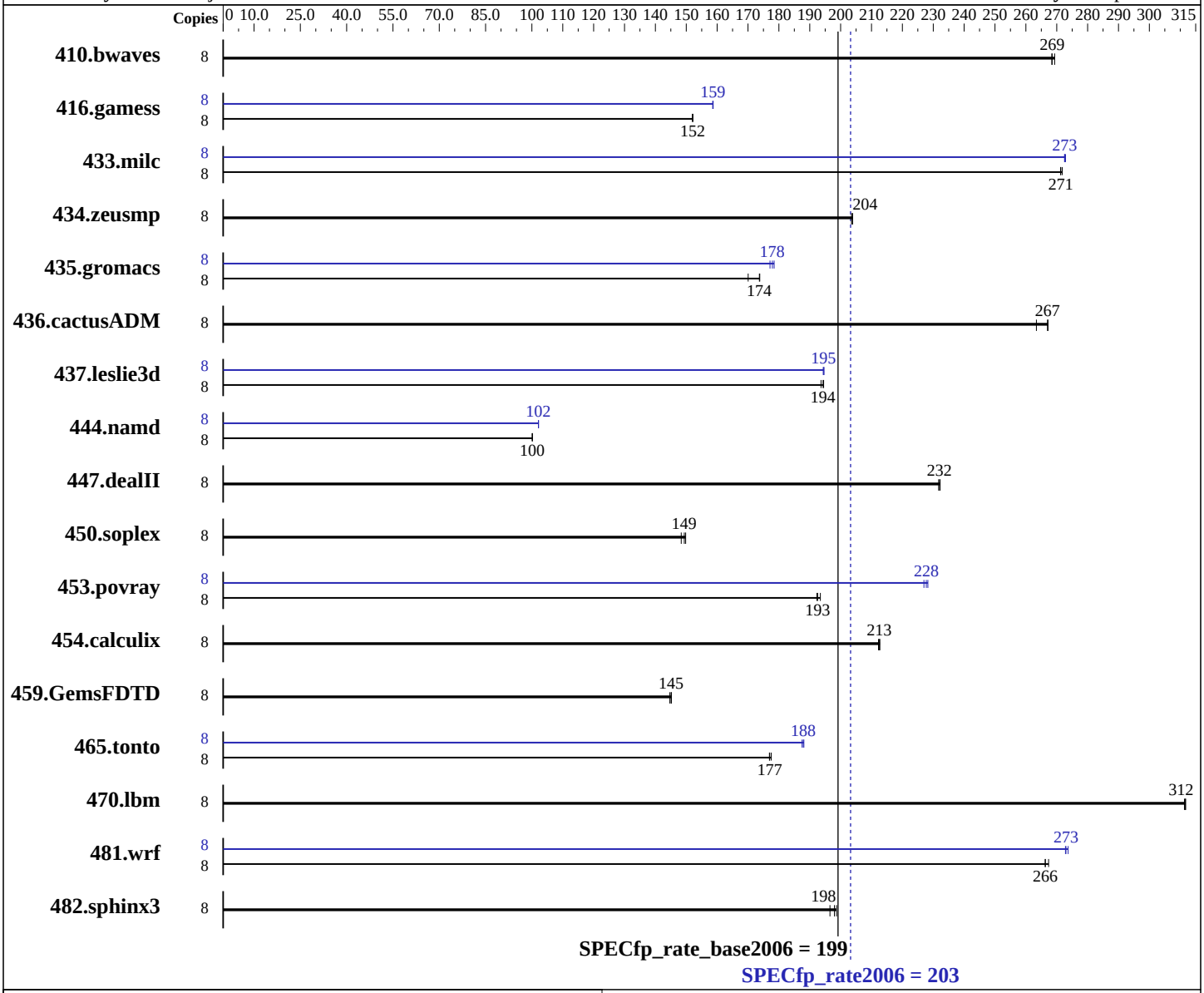
PRIMERGY TX300 S8, Intel Xeon E5-2603 v2, 1.80 GHz

SPECfp®_rate2006 = 203

SPECfp_rate_base2006 = 199

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Sep-2013
**Hardware Availability:** Oct-2013
**Software Availability:** Sep-2013




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2603 v2 |
| CPU Characteristics: | |
| CPU MHz: | 1800 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.4 (Santiago)<br>2.6.32-358.11.1.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;<br>Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY TX300 S8, Intel Xeon E5-2603 v2, 1.80 GHz

SPECfp_rate2006 = 203

SPECfp_rate_base2006 = 199

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Sep-2013
**Hardware Availability:** Oct-2013
**Software Availability:** Sep-2013

L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 405 | 268 | **405** | **269** | 404 | 269 | 8 | 405 | 268 | **405** | **269** | 404 | 269 |
| 416.gamess | 8 | **1030** | **152** | 1030 | 152 | 1030 | 152 | 8 | 987 | 159 | 988 | 159 | **987** | **159** |
| 433.milc | 8 | **271** | **271** | 271 | 271 | 270 | 272 | 8 | 269 | 273 | **269** | **273** | 269 | 273 |
| 434.zeusmp | 8 | 358 | 204 | **357** | **204** | 357 | 204 | 8 | 358 | 204 | **357** | **204** | 357 | 204 |
| 435.gromacs | 8 | 336 | 170 | **329** | **174** | 329 | 174 | 8 | 322 | 177 | 320 | 178 | **321** | **178** |
| 436.cactusADM | 8 | **358** | **267** | 363 | 263 | 358 | 267 | 8 | **358** | **267** | 363 | 263 | 358 | 267 |
| 437.leslie3d | 8 | **387** | **194** | 388 | 194 | 387 | 194 | 8 | 387 | 194 | 386 | 195 | **387** | **195** |
| 444.namd | 8 | **641** | **100** | 641 | 100 | 641 | 100 | 8 | **628** | **102** | 628 | 102 | 628 | 102 |
| 447.dealII | 8 | 394 | 232 | **394** | **232** | 395 | 232 | 8 | 394 | 232 | **394** | **232** | 395 | 232 |
| 450.soplex | 8 | 450 | 148 | 445 | 150 | **447** | **149** | 8 | 450 | 148 | 445 | 150 | **447** | **149** |
| 453.povray | 8 | **221** | **193** | 221 | 192 | 220 | 193 | 8 | 186 | 228 | **187** | **228** | 188 | 227 |
| 454.calculix | 8 | 311 | 212 | 310 | 213 | **311** | **213** | 8 | 311 | 212 | 310 | 213 | **311** | **213** |
| 459.GemsFDTD | 8 | 587 | 145 | 585 | 145 | **585** | **145** | 8 | 587 | 145 | 585 | 145 | **585** | **145** |
| 465.tonto | 8 | **445** | **177** | 445 | 177 | 443 | 178 | 8 | 419 | 188 | **419** | **188** | 420 | 188 |
| 470.lbm | 8 | **353** | **312** | 353 | 312 | 353 | 311 | 8 | **353** | **312** | 353 | 312 | 353 | 311 |
| 481.wrf | 8 | 336 | 266 | **336** | **266** | 334 | 267 | 8 | **327** | **273** | 327 | 274 | 328 | 273 |
| 482.sphinx3 | 8 | 785 | 199 | **788** | **198** | 793 | 197 | 8 | 785 | 199 | **788** | **198** | 793 | 197 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Energy Performance = Performance
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 This result was measured on the PRIMERGY RX350 S8. The PRIMERGY RX350 S8
 and the PRIMERGY TX300 S8 are electronically equivalent.
 For information about Fujitsu please visit: http://www.fujitsu.com
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

# Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

Continued on next page

## Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

Continued on next page

## Peak Optimization Flags (Continued)

454.calculix: `basepeak = yes`

481.wrf: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml`


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 17:26:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 November 2013.