



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF, Intel Xeon E5-2697 v2)

SPECfp®2006 = 103

SPECfp_base2006 = 98.4

CPU2006 license: 001176

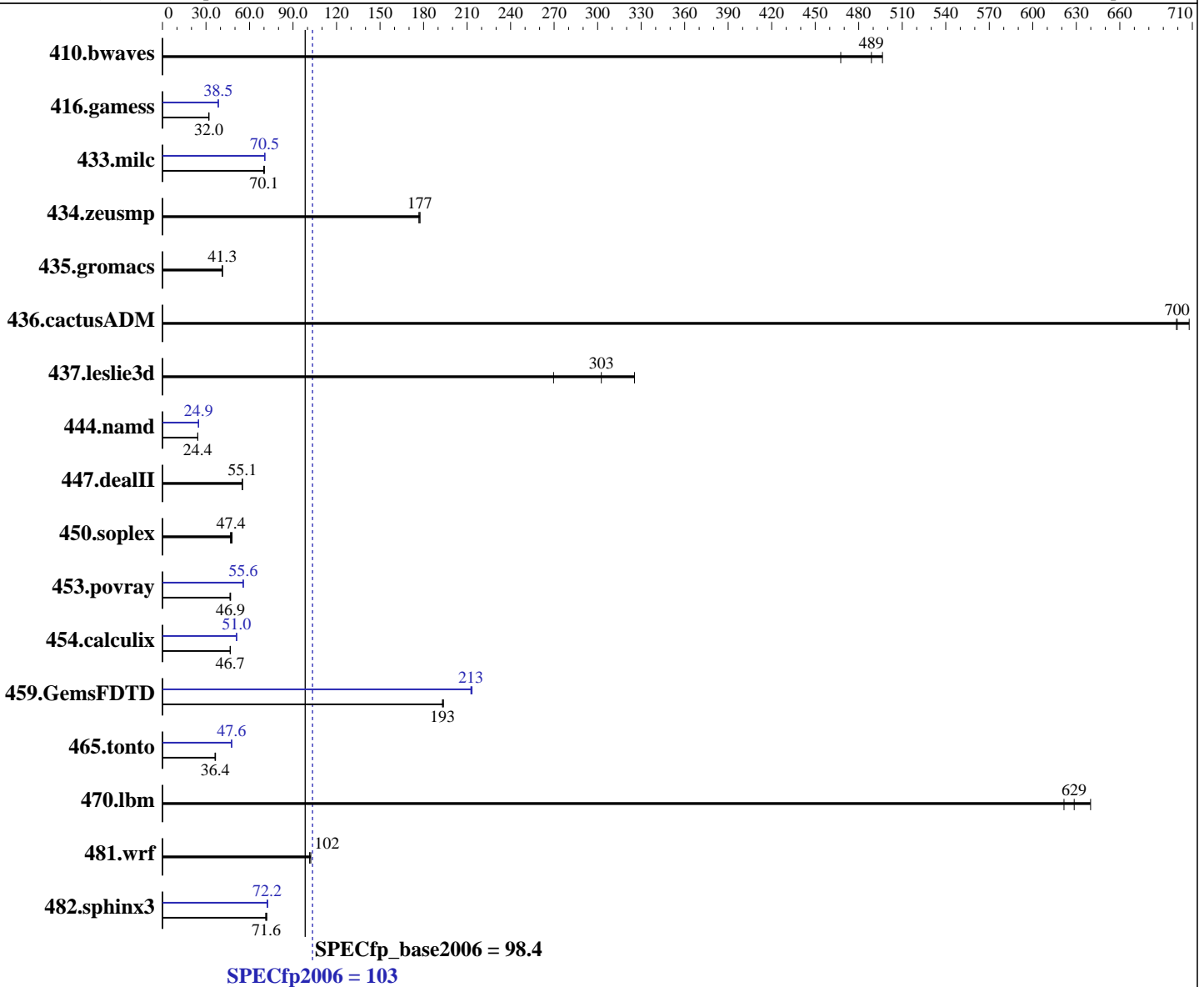
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2697 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2700
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.18.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF, Intel Xeon E5-2697 v2)

SPECfp2006 = **103**

SPECfp_base2006 = **98.4**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 1000 GB SATA III, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	29.1	468	27.4	496	<u>27.8</u>	<u>489</u>	29.1	468	27.4	496	<u>27.8</u>	<u>489</u>
416.gamess	613	32.0	611	32.0	<u>613</u>	<u>32.0</u>	509	38.4	508	38.5	<u>509</u>	<u>38.5</u>
433.milc	131	69.8	<u>131</u>	<u>70.1</u>	131	70.3	130	70.6	130	70.5	<u>130</u>	<u>70.5</u>
434.zeusmp	<u>51.3</u>	<u>177</u>	51.5	177	51.3	177	<u>51.3</u>	<u>177</u>	51.5	177	51.3	177
435.gromacs	<u>173</u>	<u>41.3</u>	173	41.3	173	41.2	<u>173</u>	<u>41.3</u>	173	41.3	173	41.2
436.cactusADM	16.9	708	<u>17.1</u>	<u>700</u>	17.1	699	16.9	708	<u>17.1</u>	<u>700</u>	17.1	699
437.leslie3d	<u>31.1</u>	<u>303</u>	34.9	270	28.9	325	<u>31.1</u>	<u>303</u>	34.9	270	28.9	325
444.namd	329	24.4	<u>329</u>	<u>24.4</u>	329	24.4	323	24.8	<u>323</u>	<u>24.9</u>	323	24.9
447.dealII	208	55.0	<u>208</u>	<u>55.1</u>	208	55.1	208	55.0	<u>208</u>	<u>55.1</u>	208	55.1
450.soplex	174	48.0	178	46.9	<u>176</u>	<u>47.4</u>	174	48.0	178	46.9	<u>176</u>	<u>47.4</u>
453.povray	113	47.0	<u>113</u>	<u>46.9</u>	114	46.5	95.8	55.5	95.3	55.8	<u>95.6</u>	<u>55.6</u>
454.calculix	<u>177</u>	<u>46.7</u>	176	46.8	177	46.7	162	51.0	<u>162</u>	<u>51.0</u>	161	51.2
459.GemsFDTD	55.0	193	<u>54.9</u>	<u>193</u>	54.8	194	49.7	213	49.9	213	<u>49.9</u>	<u>213</u>
465.tonto	269	36.6	271	36.3	<u>271</u>	<u>36.4</u>	<u>207</u>	<u>47.6</u>	207	47.6	206	47.7
470.lbm	<u>21.9</u>	<u>629</u>	21.5	640	22.1	622	<u>21.9</u>	<u>629</u>	21.5	640	22.1	622
481.wrf	110	102	<u>110</u>	<u>102</u>	109	102	110	102	<u>110</u>	<u>102</u>	109	102
482.sphinx3	<u>272</u>	<u>71.6</u>	271	71.9	274	71.1	<u>270</u>	<u>72.2</u>	270	72.1	269	72.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,0,1"

LD_LIBRARY_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 2



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF, Intel Xeon E5-2697 v2)

SPECfp2006 = 103

SPECfp_base2006 = 98.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Oct-2013
Hardware Availability: Oct-2013
Software Availability: Sep-2013

General Notes (Continued)

numactl --interleave=all runspec <etc>
disabled hyper-threading in OS using CPU hotplugging

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF, Intel Xeon E5-2697 v2)

SPECfp2006 = 103

SPECfp_base2006 = 98.4

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF, Intel Xeon E5-2697 v2)

SPECfp2006 = 103

SPECfp_base2006 = 98.4

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro SuperServer 6017R-72RFTP
(X9DRW-7TPF , Intel Xeon E5-2697 v2)

SPECfp2006 = 103

SPECfp_base2006 = 98.4

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 17:20:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 December 2013.