



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 46.4

ProLiant BL460c Gen8
(2.40 GHz, Intel Xeon E5-2630L v2)

SPECint_base2006 = 43.6

CPU2006 license: 3

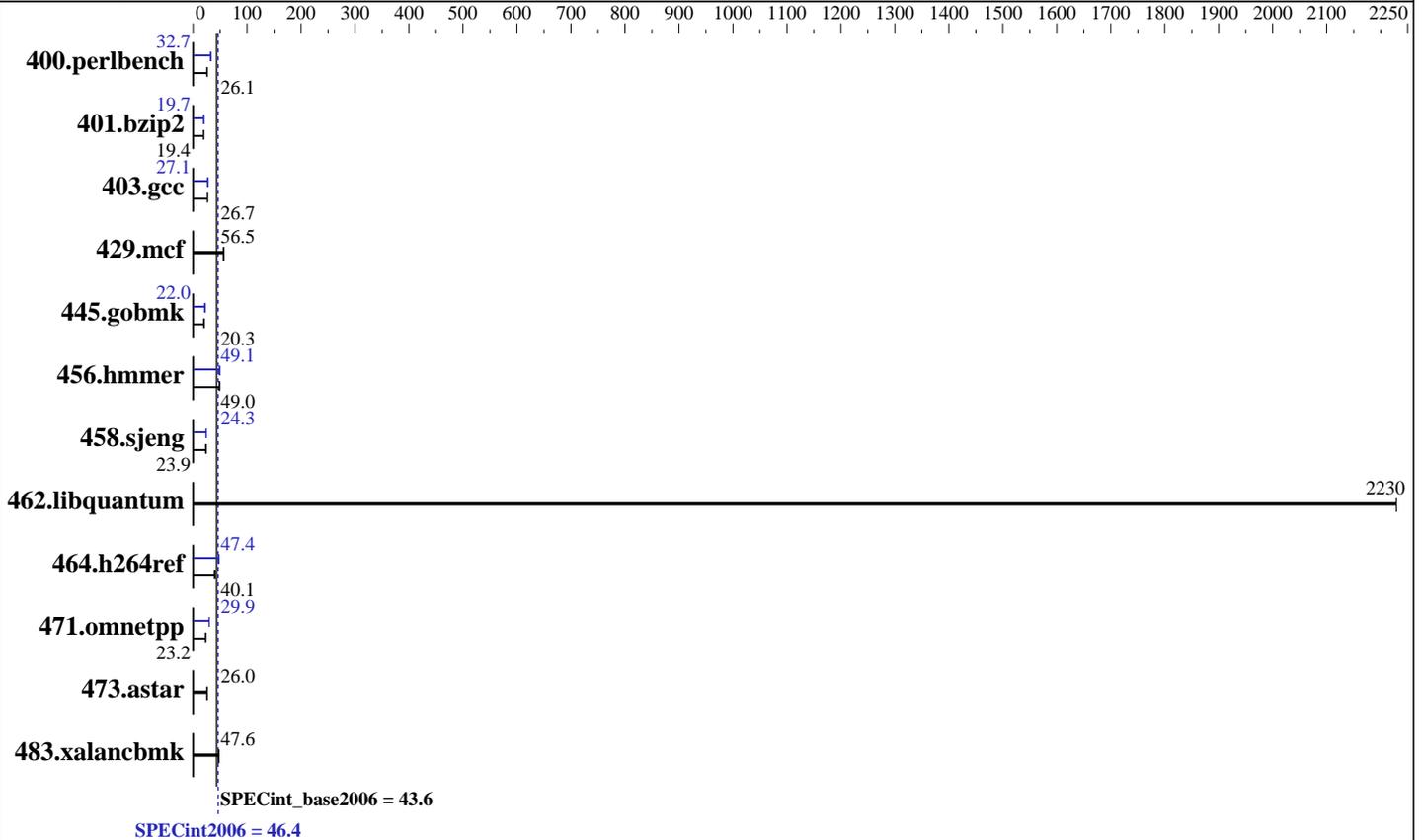
Test date: Oct-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2013

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2630L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 146 GB 15 K SAS, RAID 1
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
 Kernel 3.0.76-0.11-default
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	374	26.1	<u>375</u>	<u>26.1</u>	377	25.9	299	32.6	299	32.7	<u>299</u>	<u>32.7</u>
401.bzip2	496	19.5	496	19.4	<u>496</u>	<u>19.4</u>	491	19.7	<u>491</u>	<u>19.7</u>	491	19.7
403.gcc	302	26.7	<u>302</u>	<u>26.7</u>	302	26.6	<u>298</u>	<u>27.1</u>	297	27.1	298	27.0
429.mcf	161	56.6	<u>161</u>	<u>56.5</u>	162	56.5	161	56.6	<u>161</u>	<u>56.5</u>	162	56.5
445.gobmk	517	20.3	<u>516</u>	<u>20.3</u>	516	20.3	<u>476</u>	<u>22.0</u>	476	22.0	476	22.0
456.hammer	190	49.1	<u>190</u>	<u>49.0</u>	193	48.2	<u>190</u>	<u>49.1</u>	190	49.1	191	49.0
458.sjeng	507	23.9	<u>506</u>	<u>23.9</u>	506	23.9	498	24.3	497	24.4	<u>498</u>	<u>24.3</u>
462.libquantum	9.30	2230	9.30	2230	<u>9.30</u>	<u>2230</u>	9.30	2230	9.30	2230	<u>9.30</u>	<u>2230</u>
464.h264ref	553	40.0	552	40.1	<u>552</u>	<u>40.1</u>	<u>467</u>	<u>47.4</u>	467	47.4	468	47.3
471.omnetpp	269	23.2	<u>269</u>	<u>23.2</u>	269	23.2	209	30.0	<u>209</u>	<u>29.9</u>	209	29.9
473.astar	<u>270</u>	<u>26.0</u>	270	26.0	270	26.0	<u>270</u>	<u>26.0</u>	270	26.0	270	26.0
483.xalancbmk	<u>145</u>	<u>47.6</u>	145	47.6	148	46.7	<u>145</u>	<u>47.6</u>	145	47.6	148	46.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Reclaim mode enabled with:
  echo 1 > /proc/sys/vm/zone_reclaim_mode
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
  echo 1 > /proc/sys/vm/drop_caches
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
Intel Hyperthreading Options set to Disabled
HP Power Profile set to Maximum Performance
Minimum Processor Idle Power Core State set to C1E
Minimum Processor Idle Power Package State set to C6 (non-retention)
Energy/Performance Bias is set to Maximum Performance
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x
```

```
Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on linux-bl460cgen8-bay11-speed Wed Oct 9 22:50:59 2013
```

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630L v2 @ 2.40GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
 cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      132130196 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux-bl460cgen8-bay11-speed 3.0.76-0.11-default #1 SMP Fri Jun 14
08:21:43 UTC 2013 (ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 9 22:44 last=S
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext3  127G  9.2G  112G   8% /
```

```
Additional information from dmidecode:
BIOS HP I31 09/08/2013
Memory:
16x HP 689911-071 8 GB 1600 MHz
```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>



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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 19:18:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 November 2013.