



# SPEC® CFP2006 Result

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**NEC Corporation**

**SPECfp®2006 = 103**

Express5800/R120e-2M (Intel Xeon E5-2697 v2)

**SPECfp\_base2006 = 97.7**

**CPU2006 license:** 9006

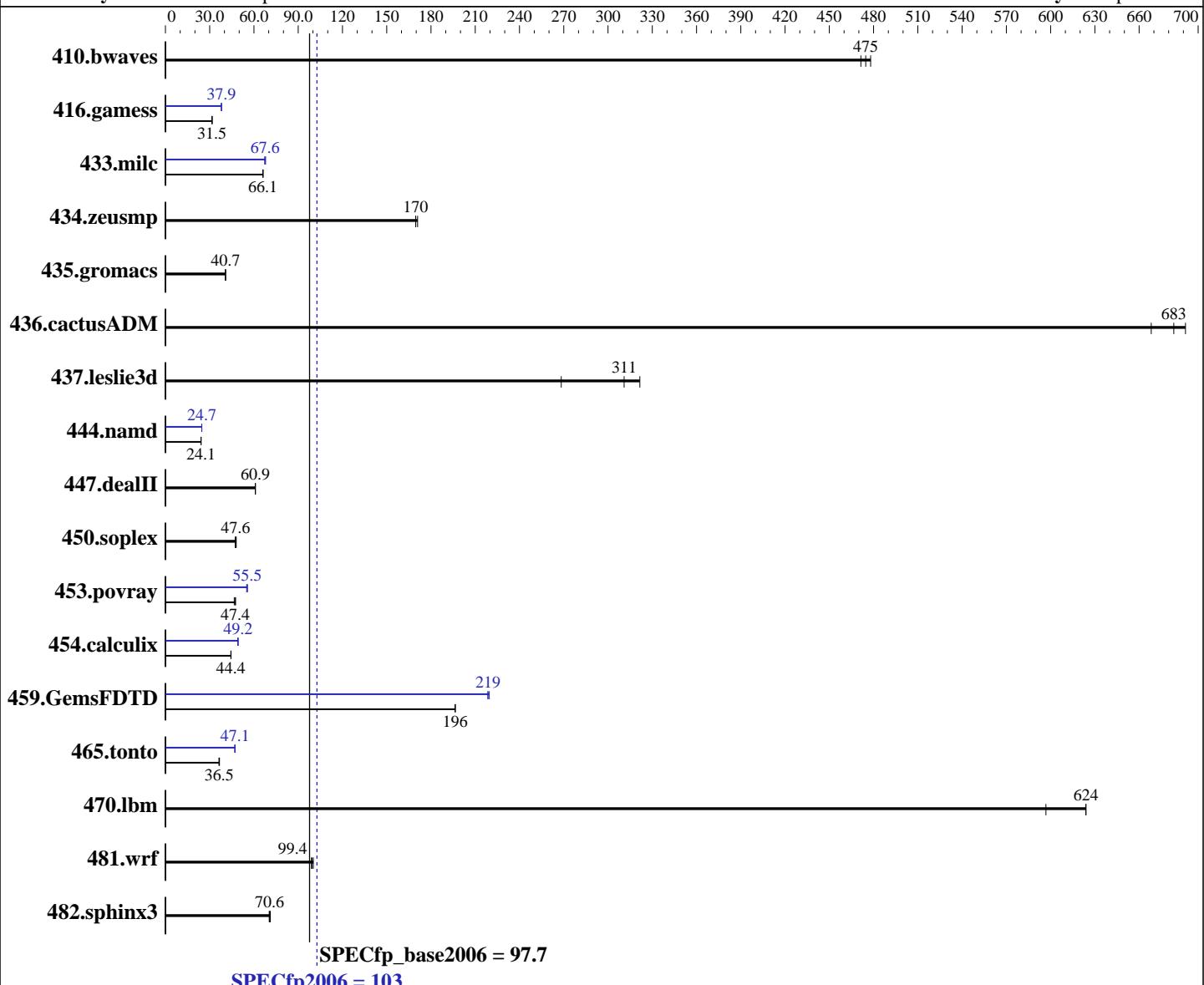
**Test date:** Oct-2013

**Test sponsor:** NEC Corporation

**Hardware Availability:** Sep-2013

**Tested by:** NEC Corporation

**Software Availability:** Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2697 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2700  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Compiler: Kernel 2.6.32-358.18.1.el6.x86\_64  
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.8	471	28.4	478	<b>28.6</b>	<b>475</b>	28.8	471	28.4	478	<b>28.6</b>	<b>475</b>
416.gamess	<b>621</b>	<b>31.5</b>	621	31.5	617	31.7	<b>516</b>	<b>37.9</b>	516	37.9	516	38.0
433.milc	139	66.2	<b>139</b>	<b>66.1</b>	139	66.0	<b>136</b>	67.3	135	67.9	<b>136</b>	<b>67.6</b>
434.zeusmp	<b>53.6</b>	<b>170</b>	53.2	171	53.6	170	<b>53.6</b>	<b>170</b>	53.2	171	53.6	170
435.gromacs	<b>175</b>	<b>40.7</b>	175	40.8	176	40.6	<b>175</b>	<b>40.7</b>	175	40.8	176	40.6
436.cactusADM	17.9	668	17.3	691	<b>17.5</b>	<b>683</b>	17.9	668	17.3	691	<b>17.5</b>	<b>683</b>
437.leslie3d	29.2	322	35.0	268	<b>30.2</b>	<b>311</b>	29.2	322	35.0	268	<b>30.2</b>	<b>311</b>
444.namd	332	24.2	333	24.1	<b>332</b>	<b>24.1</b>	325	24.7	<b>325</b>	<b>24.7</b>	326	24.6
447.dealII	188	61.0	<b>188</b>	<b>60.9</b>	188	60.9	188	61.0	<b>188</b>	<b>60.9</b>	188	60.9
450.soplex	174	48.0	<b>175</b>	<b>47.6</b>	176	47.3	174	48.0	<b>175</b>	<b>47.6</b>	176	47.3
453.povray	<b>112</b>	<b>47.4</b>	112	47.5	114	46.6	<b>95.9</b>	<b>55.5</b>	96.6	55.1	95.8	55.5
454.calculix	185	44.5	<b>186</b>	<b>44.4</b>	186	44.3	167	49.3	<b>168</b>	<b>49.2</b>	168	49.0
459.GemsFDTD	<b>54.0</b>	<b>196</b>	54.0	196	54.0	196	<b>48.5</b>	219	48.3	220	<b>48.5</b>	<b>219</b>
465.tonto	271	36.3	269	36.6	<b>270</b>	<b>36.5</b>	209	47.2	<b>209</b>	<b>47.1</b>	209	47.1
470.lbm	22.0	624	23.0	597	<b>22.0</b>	<b>624</b>	22.0	624	23.0	597	<b>22.0</b>	<b>624</b>
481.wrf	113	99.0	111	100	<b>112</b>	<b>99.4</b>	113	99.0	111	100	<b>112</b>	<b>99.4</b>
482.sphinx3	<b>276</b>	<b>70.6</b>	274	71.1	277	70.3	<b>276</b>	<b>70.6</b>	274	71.1	277	70.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Energy Performance: Performance

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "24"

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## General Notes (Continued)

Added glibc-static-2.12-1.107.el6.x86\_64.rpm  
to enable static linking

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
The Express5800/R120e-1M and  
the Express5800/R120e-2M models are electronically equivalent.  
The results have been measured on the Express5800/R120e-1M model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>



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