SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

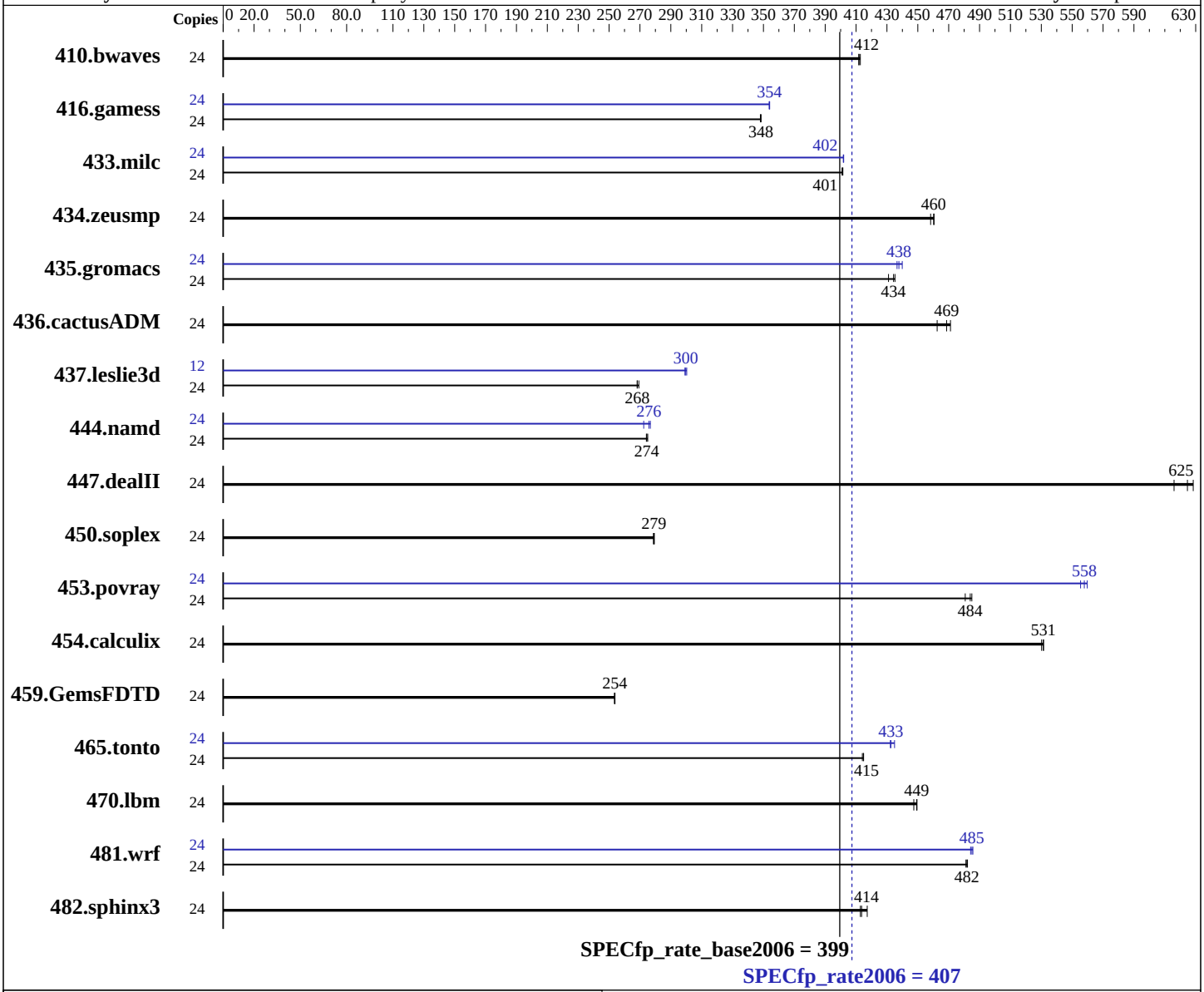
ProLiant BL460c Gen8
(2.40 GHz, Intel Xeon E5-2630L v2)

SPECfp®_rate2006 = 407

SPECfp_rate_base2006 = 399

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E5-2630L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) Kernel version 2.6.32-358.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant BL460c Gen8
(2.40 GHz, Intel Xeon E5-2630L v2)

SPECfp_rate2006 = 407

SPECfp_rate_base2006 = 399

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013

L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	792	412	**792**	**412**	790	413	24	792	412	**792**	**412**	790	413
416.gamess	24	1349	348	**1349**	**348**	1350	348	24	**1328**	**354**	1327	354	1329	354
433.milc	24	549	401	550	401	**549**	**401**	24	**548**	**402**	548	402	548	402
434.zeusmp	24	477	458	474	461	**475**	**460**	24	477	458	474	461	**475**	**460**
435.gromacs	24	398	431	394	435	**395**	**434**	24	**391**	**438**	393	437	390	440
436.cactusADM	24	**612**	**469**	609	471	620	463	24	**612**	**469**	609	471	620	463
437.leslie3d	24	841	268	838	269	**841**	**268**	12	377	299	376	300	**376**	**300**
444.namd	24	702	274	700	275	**701**	**274**	24	696	277	**698**	**276**	707	272
447.dealII	24	**440**	**625**	446	616	437	628	24	**440**	**625**	446	616	437	628
450.soplex	24	718	279	717	279	**717**	**279**	24	718	279	717	279	**717**	**279**
453.povray	24	266	481	263	485	**264**	**484**	24	**229**	**558**	228	560	230	555
454.calculix	24	373	530	**373**	**531**	373	531	24	373	530	**373**	**531**	373	531
459.GemsFDTD	24	**1003**	**254**	1003	254	1005	253	24	**1003**	**254**	1003	254	1005	253
465.tonto	24	569	415	**569**	**415**	570	414	24	543	435	546	432	**546**	**433**
470.lbm	24	734	449	**734**	**449**	737	447	24	734	449	**734**	**449**	737	447
481.wrf	24	556	482	557	481	**556**	**482**	24	554	484	**553**	**485**	552	486
482.sphinx3	24	**1131**	**414**	1121	417	1133	413	24	**1131**	**414**	1121	417	1133	413

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
Disabled unused Linux services through "stop_services.sh" before running.
```

Platform Notes

```
BIOS Configuration:
 HP Power Profile set to Maximum Performance
 Memory Power Savings Mode set to Maximum Performance
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Thermal Configuration set to Maximum Cooling
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on BL460cGen8-RFP-BAO Sat Oct  5 03:23:38 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630L v2 @ 2.40GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       132119288 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux BL460cGen8-RFP-BAO 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST
   2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 4 14:57

SPEC is set to: /cpu2006
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda3      ext4    360G   30G  312G    9% /
```

Continued on next page

Platform Notes (Continued)

```
 Additional information from dmidecode:
   BIOS HP I31 09/08/2013
   Memory:
    16x HP 689911-071 8 GB 1600 MHz 2 rank

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

454.calculix: `basepeak = yes`

481.wrf: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 18:57:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 October 2013.