



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 95.4

PowerEdge T620 (Intel Xeon E5-2650 v2, 2.60 GHz)

SPECfp\_base2006 = 90.9

CPU2006 license: 55

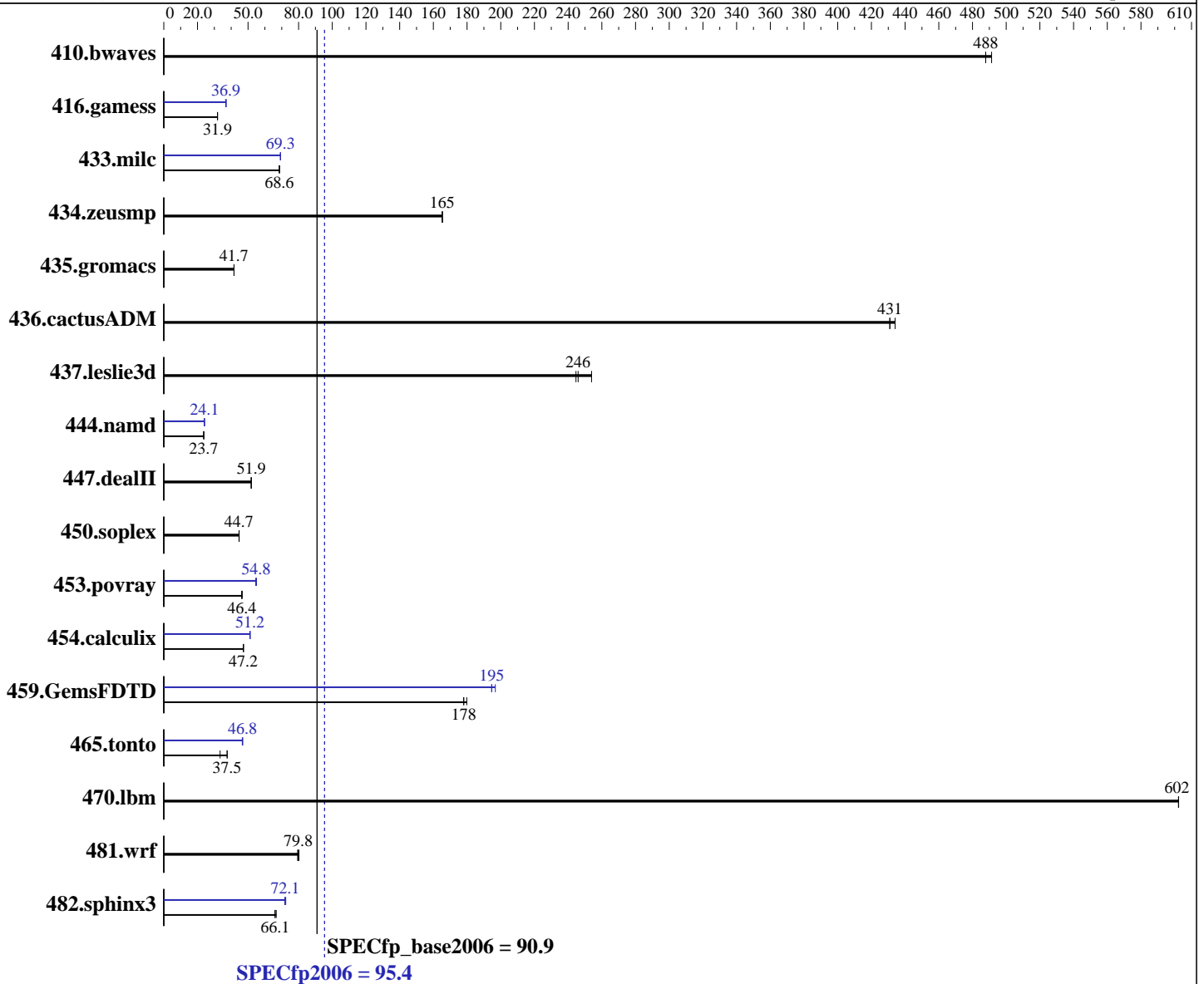
Test date: Aug-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



**Hardware**

CPU Name: Intel Xeon E5-2650 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86\_64) 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>27.9</u>	<u>488</u>	27.9	488	27.7	491	<u>27.9</u>	<u>488</u>	27.9	488	27.7	491
416.gamess	614	31.9	615	31.8	<u>614</u>	<u>31.9</u>	529	37.0	<u>531</u>	<u>36.9</u>	531	36.9
433.milc	<u>134</u>	<u>68.6</u>	134	68.6	134	68.7	133	69.3	133	69.2	<u>133</u>	<u>69.3</u>
434.zeusmp	55.0	165	55.0	165	<u>55.0</u>	<u>165</u>	55.0	165	55.0	165	<u>55.0</u>	<u>165</u>
435.gromacs	171	41.7	<u>171</u>	<u>41.7</u>	171	41.7	171	41.7	<u>171</u>	<u>41.7</u>	171	41.7
436.cactusADM	27.7	431	27.5	434	<u>27.7</u>	<u>431</u>	27.7	431	27.5	434	<u>27.7</u>	<u>431</u>
437.leslie3d	<u>38.2</u>	<u>246</u>	37.0	254	38.4	245	<u>38.2</u>	<u>246</u>	37.0	254	38.4	245
444.namd	338	23.7	<u>338</u>	<u>23.7</u>	338	23.7	333	24.1	<u>333</u>	<u>24.1</u>	332	24.1
447.dealII	220	51.9	<u>220</u>	<u>51.9</u>	220	51.9	220	51.9	<u>220</u>	<u>51.9</u>	220	51.9
450.soplex	186	44.7	<u>187</u>	<u>44.7</u>	187	44.6	186	44.7	<u>187</u>	<u>44.7</u>	187	44.6
453.povray	<u>115</u>	<u>46.4</u>	115	46.2	114	46.5	96.7	55.0	<u>97.1</u>	<u>54.8</u>	97.5	54.6
454.calculix	175	47.2	174	47.3	<u>175</u>	<u>47.2</u>	<u>161</u>	<u>51.2</u>	161	51.2	161	51.2
459.GemsFDTD	<u>59.6</u>	<u>178</u>	59.0	180	59.6	178	53.9	197	<u>54.5</u>	<u>195</u>	54.5	195
465.tonto	261	37.7	294	33.4	<u>262</u>	<u>37.5</u>	211	46.6	<u>210</u>	<u>46.8</u>	210	46.8
470.lbm	<u>22.8</u>	<u>602</u>	22.8	602	22.8	602	<u>22.8</u>	<u>602</u>	22.8	602	22.8	602
481.wrf	141	79.4	<u>140</u>	<u>79.8</u>	139	80.2	141	79.4	<u>140</u>	<u>79.8</u>	139	80.2
482.sphinx3	292	66.7	<u>295</u>	<u>66.1</u>	295	66.0	271	71.8	<u>270</u>	<u>72.1</u>	269	72.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Virtualization Technology disabled  
 Execute Disable disabled  
 Logical Processor disabled  
 System Profile set to Performance  
 Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818  
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
 running on linux Fri Aug 23 17:20:05 2013

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz
    2 "physical id"s (chips)
    16 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      264601764 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 23 11:41 last=5

```

SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  267G  15G  251G   6% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.18 08/10/2013
Memory:
2x 00AD00B300AD HMT42GR7MFR4C-RD 16 GB 1866 MHz
8x 00AD04B300AD HMT42GR7AFR4C-RD 16 GB 1866 MHz
6x 00CE00B300CE M393B2G70CB0-CMA 16 GB 1866 MHz

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"
OMP_NUM_THREADS = "16"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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