



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp®\_rate2006 = 259**

PowerEdge R720xd (Intel Xeon E5-2609 v2, 2.50 GHz)

**SPECfp\_rate\_base2006 = 254**

CPU2006 license: 55

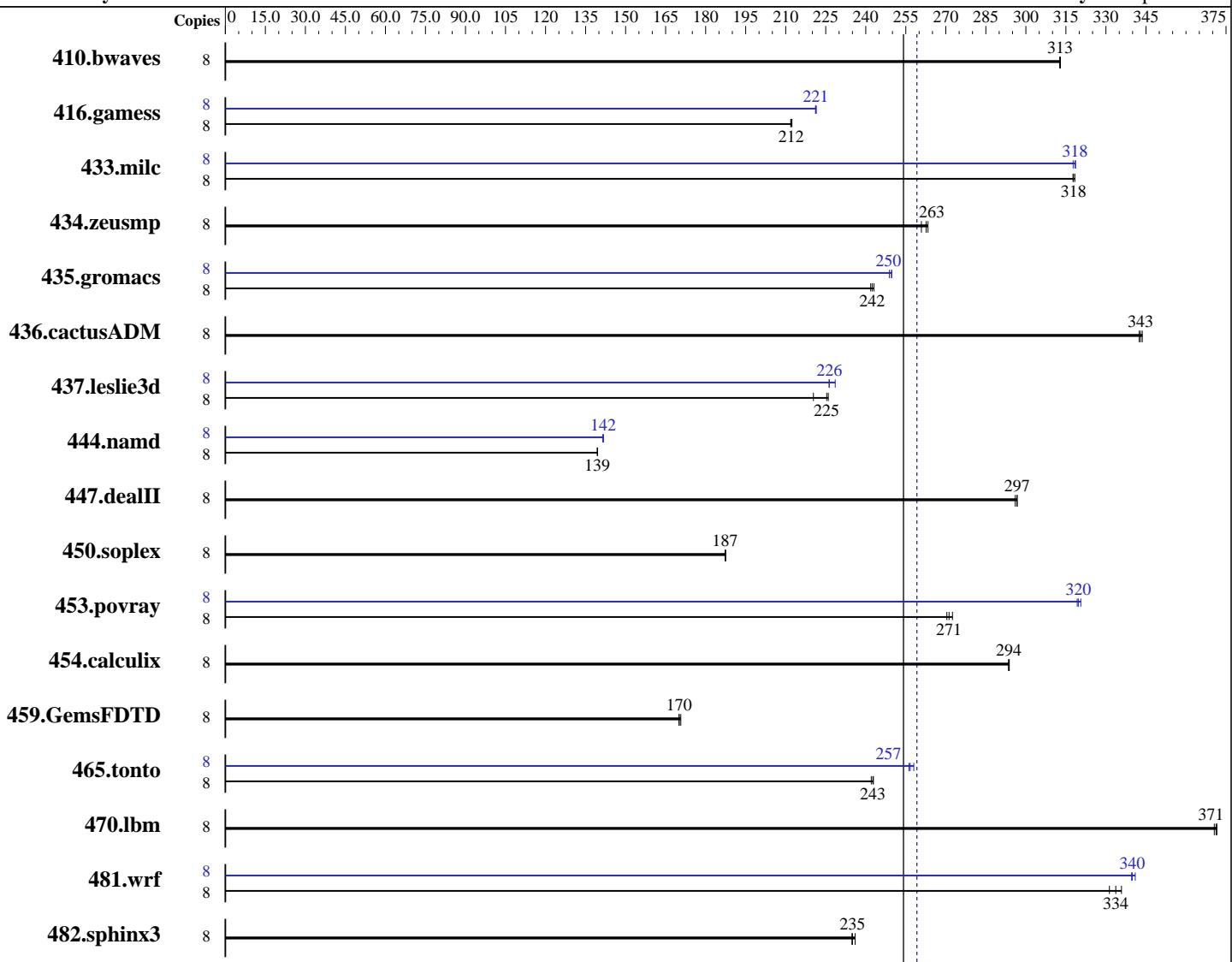
Test date: Aug-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



**SPECfp\_rate\_base2006 = 254**

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## Hardware

CPU Name: Intel Xeon E5-2609 v2  
 CPU Characteristics:  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86\_64)  
 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE  
 for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran  
 Studio XE for Linux  
 Auto Parallel: No  
 File System: ext2  
 System State: Run level 3 (multi-user)

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L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz)
Disk Subsystem:	1 x 1 TB 7200 RPM SATA
Other Hardware:	None

Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<b>348</b>	<b>313</b>	347	313	348	313	8	<b>348</b>	<b>313</b>	347	313	348	313
416.gamess	8	<b>739</b>	<b>212</b>	738	212	739	212	8	<b>707</b>	<b>221</b>	708	221	<b>708</b>	<b>221</b>
433.milc	8	231	318	231	318	<b>231</b>	<b>318</b>	8	<b>231</b>	<b>318</b>	231	318	230	319
434.zeusmp	8	<b>277</b>	<b>263</b>	279	261	276	263	8	<b>277</b>	<b>263</b>	279	261	276	263
435.gromacs	8	<b>236</b>	<b>242</b>	235	243	236	242	8	<b>229</b>	<b>250</b>	230	249	229	250
436.cactusADM	8	278	344	<b>279</b>	<b>343</b>	279	342	8	278	344	<b>279</b>	<b>343</b>	279	342
437.leslie3d	8	333	226	<b>334</b>	<b>225</b>	341	220	8	329	229	332	226	<b>332</b>	<b>226</b>
444.namd	8	460	139	460	139	<b>460</b>	<b>139</b>	8	453	142	454	141	<b>453</b>	<b>142</b>
447.dealII	8	<b>308</b>	<b>297</b>	308	297	309	296	8	<b>308</b>	<b>297</b>	308	297	309	296
450.soplex	8	356	188	356	187	<b>356</b>	<b>187</b>	8	356	188	356	187	<b>356</b>	<b>187</b>
453.povray	8	157	270	<b>157</b>	<b>271</b>	156	272	8	133	319	133	321	<b>133</b>	<b>320</b>
454.calculix	8	225	294	225	293	<b>225</b>	<b>294</b>	8	225	294	225	293	<b>225</b>	<b>294</b>
459.GemsFDTD	8	499	170	<b>499</b>	<b>170</b>	497	171	8	499	170	<b>499</b>	<b>170</b>	497	171
465.tonto	8	325	242	<b>324</b>	<b>243</b>	324	243	8	307	256	305	258	<b>307</b>	<b>257</b>
470.lbm	8	<b>296</b>	<b>371</b>	297	371	296	372	8	<b>296</b>	<b>371</b>	297	371	296	372
481.wrf	8	270	331	<b>268</b>	<b>334</b>	266	336	8	<b>263</b>	<b>340</b>	262	341	263	340
482.sphinx3	8	664	235	<b>663</b>	<b>235</b>	661	236	8	664	235	<b>663</b>	<b>235</b>	661	236

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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## Platform Notes

BIOS settings:

Virtualization Technology disabled

Execute Disable disabled

Logical Processor enabled

System Profile set to Performance

Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on linux Fri Aug 30 03:47:11 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 v2 @ 2.50GHz
        2 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 4
        siblings   : 4
        physical 0: cores 0 1 2 3
        physical 1: cores 0 1 2 3
cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      264601772 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 29 19:52 last=S
```

```
SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  817G  18G  799G   3%  /
```

Additional information from dmidecode:

BIOS Dell Inc. 2.0.18 08/10/2013

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## Platform Notes (Continued)

### Memory:

8x 00CE00B300CE M393B2G70DB0-CMA 16 GB 1333 MHz  
8x 00CE04B300CE M393B2G70DB0-CMA 16 GB 1333 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags



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## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -static -auto-ilp32
```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static
```

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
            -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -static -auto-ilp32
```

436.cactusADM: basepeak = yes

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## Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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