SPEC® CFP2006 Result

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Fujitsu

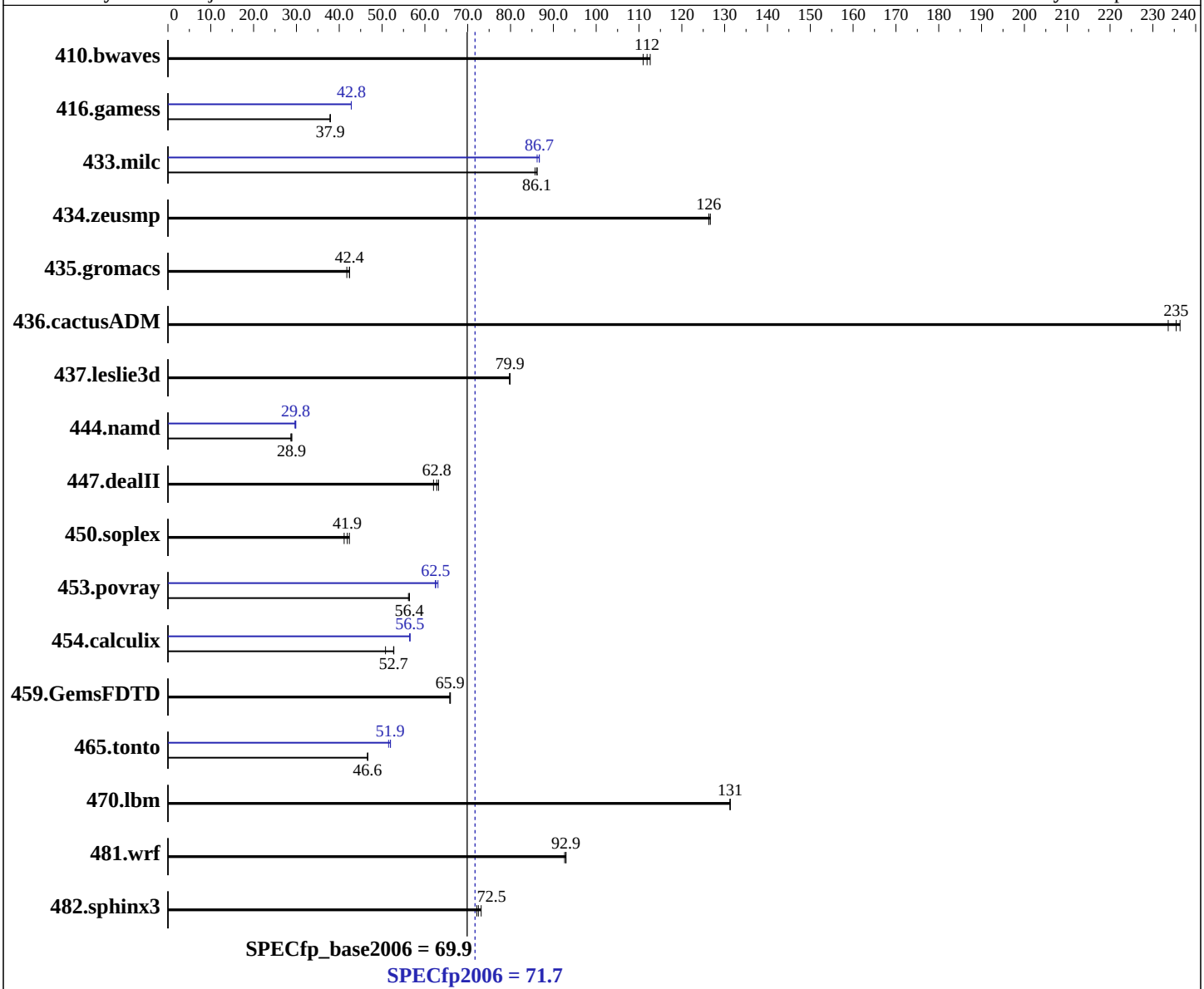
PRIMERGY TX140 S2, Intel Xeon E3-1265L v3, 2.50 GHz

SPECfp®2006 = 71.7

SPECfp_base2006 = 69.9

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013



Hardware

CPU Name:	Intel Xeon E3-1265L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux; Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	122	111	**121**	**112**	121	113	122	111	**121**	**112**	121	113
416.gamess	517	37.9	**517**	**37.9**	516	37.9	457	42.8	457	42.8	**457**	**42.8**
433.milc	**107**	**86.1**	106	86.2	107	85.7	106	86.7	106	86.2	**106**	**86.7**
434.zeusmp	71.8	127	**72.0**	**126**	72.0	126	71.8	127	**72.0**	**126**	72.0	126
435.gromacs	171	41.8	168	42.4	**169**	**42.4**	171	41.8	168	42.4	**169**	**42.4**
436.cactusADM	**50.8**	**235**	50.6	236	51.2	234	**50.8**	**235**	50.6	236	51.2	234
437.leslie3d	118	79.8	118	79.9	**118**	**79.9**	118	79.8	118	79.9	**118**	**79.9**
444.namd	279	28.7	**278**	**28.9**	278	28.9	269	29.8	270	29.7	**269**	**29.8**
447.dealII	184	62.0	**182**	**62.8**	181	63.2	184	62.0	**182**	**62.8**	181	63.2
450.soplex	203	41.1	**199**	**41.9**	197	42.4	203	41.1	**199**	**41.9**	197	42.4
453.povray	94.7	56.2	**94.4**	**56.4**	94.3	56.4	84.4	63.1	85.2	62.4	**85.1**	**62.5**
454.calculix	**157**	**52.7**	156	52.8	162	50.8	146	56.4	146	56.5	**146**	**56.5**
459.GemsFDTD	**161**	**65.9**	161	65.8	161	66.0	**161**	**65.9**	161	65.8	161	66.0
465.tonto	**211**	**46.6**	211	46.7	211	46.6	189	52.0	**189**	**51.9**	191	51.5
470.lbm	105	131	105	131	**105**	**131**	105	131	105	131	**105**	**131**
481.wrf	**120**	**92.9**	121	92.6	120	92.9	**120**	**92.9**	121	92.6	120	92.9
482.sphinx3	266	73.1	270	72.1	**269**	**72.5**	266	73.1	270	72.1	**269**	**72.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
```

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General Notes (Continued)

```
numactl --interleave=all runspec <etc>
For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 18:42:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 October 2013.