



# SPEC® CINT2006 Result

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## Supermicro

SuperServer 5038D-I (X10SLL-F, Intel Xeon E3-1285 v3, 3.60 GHz)

SPECint®\_rate2006 = 218

SPECint\_rate\_base2006 = 210

CPU2006 license: 001176

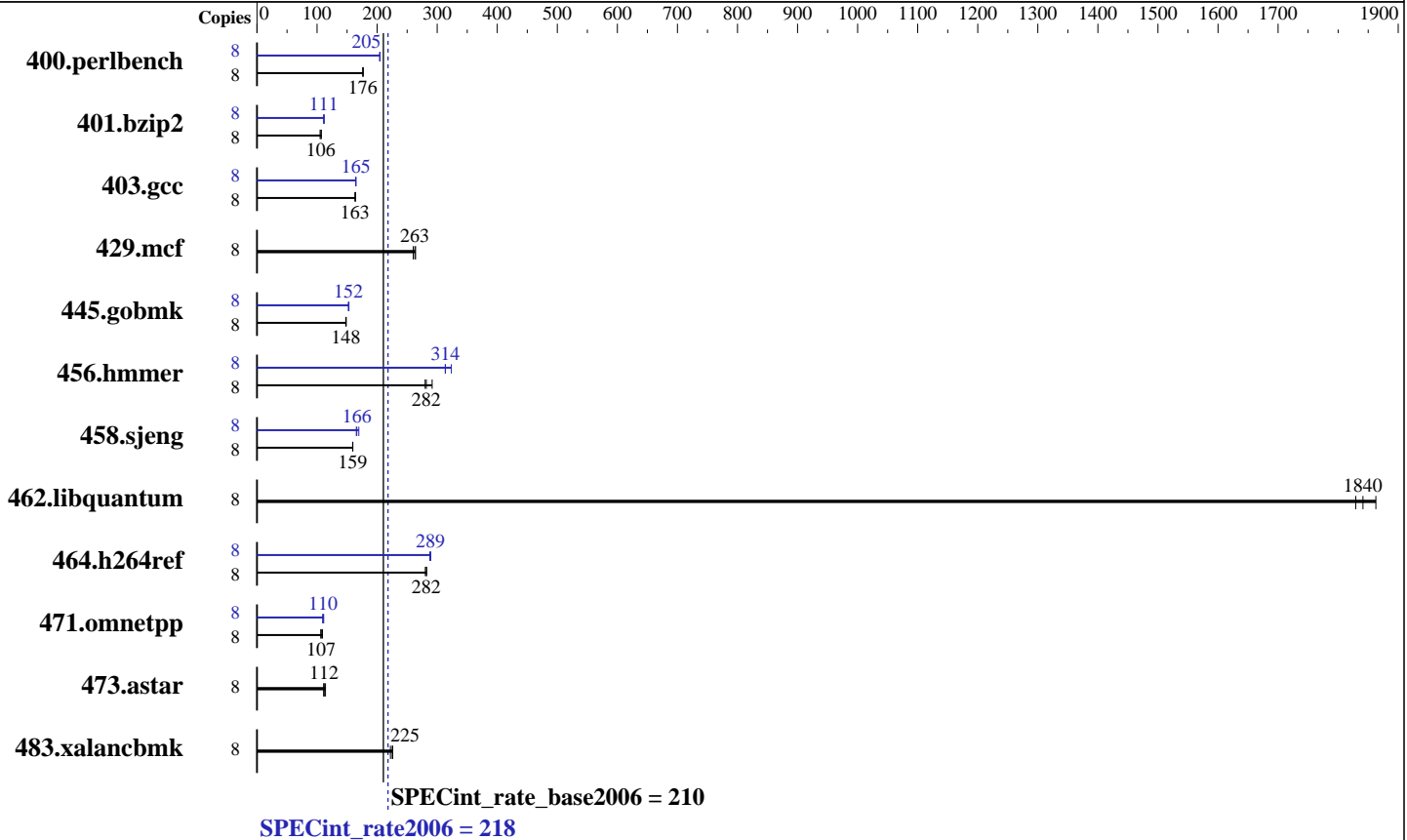
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2013

Hardware Availability: Jun-2013

Software Availability: May-2013



### Hardware

CPU Name: Intel Xeon E3-1285 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (4 x 8 GB 2Rx4 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA I, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	442	177	444	176	<b>443</b>	<b>176</b>	8	381	205	382	204	<b>382</b>	<b>205</b>
401.bzip2	8	<b>725</b>	<b>106</b>	721	107	735	105	8	<b>694</b>	<b>111</b>	695	111	690	112
403.gcc	8	392	164	<b>394</b>	<b>163</b>	394	163	8	391	165	391	165	<b>391</b>	<b>165</b>
429.mcf	8	<b>278</b>	<b>263</b>	276	264	280	260	8	<b>278</b>	<b>263</b>	276	264	280	260
445.gobmk	8	568	148	<b>566</b>	<b>148</b>	565	148	8	551	152	<b>550</b>	<b>152</b>	548	153
456.hammer	8	<b>265</b>	<b>282</b>	256	292	267	280	8	231	324	<b>238</b>	<b>314</b>	238	313
458.sjeng	8	608	159	<b>607</b>	<b>159</b>	606	160	8	585	165	571	170	<b>582</b>	<b>166</b>
462.libquantum	8	90.6	1830	<b>90.0</b>	<b>1840</b>	89.0	1860	8	90.6	1830	<b>90.0</b>	<b>1840</b>	89.0	1860
464.h264ref	8	632	280	<b>628</b>	<b>282</b>	627	282	8	612	289	<b>613</b>	<b>289</b>	615	288
471.omnetpp	8	471	106	<b>467</b>	<b>107</b>	459	109	8	450	111	<b>453</b>	<b>110</b>	458	109
473.astar	8	492	114	506	111	<b>501</b>	<b>112</b>	8	492	114	506	111	<b>501</b>	<b>112</b>
483.xalancbmk	8	244	226	248	222	<b>245</b>	<b>225</b>	8	244	226	248	222	<b>245</b>	<b>225</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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