



SPEC® CFP2006 Result

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Supermicro

SuperServer 5038D-I (X10SLL-F, Intel Xeon E3-1230L v3, 1.80 GHz)

SPECfp®2006 = **58.8**

SPECfp_base2006 = **57.2**

CPU2006 license: 001176

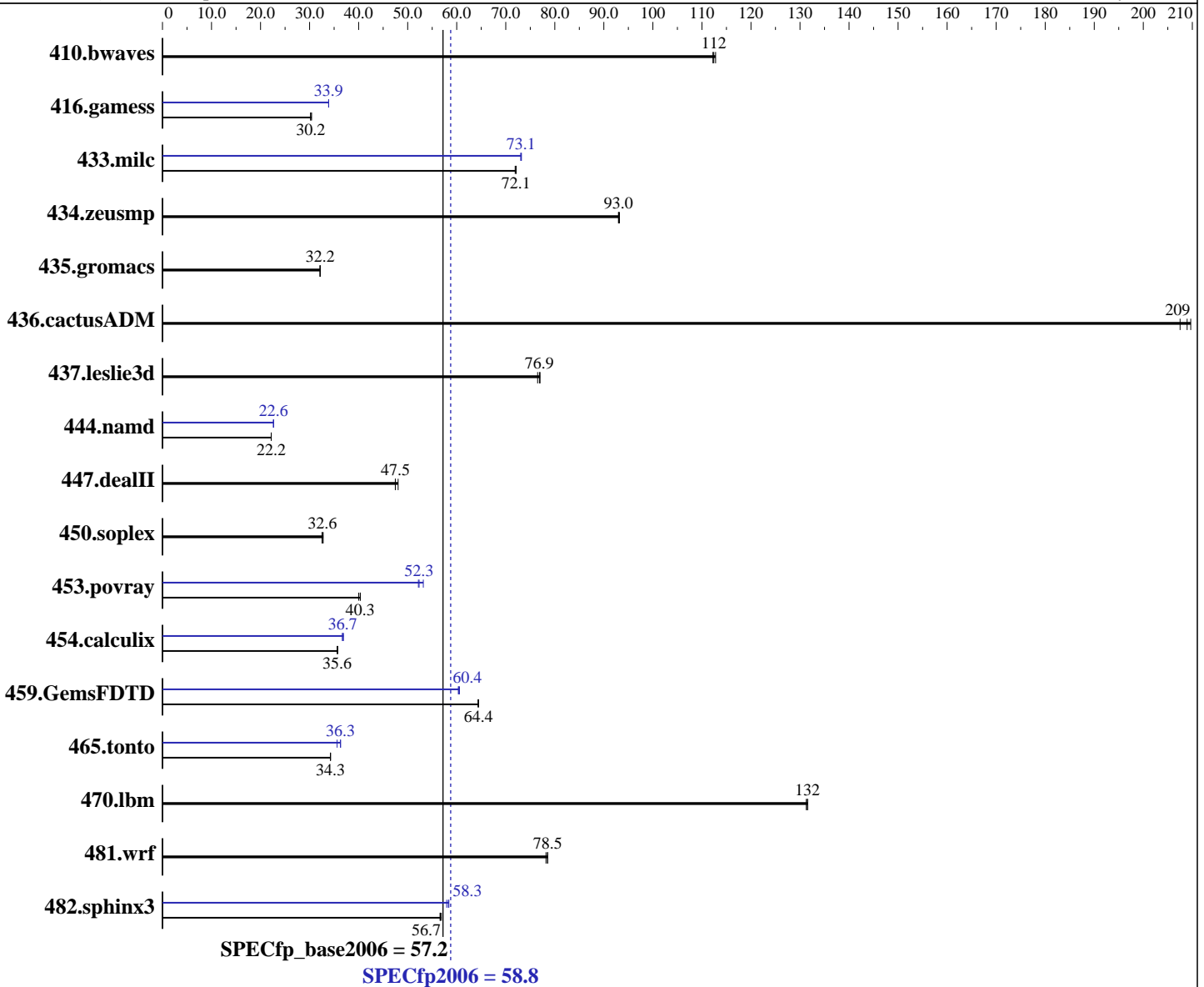
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2013

Hardware Availability: Jun-2013

Software Availability: May-2013



Hardware

CPU Name: Intel Xeon E3-1230L v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4, Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux;
 Fortran: Version 13.1.1.163 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx4 PC3-12800E-11, ECC)
Disk Subsystem: 1 x 250 GB SATA I, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	121	112	120	113	<u>121</u>	<u>112</u>	121	112	120	113	<u>121</u>	<u>112</u>
416.gamess	644	30.4	647	30.2	648	30.2	578	33.9	578	33.9	578	33.9
433.milc	127	72.1	128	71.9	<u>127</u>	<u>72.1</u>	125	73.2	<u>126</u>	<u>73.1</u>	126	73.1
434.zeusmp	97.9	93.0	97.7	93.2	97.8	93.0	97.9	93.0	97.7	93.2	97.8	93.0
435.gromacs	222	32.1	<u>222</u>	<u>32.2</u>	222	32.2	222	32.1	<u>222</u>	<u>32.2</u>	222	32.2
436.cactusADM	57.0	210	57.2	209	57.6	208	57.0	210	57.2	209	57.6	208
437.leslie3d	<u>122</u>	76.9	122	77.0	123	76.5	<u>122</u>	76.9	122	77.0	123	76.5
444.namd	362	22.2	362	22.2	362	22.2	354	22.6	354	22.6	354	22.6
447.dealII	238	48.1	241	47.5	<u>241</u>	<u>47.5</u>	238	48.1	241	47.5	<u>241</u>	<u>47.5</u>
450.soplex	254	32.8	256	32.5	<u>256</u>	<u>32.6</u>	254	32.8	256	32.5	<u>256</u>	<u>32.6</u>
453.povray	<u>132</u>	40.3	133	40.0	132	40.3	102	52.2	100	53.2	<u>102</u>	52.3
454.calculix	<u>232</u>	35.6	232	35.6	231	35.8	<u>225</u>	36.7	224	36.9	225	36.7
459.GemsFDTD	165	64.4	165	64.3	165	64.4	176	60.4	176	60.4	175	60.6
465.tonto	<u>287</u>	34.3	287	34.2	287	34.3	<u>271</u>	36.3	271	36.3	276	35.6
470.lbm	105	131	104	132	104	132	105	131	104	132	104	132
481.wrf	143	78.2	142	78.5	<u>142</u>	<u>78.5</u>	143	78.2	142	78.5	<u>142</u>	<u>78.5</u>
482.sphinx3	344	56.6	344	56.7	343	56.9	334	58.3	336	58.0	334	58.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml>

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