



SPEC[®] CINT2006 Result

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Supermicro

SuperServer 6017TR-TQF (X9DRT-IBQF,
Intel Xeon E5-2630L, 2.00 GHz)

SPECint[®]_rate2006 = 391

SPECint_rate_base2006 = 378

CPU2006 license: 001176

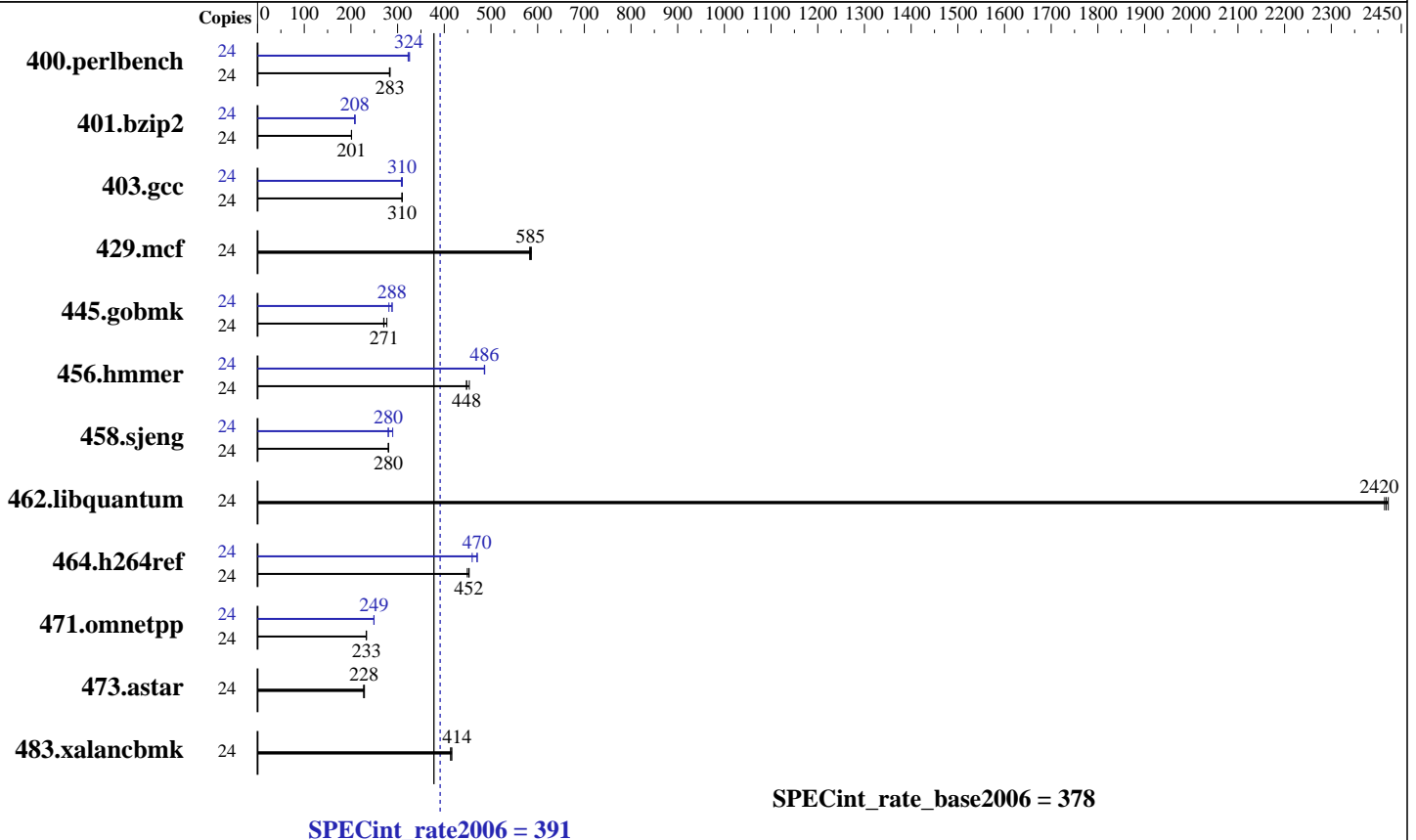
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2013

Hardware Availability: Mar-2012

Software Availability: Oct-2012



Hardware

CPU Name: Intel Xeon E5-2630L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++; Version 13.0.0.133 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	830	282	826	284	<u>827</u>	<u>283</u>	24	720	326	727	322	<u>724</u>	<u>324</u>
401.bzip2	24	1152	201	1150	201	<u>1151</u>	<u>201</u>	24	1109	209	<u>1111</u>	<u>208</u>	1112	208
403.gcc	24	624	310	625	309	623	310	24	623	310	627	308	624	310
429.mcf	24	373	587	376	583	<u>374</u>	<u>585</u>	24	373	587	376	583	<u>374</u>	<u>585</u>
445.gobmk	24	909	277	932	270	<u>930</u>	<u>271</u>	24	895	281	874	288	874	288
456.hammer	24	494	454	<u>500</u>	<u>448</u>	500	448	24	461	486	460	487	<u>460</u>	<u>486</u>
458.sjeng	24	1035	281	1037	280	<u>1036</u>	<u>280</u>	24	1038	280	1003	290	<u>1036</u>	<u>280</u>
462.libquantum	24	206	2410	205	2420	<u>206</u>	<u>2420</u>	24	206	2410	205	2420	<u>206</u>	<u>2420</u>
464.h264ref	24	1182	449	1172	453	<u>1174</u>	<u>452</u>	24	1155	460	<u>1130</u>	<u>470</u>	1128	471
471.omnetpp	24	645	233	<u>643</u>	<u>233</u>	643	233	24	601	250	602	249	<u>601</u>	<u>249</u>
473.astar	24	741	227	736	229	<u>740</u>	<u>228</u>	24	741	227	736	229	<u>740</u>	<u>228</u>
483.xalancbmk	24	398	416	<u>400</u>	<u>414</u>	401	413	24	398	416	<u>400</u>	<u>414</u>	401	413

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
 -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.xml>

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For other inquiries, please contact webmaster@spec.org.

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