SPEC® CINT2006 Result

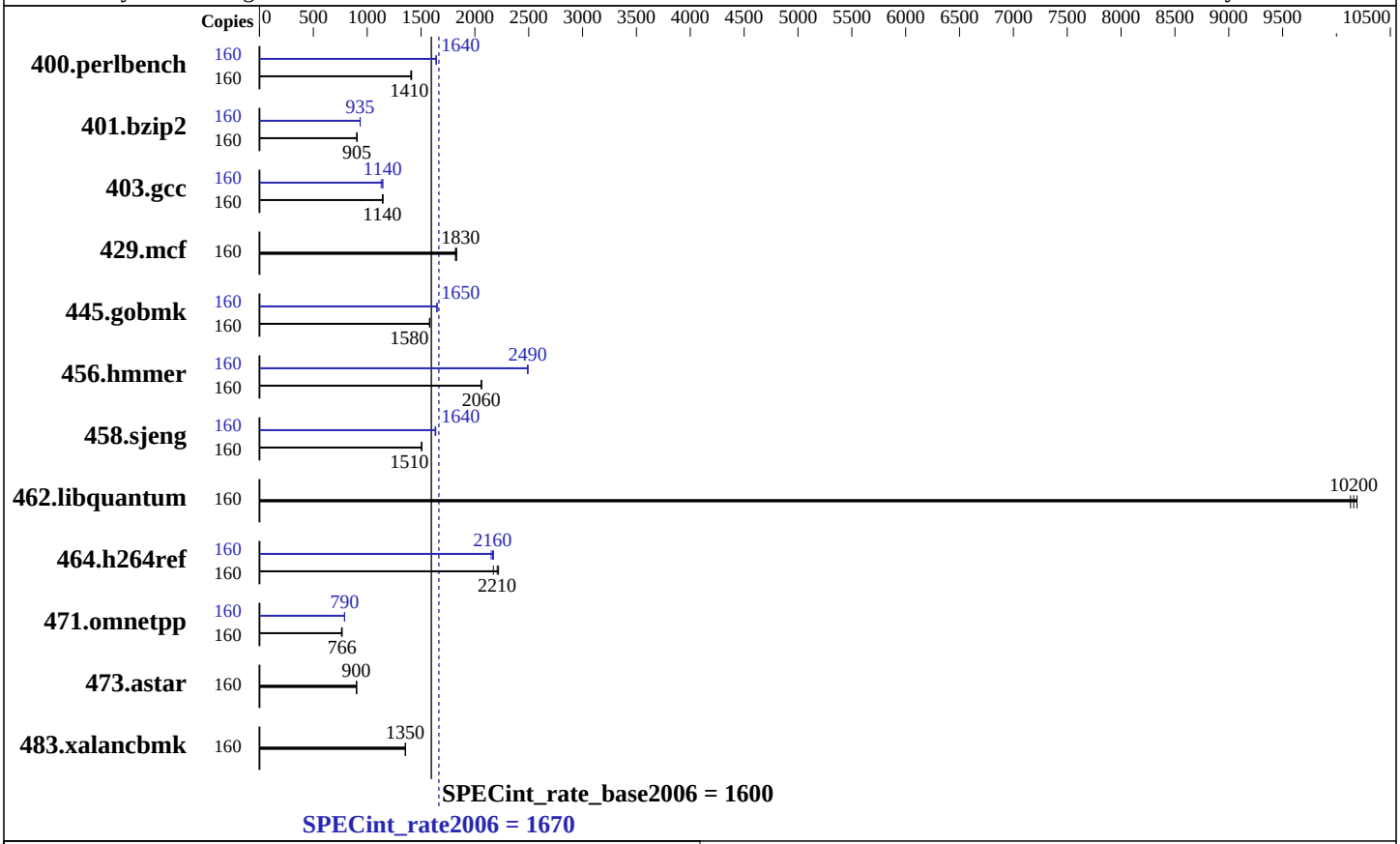
Sugon
I950r-G (Intel Xeon E7-8850)

SPECint®_rate2006 = 1670

SPECint_rate_base2006 = 1600

CPU2006 license: 9046
Test sponsor: Sugon
Tested by: Sugon

Test date: Apr-2013
Hardware Availability: Apr-2011
Software Availability: Jun-2012



Hardware

CPU Name:	Intel Xeon E7-8850
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	80 cores, 8 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4,5,6,7,8 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (64 x 8 GB 2Rx4 PC3-10600R-11, ECC)
Disk Subsystem:	1 x 2 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.3 (Santiago) 2.6.32-279.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	160	1106	1410	**1110**	**1410**	1112	1410	160	951	1640	954	1640	**952**	**1640**
401.bzip2	160	**1705**	**905**	1705	906	1709	903	160	1653	934	1650	936	**1651**	**935**
403.gcc	160	1129	1140	1122	1150	**1128**	**1140**	160	1125	1140	1141	1130	**1126**	**1140**
429.mcf	160	797	1830	802	1820	**799**	**1830**	160	797	1830	802	1820	**799**	**1830**
445.gobmk	160	**1063**	**1580**	1063	1580	1061	1580	160	1019	1650	**1018**	**1650**	1018	1650
456.hmmer	160	**725**	**2060**	724	2060	725	2060	160	600	2490	**599**	**2490**	599	2490
458.sjeng	160	1285	1510	**1286**	**1510**	1286	1510	160	1183	1640	**1184**	**1640**	1188	1630
462.libquantum	160	**326**	**10200**	325	10200	327	10100	160	**326**	**10200**	325	10200	327	10100
464.h264ref	160	1631	2170	**1604**	**2210**	1595	2220	160	1629	2170	1646	2150	**1636**	**2160**
471.omnetpp	160	1313	762	**1306**	**766**	1304	767	160	1266	790	1265	791	**1266**	**790**
473.astar	160	1244	903	1249	899	**1248**	**900**	160	1244	903	1249	899	**1248**	**900**
483.xalancbmk	160	815	1350	814	1360	**815**	**1350**	160	815	1350	814	1360	**815**	**1350**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /home/speccpu/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on speccpu.sugon.com Fri Apr 19 18:10:14 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : I Intel(R) Xeon(R) CPU E7- 8850 @ 2.00GHz
   model name : Intel(R) Xeon(R) CPU E7- 8850 @ 2.00GHz
      8 "physical id"s (chips)
      160 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
```

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Platform Notes (Continued)

```
        siblings  : 20
        physical 0: cores 0 1 2 8 9 16 17 18 24 25
        physical 1: cores 0 1 2 8 9 16 17 18 24 25
        physical 2: cores 0 1 2 8 9 16 17 18 24 25
        physical 3: cores 0 1 2 8 9 16 17 18 24 25
        physical 4: cores 0 1 2 8 9 16 17 18 24 25
        physical 5: cores 0 1 2 8 9 16 17 18 24 25
        physical 6: cores 0 1 2 8 9 16 17 18 24 25
        physical 7: cores 0 1 2 8 9 16 17 18 24 25
     cache size : 24576 KB

  From /proc/meminfo
     MemTotal:       529291760 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux speccpu.sugon.com 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT
     2012 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Apr 19 18:07

  SPEC is set to: /home/speccpu
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_speccpu-lv_home
                    ext4    1.8T  947G  743G  57% /home

  Additional information from dmidecode:
    Memory:
     64x Samsung M393B1G70BH0-YK0 8 GB 1067 MHz 1 rank

  (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/speccpu/libs/32:/home/speccpu/libs/64"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RHEL5.5
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
  Filesystem page cache cleared with:
```

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General Notes (Continued)

```
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
`icc  -m32`

C++ benchmarks:
`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`
`-Wl,-z,muldefs -L/smartheap -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc  -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

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Peak Compiler Invocation (Continued)

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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