SPEC® CFP2006 Result

Supermicro

SuperServer 6047R-E1R36N (X9DRi-LN4F+, Intel E5-2680)

$SPECfp^®\_rate2006 =$ **488**

SPECfp_rate_base2006 = 474
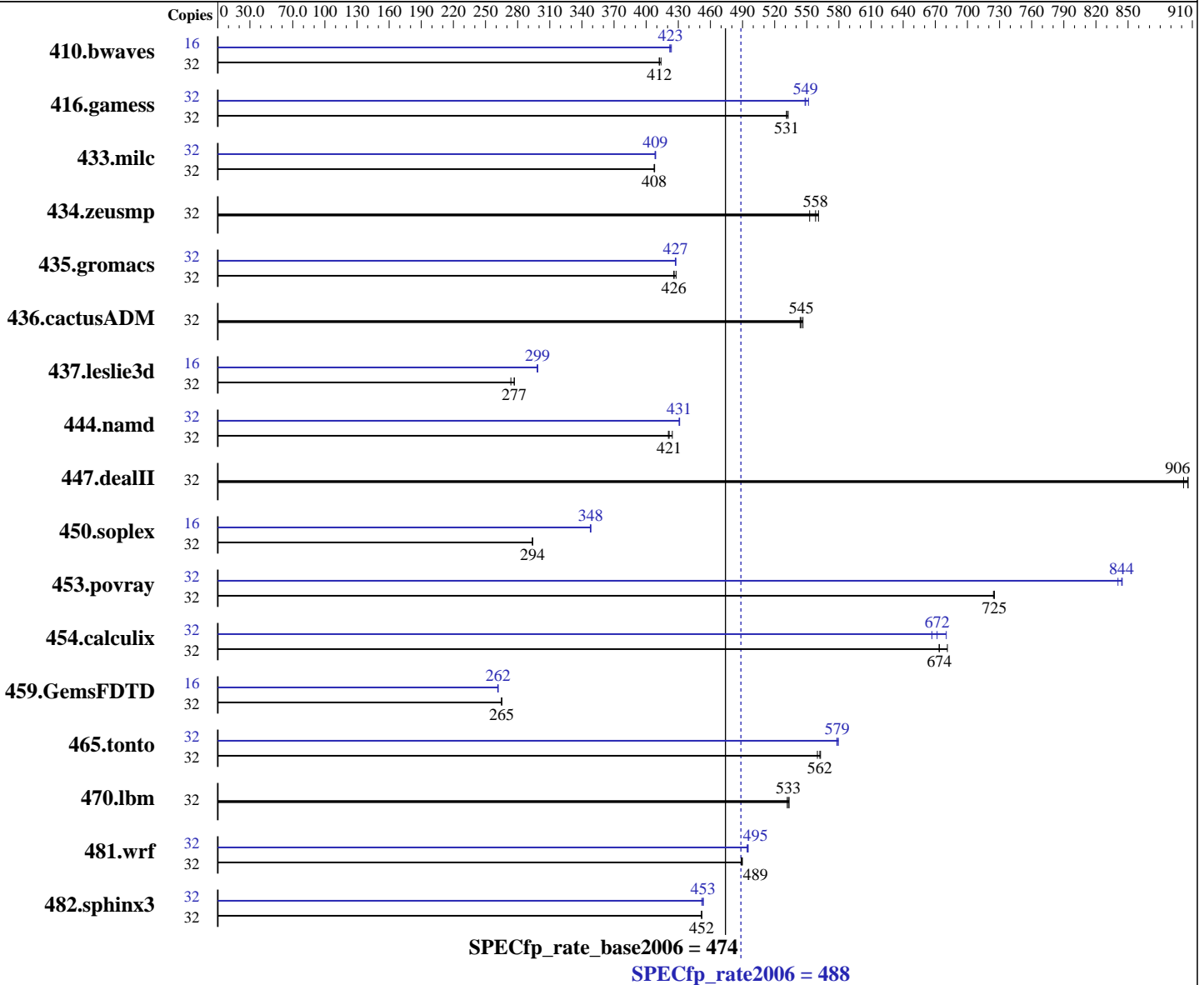
CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2013

Hardware Availability: Feb-2012

Software Availability: Dec-2011

SPECfp_rate_base2006 = 474

SPECfp_rate2006 = 488

Hardware

CPU Name:	Intel Xeon E5-2680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)

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L3 Cache:	20 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)	Other Software:	None
Disk Subsystem:	1 x 1 TB SATA II, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	1051	414	**1055**	**412**	1055	412	16	515	422	513	424	**514**	**423**
416.gamess	32	1180	531	1176	533	**1179**	**531**	32	1143	548	**1142**	**549**	1136	552
433.milc	32	**721**	**408**	720	408	721	408	32	720	408	719	409	**719**	**409**
434.zeusmp	32	519	561	**522**	**558**	527	553	32	519	561	**522**	**558**	527	553
435.gromacs	32	536	426	534	428	**536**	**426**	32	534	428	535	427	**535**	**427**
436.cactusADM	32	**702**	**545**	700	546	703	544	32	**702**	**545**	700	546	703	544
437.leslie3d	32	1099	274	**1087**	**277**	1087	277	16	504	298	503	299	**504**	**299**
444.namd	32	605	424	610	421	**609**	**421**	32	596	431	595	431	**596**	**431**
447.dealII	32	**404**	**906**	406	902	404	906	32	**404**	**906**	406	902	404	906
450.soplex	32	**908**	**294**	908	294	908	294	16	383	348	384	348	**383**	**348**
453.povray	32	235	725	235	724	**235**	**725**	32	**202**	**844**	202	845	203	841
454.calculix	32	**392**	**674**	392	674	388	681	32	388	680	396	667	**393**	**672**
459.GemsFDTD	32	1281	265	**1281**	**265**	1281	265	16	**649**	**262**	648	262	649	261
465.tonto	32	**560**	**562**	559	563	562	560	32	545	578	543	579	**544**	**579**
470.lbm	32	824	534	**825**	**533**	827	532	32	824	534	**825**	**533**	827	532
481.wrf	32	730	490	731	489	**730**	**489**	32	723	494	**722**	**495**	722	495
482.sphinx3	32	**1380**	**452**	1380	452	1380	452	32	**1376**	**453**	1376	453	1380	452

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>        /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc   -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

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Test date: Apr-2013
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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
```

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Test sponsor:	Supermicro	**Hardware Availability:**	Feb-2012
Tested by:	Supermicro	**Software Availability:**	Dec-2011

Peak Portability Flags (Continued)

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
              -opt-mem-layout-trans=3

     470.lbm: basepeak = yes

  482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2
```

C++ benchmarks:

```
   444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
             -auto-ilp32

  447.dealII: basepeak = yes

   450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

   453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
   410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -static

   416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
              -inline-level=0 -scalar-rep- -static

  434.zeusmp: basepeak = yes

  437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

 459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
             -static -auto-ilp32 -opt-mem-layout-trans=3
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
              -opt-mem-layout-trans=3
```

```
481.wrf: Same as 454.calculix
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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