SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Altos R360 F2 (Intel Xeon E5-2690)

$SPECfp^{®}_rate2006 = $ 500

$SPECfp_rate_base2006 = $ 491
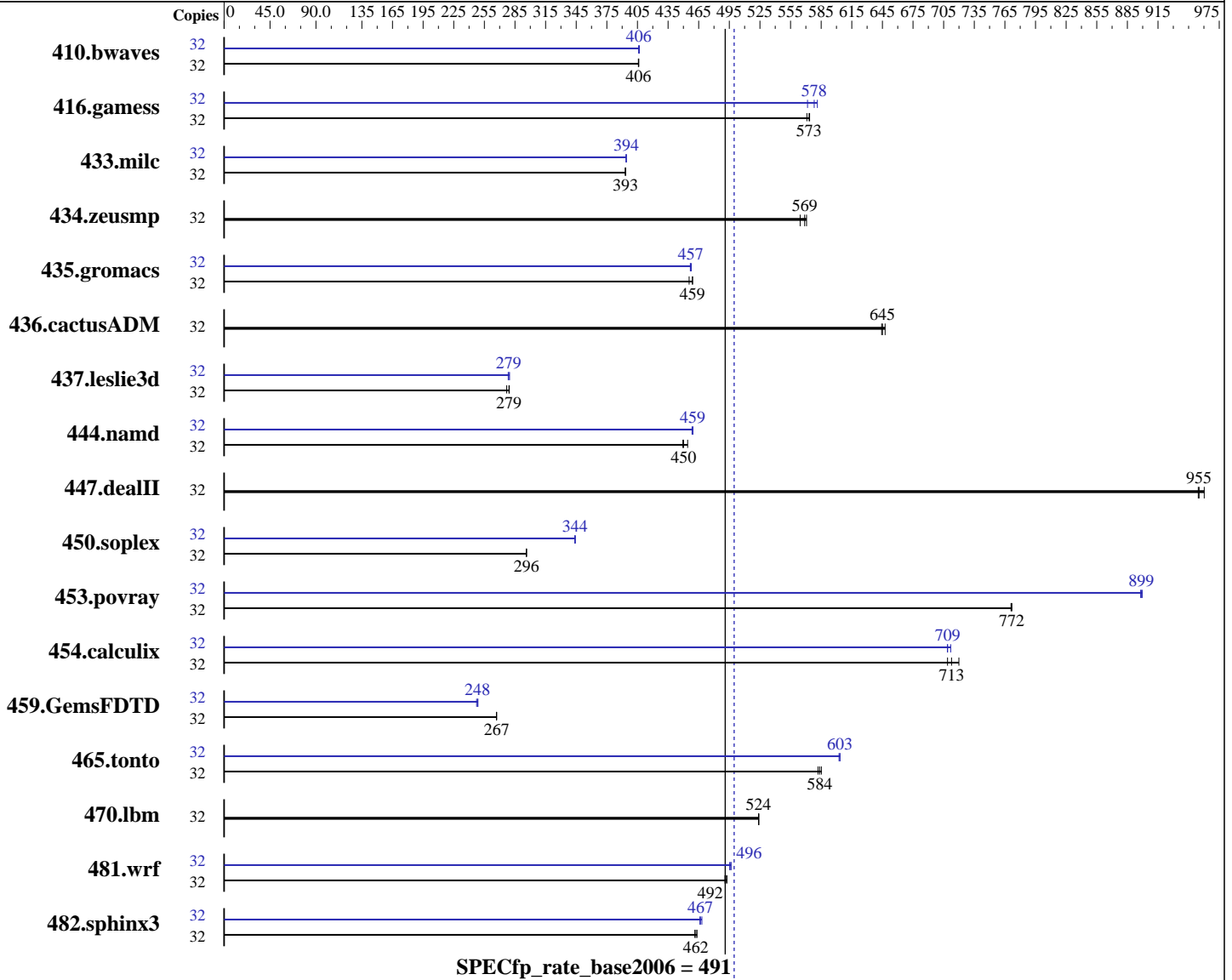
CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2013
Hardware Availability: Apr-2013
Software Availability: Jun-2012

Benchmark	Copies	Result (blue)	Result (black)
410.bwaves	32 / 32	406	406
416.gamess	32 / 32	578	573
433.milc	32 / 32	394	393
434.zeusmp	32		569
435.gromacs	32 / 32	457	459
436.cactusADM	32		645
437.leslie3d	32 / 32	279	279
444.namd	32 / 32	459	450
447.dealII	32		955
450.soplex	32 / 32	344	296
453.povray	32 / 32	899	772
454.calculix	32 / 32	709	713
459.GemsFDTD	32 / 32	248	267
465.tonto	32 / 32	603	584
470.lbm	32		524
481.wrf	32 / 32	496	492
482.sphinx3	32 / 32	467	462

SPECfp_rate_base2006 = 491

SPECfp_rate2006 = 500

Hardware

CPU Name: Intel Xeon E5-2690
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 2900
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago)
2.6.32-279.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 1 TB SATA, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	**_1071_**	**_406_**	1070	406	1071	406	32	**_1070_**	**_406_**	1071	406	1069	407
416.gamess	32	1097	571	**_1093_**	**_573_**	1092	574	32	1096	572	**_1084_**	**_578_**	1078	581
433.milc	32	747	393	747	393	**_747_**	**_393_**	32	745	394	**_746_**	**_394_**	746	394
434.zeusmp	32	516	564	510	571	**_512_**	**_569_**	32	516	564	510	571	**_512_**	**_569_**
435.gromacs	32	498	459	**_498_**	**_459_**	501	456	32	500	457	499	458	**_500_**	**_457_**
436.cactusADM	32	590	648	**_593_**	**_645_**	594	644	32	590	648	**_593_**	**_645_**	594	644
437.leslie3d	32	1086	277	**_1078_**	**_279_**	1076	279	32	**_1078_**	**_279_**	1081	278	1075	280
444.namd	32	**_570_**	**_450_**	571	449	565	454	32	560	458	559	459	**_559_**	**_459_**
447.dealII	32	**_383_**	**_955_**	384	954	381	960	32	**_383_**	**_955_**	384	954	381	960
450.soplex	32	902	296	**_901_**	**_296_**	901	296	32	777	344	**_776_**	**_344_**	775	344
453.povray	32	221	771	221	772	**_221_**	**_772_**	32	**_189_**	**_899_**	189	900	190	898
454.calculix	32	372	709	**_370_**	**_713_**	367	720	32	373	709	371	712	**_372_**	**_709_**
459.GemsFDTD	32	1272	267	**_1272_**	**_267_**	1272	267	32	**_1368_**	**_248_**	1365	249	1372	247
465.tonto	32	**_540_**	**_584_**	541	582	538	585	32	**_522_**	**_603_**	522	603	523	602
470.lbm	32	**_840_**	**_524_**	840	524	839	524	32	**_840_**	**_524_**	840	524	839	524
481.wrf	32	726	493	**_726_**	**_492_**	728	491	32	**_720_**	**_496_**	722	495	720	497
482.sphinx3	32	**_1349_**	**_462_**	1346	463	1352	461	32	**_1336_**	**_467_**	1338	466	1332	468

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on altosr380f2 Fri Mar 15 11:53:31 2013

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2690 0 @ 2.90GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       132114000 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux altosr380f2 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 15 11:50

SPEC is set to: /usr/cpu2006
   Filesystem     Type     Size  Used Avail Use% Mounted on
   /dev/mapper/vg_altosr380f2-lv_root
                  ext4     913G   25G  842G   3% /

Additional information from dmidecode:
  Memory:
   16x Hynix HMT31GR7CFR4C-PB 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Altos R360 F2 and Altos R380 F2 are electronically equivalent.
 This result was measured on Altos R380 F2.
```

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc   -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
454.calculix: -DSPEC_CPU_LP64 -nofor_main
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
          -opt-mem-layout-trans=3

 470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
          -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
             -static -auto-ilp32 -opt-mem-layout-trans=3
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
              -opt-mem-layout-trans=3
```

```
481.wrf: Same as 454.calculix
```

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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