



# SPEC® CFP2006 Result

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Huawei

**SPECfp®2006 = 55.5**

Tecal RH5885 V2 (Intel Xeon E7-4850)

**SPECfp\_base2006 = 53.4**

CPU2006 license: 3175

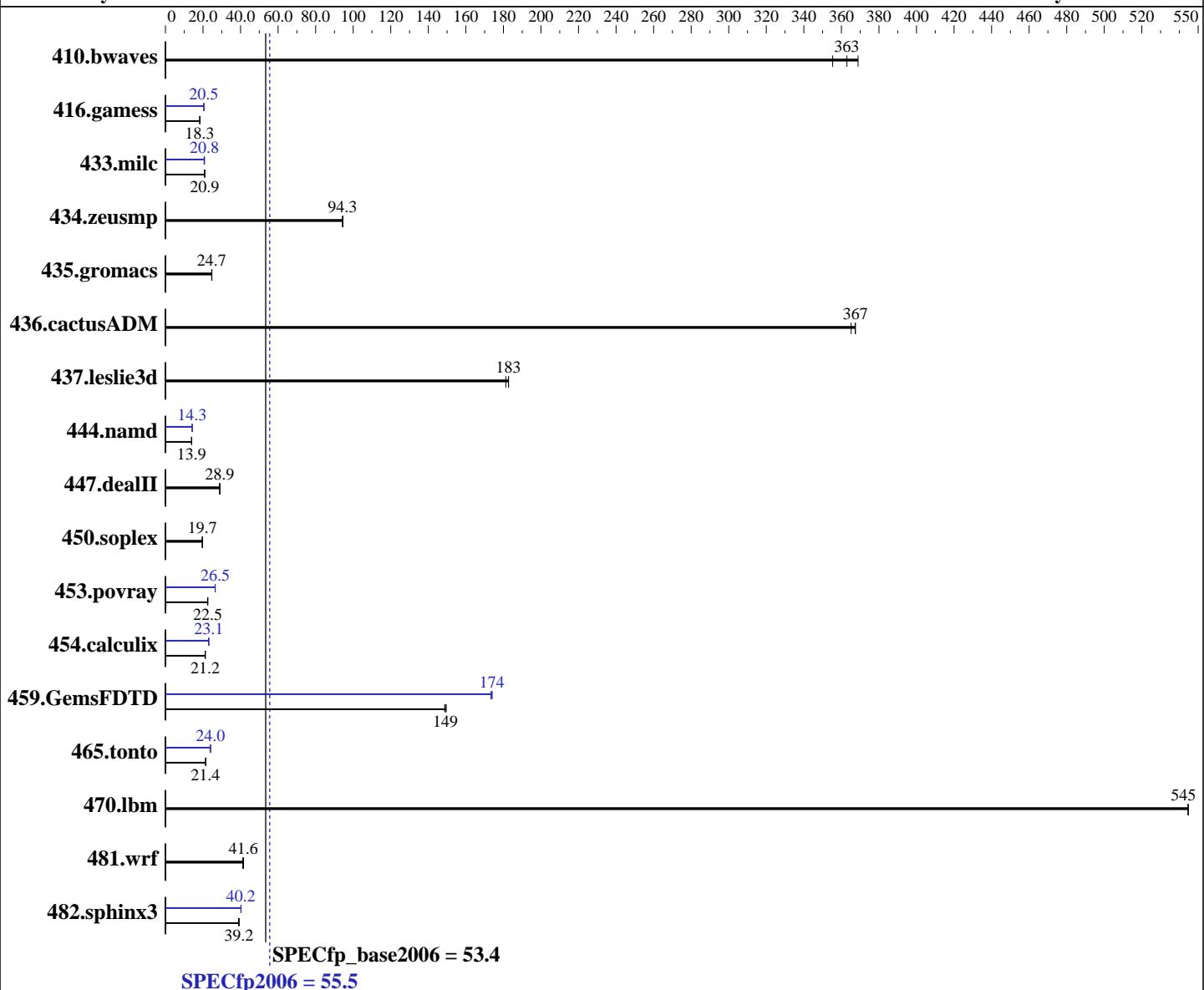
**Test date:** Feb-2013

**Test sponsor:** Huawei

**Hardware Availability:** Oct-2012

**Tested by:** Huawei

**Software Availability:** Oct-2012



## Hardware

CPU Name: Intel Xeon E7-4850  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 Compiler: 2.6.32-220.el6.x86\_64  
 C/C++: Version 13.0.0.079 of Intel C++ Studio XE for Linux;  
 Fortran: Version 13.0.0.079 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1 TB (64 x 16 GB 4Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	38.2	355	<b>37.4</b>	<b>363</b>	36.8	369	38.2	355	<b>37.4</b>	<b>363</b>	36.8	369
416.gamess	1068	18.3	<b>1070</b>	<b>18.3</b>	1071	18.3	955	20.5	<b>955</b>	<b>20.5</b>	956	20.5
433.milc	439	20.9	440	20.9	<b>440</b>	<b>20.9</b>	446	20.6	<b>442</b>	<b>20.8</b>	441	20.8
434.zeusmp	96.5	94.3	<b>96.5</b>	<b>94.3</b>	96.3	94.5	96.5	94.3	<b>96.5</b>	<b>94.3</b>	96.3	94.5
435.gromacs	290	24.6	<b>289</b>	<b>24.7</b>	288	24.8	290	24.6	<b>289</b>	<b>24.7</b>	288	24.8
436.cactusADM	32.5	367	<b>32.5</b>	<b>367</b>	32.7	365	32.5	367	<b>32.5</b>	<b>367</b>	32.7	365
437.leslie3d	51.8	181	51.4	183	<b>51.4</b>	<b>183</b>	51.8	181	51.4	183	<b>51.4</b>	<b>183</b>
444.namd	575	13.9	575	14.0	<b>575</b>	<b>13.9</b>	563	14.3	562	14.3	<b>563</b>	<b>14.3</b>
447.dealII	<b>395</b>	<b>28.9</b>	395	29.0	396	28.9	<b>395</b>	<b>28.9</b>	395	29.0	396	28.9
450.soplex	423	19.7	425	19.6	<b>424</b>	<b>19.7</b>	423	19.7	425	19.6	<b>424</b>	<b>19.7</b>
453.povray	<b>236</b>	<b>22.5</b>	237	22.4	236	22.6	201	26.5	<b>201</b>	<b>26.5</b>	200	26.6
454.calculix	388	21.3	<b>389</b>	<b>21.2</b>	390	21.1	357	23.1	<b>358</b>	<b>23.1</b>	358	23.1
459.GemsFDTD	70.9	150	<b>71.2</b>	<b>149</b>	71.4	149	61.2	173	61.0	174	<b>61.0</b>	<b>174</b>
465.tonto	460	21.4	459	21.4	<b>460</b>	<b>21.4</b>	411	24.0	409	24.0	<b>410</b>	<b>24.0</b>
470.lbm	25.2	545	<b>25.2</b>	<b>545</b>	25.2	545	25.2	545	<b>25.2</b>	<b>545</b>	25.2	545
481.wrf	269	41.6	271	41.2	<b>269</b>	<b>41.6</b>	269	41.6	271	41.2	<b>269</b>	<b>41.6</b>
482.sphinx3	497	39.2	<b>497</b>	<b>39.2</b>	501	38.9	485	40.2	484	40.3	<b>484</b>	<b>40.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Intel Hyper-Threading set to Disabled

Power Technology set to Custom, Performance/Watt set to Traditional

Sysinfo program /home/cpu2006/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date::: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3

running on RH5885-24 Tue Feb 19 18:49:39 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7- 4850 @ 2.00GHz
        4 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings : 10
    physical 0: cores 0 1 2 8 9 16 17 18 24 25
    physical 1: cores 0 1 2 8 9 16 17 18 24 25
    physical 2: cores 0 1 2 8 9 16 17 18 24 25
    physical 3: cores 0 1 2 8 9 16 17 18 24 25
    cache size : 24576 KB
```

```
From /proc/meminfo
MemTotal:      1058808572 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RH5885-24 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 19 09:15
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_rh588524-lv_home
                ext4   409G   16G  373G   5%  /home
```

Additional information from dmidecode:

```
Memory:
 64x Hyundai HMT42GR7BMR4C-H9 16 GB 1067 MHz 4 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 4x Xeon E7-8870 CPU + 1024GB memory using RHEL6.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
               -inline-level=0 -opt-prefetch -parallel

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll14
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revG.20130313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revG.20130313.xml>



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