SPEC® CINT2006 Result

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Itautec

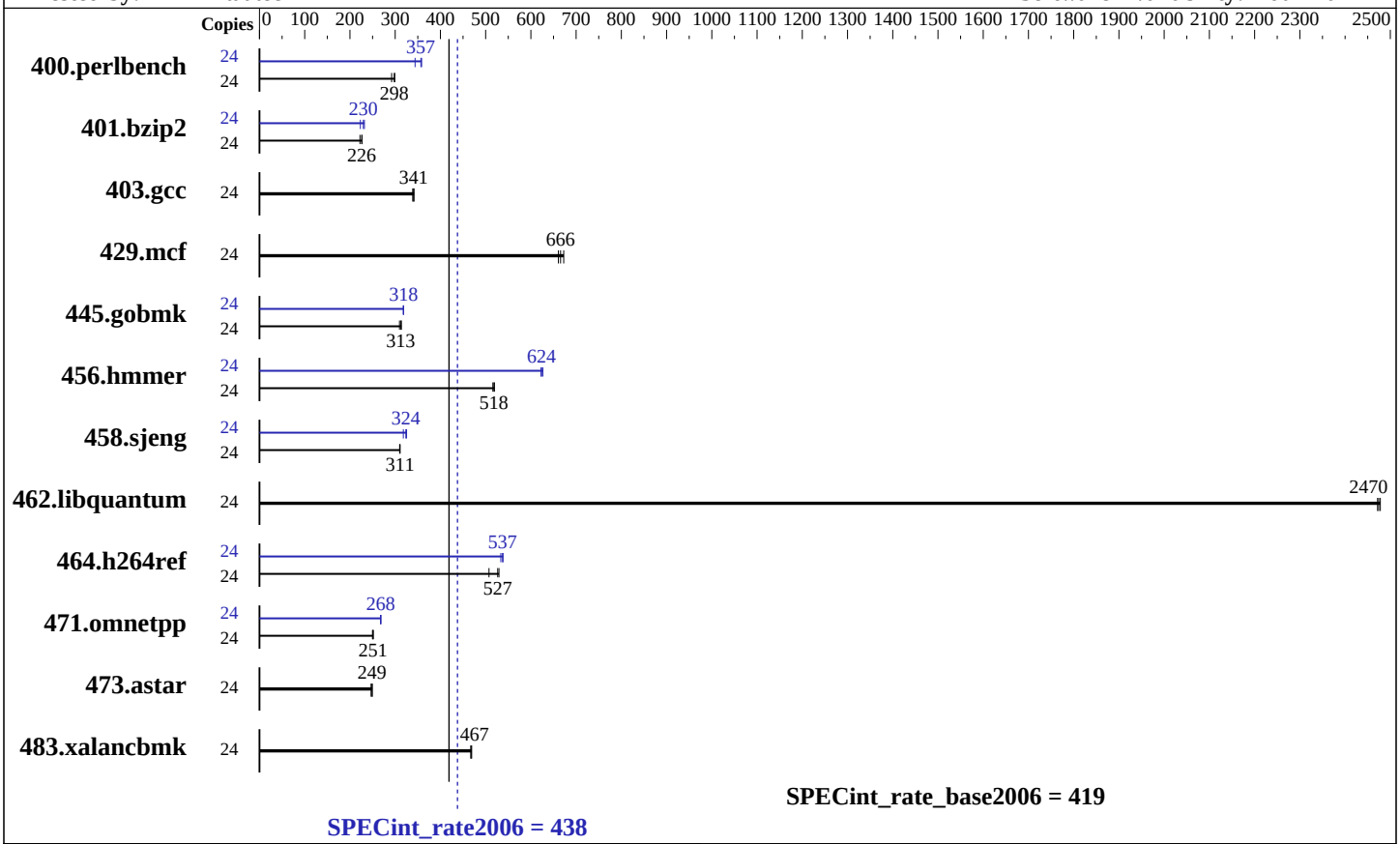
Servidor Itautec MX225 (Intel Xeon E5-2630)

SPECint®_rate2006 = 438

SPECint_rate_base2006 = 419

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2013
Hardware Availability: Jun-2012
Software Availability: Jun-2012



Hardware

CPU Name:	Intel Xeon E5-2630
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1000 GB, SATA-3, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86_64
Compiler:	C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**787**	**298**	784	299	803	292	24	**657**	**357**	682	344	654	358
401.bzip2	24	**1023**	**226**	1023	226	1043	222	24	**1009**	**230**	1038	223	998	232
403.gcc	24	**567**	**341**	570	339	566	342	24	**567**	**341**	570	339	566	342
429.mcf	24	325	673	**329**	**666**	331	661	24	325	673	**329**	**666**	331	661
445.gobmk	24	813	310	803	314	**805**	**313**	24	**791**	**318**	792	318	790	318
456.hmmer	24	434	516	**432**	**518**	431	519	24	360	622	357	626	**359**	**624**
458.sjeng	24	937	310	935	311	**935**	**311**	24	913	318	**897**	**324**	893	325
462.libquantum	24	201	2480	**201**	**2470**	201	2470	24	201	2480	**201**	**2470**	201	2470
464.h264ref	24	1047	507	**1009**	**527**	1003	529	24	**989**	**537**	996	533	986	538
471.omnetpp	24	**597**	**251**	600	250	597	251	24	**559**	**268**	561	268	559	268
473.astar	24	**677**	**249**	683	247	676	249	24	**677**	**249**	683	247	676	249
483.xalancbmk	24	**354**	**467**	354	467	353	469	24	**354**	**467**	354	467	353	469

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

Platform Notes

```
Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on mx225 Sat Feb 16 07:57:03 2013

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Genuine Intel(R) CPU @ 2.30GHz
   model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
```

Continued on next page

Platform Notes (Continued)

```
        siblings  : 12
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
     cache size : 15360 KB

  From /proc/meminfo
     MemTotal:       65917564 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux mx225 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012 x86_64
     x86_64 x86_64 GNU/Linux

  run-level 3 Feb 15 18:27

  SPEC is set to: /home/rcaneca/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_mx225-lv_home
                    ext4    836G  2.2G  791G   1% /home

  (End of data from sysinfo program)
```

General Notes

```
This result was measured on the Servidor Itautec MX225.
The Servidor Itautec MX215 and the Servidor Itautec MX225
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`
`-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`

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