



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 665

PowerEdge M915 (AMD Opteron 6320, 2.80 GHz)

SPECint\_rate\_base2006 = 586

CPU2006 license: 55

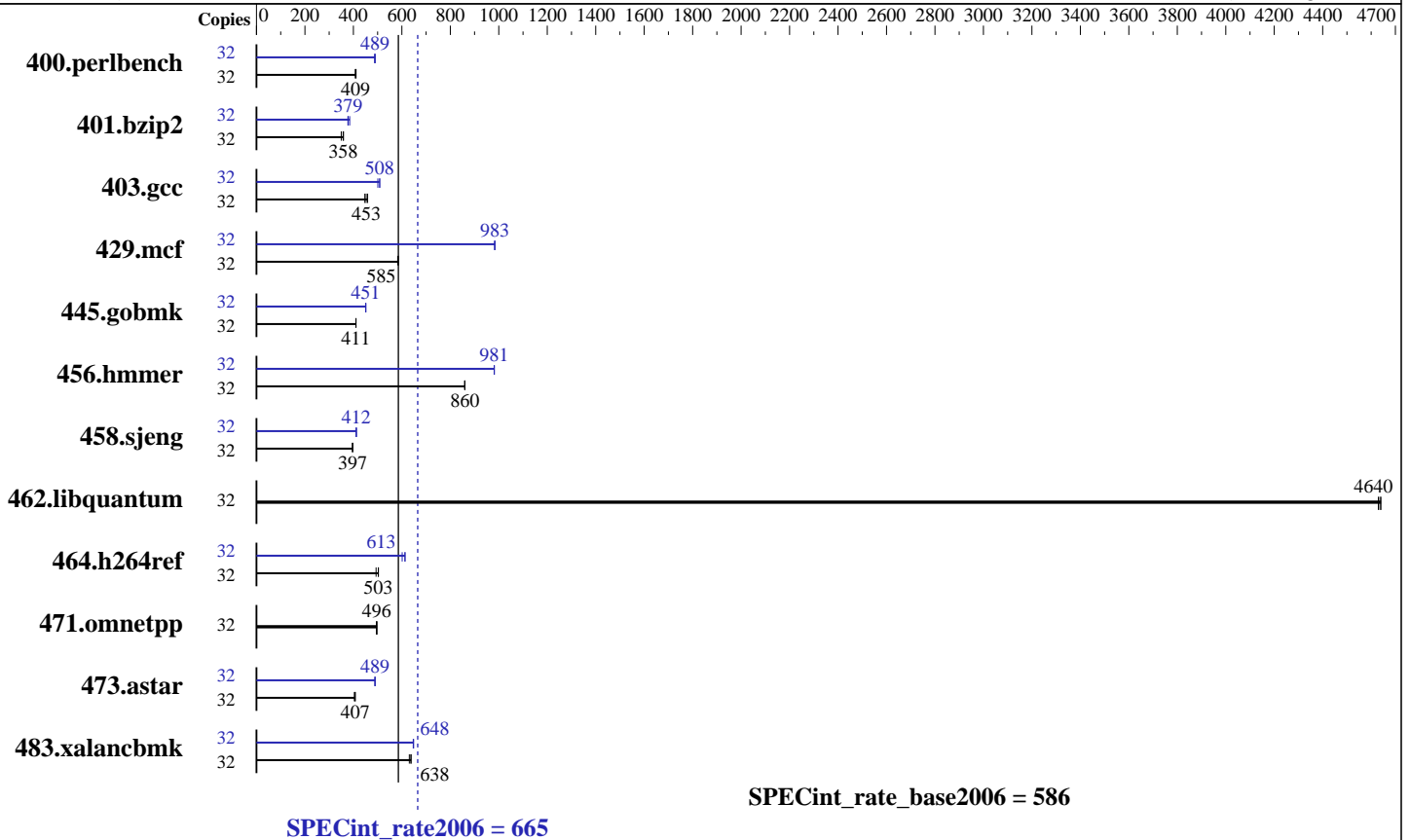
Test date: Nov-2012

Test sponsor: Dell Inc.

Hardware Availability: Dec-2012

Tested by: Dell Inc.

Software Availability: Aug-2012



## Hardware

CPU Name: AMD Opteron 6320  
 CPU Characteristics: AMD Turbo CORE technology up to 3.30 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 256 KB I on chip per chip,  
 64 KB I shared / 2 cores;  
 16 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores  
 L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 4 cores  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB 10000 RPM SAS  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 6.3,  
 Kernel 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 4.5.2 of x86 Open64 Compiler Suite  
 (from AMD)  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 10.0 32-bit Library for Linux



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	761	411	<u>765</u>	<u>409</u>	766	408	32	638	490	641	488	<u>639</u>	<u>489</u>
401.bzip2	32	<u>863</u>	<u>358</u>	883	350	860	359	32	<u>816</u>	<u>379</u>	818	377	803	385
403.gcc	32	576	447	<u>569</u>	<u>453</u>	562	459	32	507	509	<u>507</u>	<u>508</u>	514	501
429.mcf	32	498	586	<u>499</u>	<u>585</u>	501	583	32	297	984	297	983	<u>297</u>	<u>983</u>
445.gobmk	32	<u>818</u>	<u>411</u>	818	410	817	411	32	<u>744</u>	<u>451</u>	744	451	745	450
456.hammer	32	347	860	348	859	<u>347</u>	<u>860</u>	32	304	982	304	981	<u>304</u>	<u>981</u>
458.sjeng	32	975	397	<u>976</u>	<u>397</u>	977	397	32	<u>940</u>	<u>412</u>	942	411	939	412
462.libquantum	32	143	4640	<u>143</u>	<u>4640</u>	143	4630	32	143	4640	<u>143</u>	<u>4640</u>	143	4630
464.h264ref	32	1435	494	1407	503	<u>1408</u>	<u>503</u>	32	1178	601	<u>1156</u>	<u>613</u>	1155	613
471.omnetpp	32	402	497	<u>403</u>	<u>496</u>	404	495	32	402	497	<u>403</u>	<u>496</u>	404	495
473.astar	32	<u>552</u>	<u>407</u>	556	404	551	407	32	459	490	459	489	<u>459</u>	<u>489</u>
483.xalancbmk	32	<u>346</u>	<u>638</u>	350	631	346	638	32	341	647	<u>341</u>	<u>648</u>	341	648

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent\_hugepage=never as a boot parameter in /boot/grub/menu.lst

Set vm/nr\_hugepages=28672 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "896"

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/amd1206-rate-libs-revA/32:/root/cpu2006-1.2/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at  
<http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3



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## Base Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-Ofast -CG:local\_sched\_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000  
-IPA:small\_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2  
-march=bdver1

C++ benchmarks:  
-Ofast -m32 -INLINE:aggressive=on -CG:cmp\_peep=on -D\_\_OPEN64\_FAST\_SET  
-march=bdver1 -L/root/work/libraries/SmartHeap-10/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

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## Peak Portability Flags (Continued)

```

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-GRA:aggr_loop_splitting=off -GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-march=bdver2

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bdt=2m:heap=2m -mso -march=bdver1

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
-IPA:pu_reorder=1 -LNO:ignore_feedback=off -WOPT:if_conv=2
-HP:bd=2m:heap=2m -march=bdver1

456.hmmer: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -OPT:alias=disjoint
-OPT:unroll_times_max=16 -OPT:unroll_size=512
-OPT:unroll_level=2 -OPT:keep_ext=on -CG:cflow=0
-CG:cmp_peep=on -CG:pre_local_sched=off -HP:bdt=2m:heap=2m
-CG:p2align=0 -CG:load_exe=3 -CG:dsched=on -march=bdver1

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## Peak Optimization Flags (Continued)

458.sjeng: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast  
-CG:ptr\_load\_use=0 -CG:divrem\_opt=on -CG:movext\_icmp=off  
-CG:locs\_best=on -LNO:full\_unroll=10 -IPA:pu\_reorder=2  
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1

462.libquantum: basepeak = yes

464.h264ref: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3  
-OPT:unroll\_size=256 -OPT:unroll\_times\_max=2  
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr\_load\_use=0  
-CG:local\_sched\_alg=1 -HP:bdt=2m:heap=2m -march=bdver1

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast  
-WOPT:if\_conv=0 -WOPT:sib=on -CG:divrem\_opt=on  
-CG:p2align=1 -CG:dsched=on -GRA:optimize\_boundary=on  
-OPT:alias=disjoint -INLINE:aggressive=on  
-IPA:small\_pu=3000 -IPA:plimit=3000 -HP:bdt=2m:heap=2m  
-march=bdver1

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll\_size=512  
-OPT:unroll\_times\_max=8 -D\_\_OPEN64\_FAST\_SET  
-INLINE:aggressive=on -m32 -CG:cmp\_peep=on  
-CG:local\_sched=off -CG:p2align=1 -GRA:unspill=on  
-TENV:frame\_pointer=off -fno-emit-exceptions -march=bdver2  
-mno-fma4  
-L/root/work/libraries/SmartHeap-10/lib -lsmarheap

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-II.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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