



# SPEC® CFP2006 Result

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## Acer Incorporated

## SPECfp®\_rate2006 = 369

## Acer AR585 F1 (AMD Opteron 6308)

## SPECfp\_rate\_base2006 = 340

CPU2006 license: 97

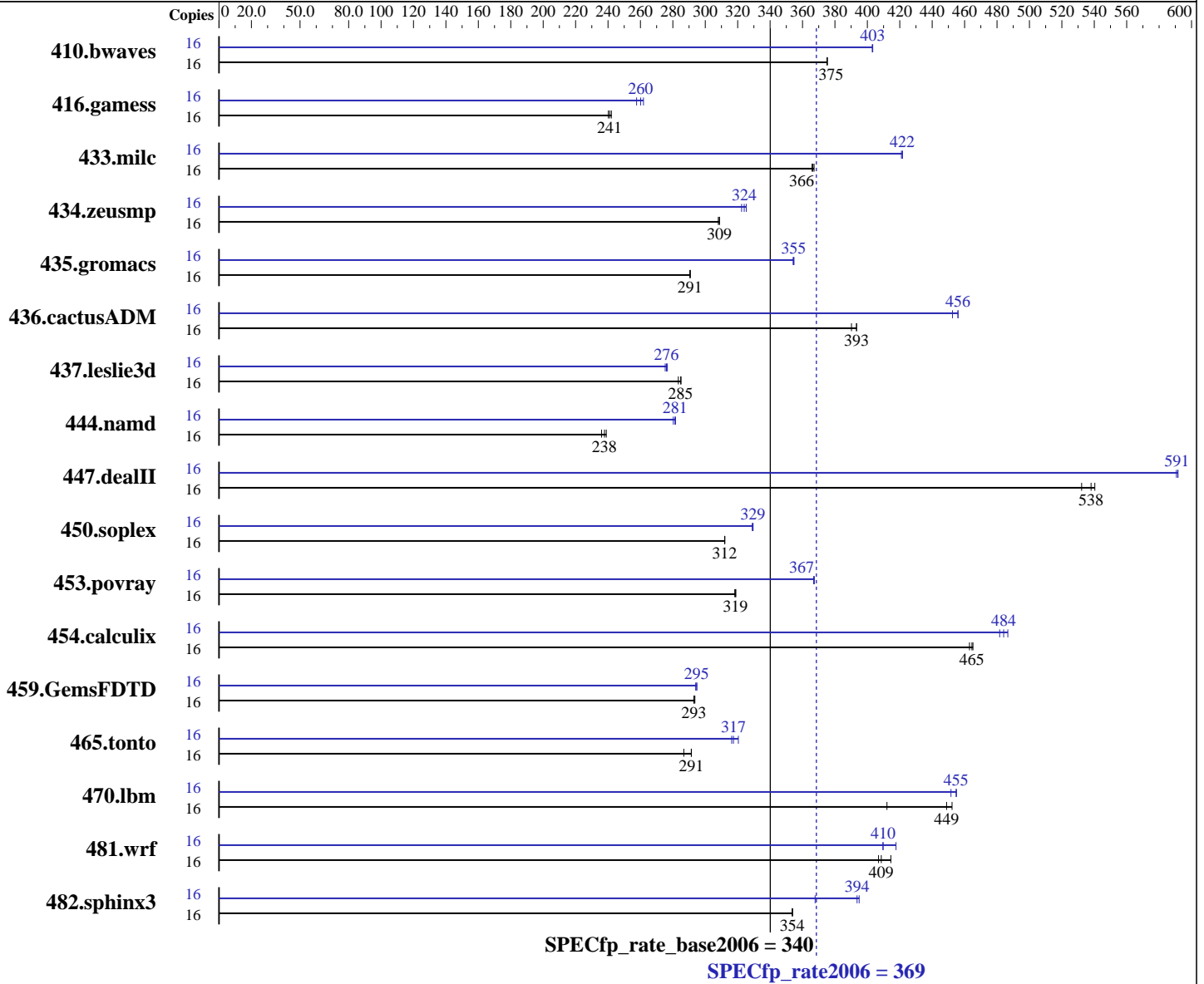
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2012

Hardware Availability: Nov-2012

Software Availability: Aug-2012



**Hardware**

CPU Name: AMD Opteron 6308  
 CPU Characteristics:  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip  
 CPU(s) orderable: 2,4 chips

*Continued on next page*

**Software**

Operating System: Red Hat Enterprise Linux Server release 6.3, Kernel 2.6.32-279.el6.x86\_64  
 Compiler: C/C++/Fortran: Version 4.5.2 of x86 Open64 Compiler Suite (from AMD)  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit

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Primary Cache: 128 KB I on chip per chip,  
64 KB I shared / 2 cores;  
16 KB D on chip per core

Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores

L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 2 cores

Other Cache: None

Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)

Disk Subsystem: 1 x 500 GB SATA, 7200 RPM

Other Hardware: None

Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	16	<u>579</u>	<u>375</u>	580	375	579	375	16	<u>539</u>	<u>403</u>	539	403	539	403		
416.gamess	16	1294	242	<u>1300</u>	<u>241</u>	1304	240	16	1196	262	<u>1204</u>	<u>260</u>	1216	258		
433.milc	16	400	367	401	366	<u>401</u>	<u>366</u>	16	348	422	<u>348</u>	<u>422</u>	349	421		
434.zeusmp	16	<u>472</u>	<u>309</u>	473	308	472	309	16	448	325	452	322	<u>449</u>	<u>324</u>		
435.gromacs	16	393	291	393	291	<u>393</u>	<u>291</u>	16	<u>322</u>	<u>355</u>	322	355	323	354		
436.cactusADM	16	486	393	490	390	<u>486</u>	<u>393</u>	16	423	453	419	456	<u>419</u>	<u>456</u>		
437.leslie3d	16	528	285	531	283	<u>528</u>	<u>285</u>	16	544	277	<u>545</u>	<u>276</u>	546	275		
444.namd	16	<u>540</u>	<u>238</u>	544	236	537	239	16	<u>456</u>	<u>281</u>	458	280	456	282		
447.dealII	16	339	540	344	532	<u>340</u>	<u>538</u>	16	310	591	309	592	<u>309</u>	<u>591</u>		
450.soplex	16	428	312	428	312	<u>428</u>	<u>312</u>	16	405	330	406	329	<u>405</u>	<u>329</u>		
453.povray	16	268	318	267	319	<u>267</u>	<u>319</u>	16	232	367	232	367	<u>232</u>	<u>367</u>		
454.calculix	16	285	463	<u>284</u>	<u>465</u>	284	465	16	271	487	<u>273</u>	<u>484</u>	274	482		
459.GemsFDTD	16	578	294	579	293	<u>579</u>	<u>293</u>	16	<u>576</u>	<u>295</u>	576	295	577	294		
465.tonto	16	549	287	540	291	<u>540</u>	<u>291</u>	16	<u>496</u>	<u>317</u>	498	316	491	320		
470.lbm	16	486	452	533	412	<u>490</u>	<u>449</u>	16	483	455	<u>483</u>	<u>455</u>	487	452		
481.wrf	16	439	407	431	415	<u>437</u>	<u>409</u>	16	436	410	428	418	<u>436</u>	<u>410</u>		
482.sphinx3	16	<u>882</u>	<u>354</u>	882	354	881	354	16	848	368	<u>792</u>	<u>394</u>	790	395		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set transparent\_hugepage=never as a boot parameter in /boot/grub/menu.lst  
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## Operating System Notes (Continued)

```
Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "896"

LD\_LIBRARY\_PATH = "/usr/cpu2006/amd1206-rate-libs-revA/32:/usr/cpu2006/amd1206-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>

Binaries were compiled on a system with 2x AMD Opteron 6386SE chips + 128GB Memory using RHEL 6.3

## Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
```

Continued on next page

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Page 3



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## Base Portability Flags (Continued)

481.wrf: -DSPEC\_CPU\_LINUX -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LP64  
-fno-second-underscore  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-Ofast -OPT:malloc\_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000  
-IPA:small\_pu=100 -mso -march=bdver1

C++ benchmarks:

-Ofast -static -CG:load\_exe=0 -OPT:malloc\_alg=1 -INLINE:aggressive=on  
-HP:bd=2m:heap=2m -D\_\_OPEN64\_FAST\_SET -march=bdver1

Fortran benchmarks:

-Ofast -LNO:blocking=off -LNO:simd\_peel\_align=on -OPT:rsqrt=2  
-OPT:unroll\_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1

Benchmarks using both Fortran and C:

-Ofast -OPT:malloc\_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000  
-IPA:small\_pu=100 -mso -march=bdver1 -LNO:blocking=off  
-LNO:simd\_peel\_align=on -OPT:rsqrt=2 -OPT:unroll\_size=256

## Peak Compiler Invocation

C benchmarks:

openc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

openc openf95

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64

Continued on next page



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## Peak Portability Flags (Continued)

435.gromacs: -DSPEC\_CPU\_LP64  
 436.cactusADM: -DSPEC\_CPU\_LP64 -fno-second-underscore  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LINUX -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LP64  
 -fno-second-underscore

## Peak Optimization Flags

C benchmarks:

433.milc: -Ofast -CG:movnti=1 -CG:locs\_best=on -HP:bdt=2m:heap=2m  
 -IPA:plimit=7000 -IPA:callee\_limit=1200  
 -OPT:struct\_array\_copy=2 -OPT:alias=field\_sensitive -mso  
 -march=bdver1

470.lbm: -Ofast -CG:cmp\_peep=on -OPT:keep\_ext=on -HP:bdt=2m:heap=2m  
 -IPA:plimit=8000 -IPA:small\_pu=100 -march=bdver1 -mso

482.sphinx3: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast  
 -m32 -IPA:plimit=1000 -OPT:malloc\_alg=2 -CG:cmp\_peep=on  
 -CG:p2align=0 -CG:load\_exe=1 -CG:dsched=on  
 -INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch\_ahead=4  
 -mso -march=bdver2

C++ benchmarks:

444.namd: -Ofast -IPA:plimit=3000 -LNO:ignore\_feedback=off  
 -CG:local\_sched\_alg=0 -CG:load\_exe=0 -OPT:unroll\_size=256  
 -fno-exceptions -HP:bdt=2m:heap=2m -LNO:if\_select\_conv=1  
 -OPT:alias=disjoint -LNO:psimd\_iso\_unroll=ON -march=bdver1

447.dealIII: -Ofast -D\_\_OPEN64\_FAST\_SET -static -INLINE:aggressive=on  
 -LNO:opt=1 -LNO:simd=2 -fno-emit-exceptions -m32  
 -OPT:unroll\_times\_max=8 -OPT:unroll\_size=256  
 -OPT:unroll\_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on  
 -CG:cmp\_peep=on -CG:movext\_icmp=off -TENV:frame\_pointer=off  
 -march=bdver1

450.soplex: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3  
 -LNO:ignore\_feedback=off -INLINE:aggressive=on -OPT:RO=1  
 -OPT:IEEE\_arith=3 -OPT:IEEE\_NaN\_Inf=off  
 -OPT:fold\_unsigned\_relops=on -fno-exceptions -CG:p2align=0  
 -m32 -mno-fma4 -HP:bdt=2m:heap=2m -WOPT:sib=on

Continued on next page



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## Peak Optimization Flags (Continued)

450.soplex (continued):

-march=bdver1

453.povray: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-CG:pre\_local\_sched=off -CG:p2align=0 -CG:p2align\_split=on

-CG:dsched=on -INLINE:aggressive=on -HP:bd=2m:heap=2m

-OPT:transform=2 -OPT:alias=disjoint -WOPT:aggcm=0

-march=bdver2

Fortran benchmarks:

410.bwaves: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-OPT:Ofast -OPT:treeheight=on -LNO:blocking=off

-LNO:ignore\_feedback=off -LNO:fu=4 -LNO:loop\_model\_simd=on

-LNO:simd\_rm\_unity\_remainder=on -WOPT:aggstr=0

-HP:bd=2m:heap=2m -CG:cmp\_peep=on -march=bdver1

416.gamess: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-LNO:fu=6 -LNO:blocking=0 -LNO:simd=2 -OPT:ro=3

-OPT:recip=on -CG:local\_sched\_alg=1 -HP:bd=2m:heap=2m

-WOPT:sib=on -march=bdver1

434.zeusmp: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-LNO:blocking=off -LNO:interchange=off -IPA:plimit=1500

-HP:bd=2m:heap=2m -march=bdver1

437.leslie3d: -Ofast -CG:pre\_minreg\_level=2 -LNO:simd=0 -LNO:fusion=2

-HP:bd=2m:heap=2m -mso -march=bdver1

459.GemsFDTD: -Ofast -IPA:plimit=1500 -OPT:unroll\_size=1024

-OPT:unroll\_times\_max=16 -LNO:fission=2

-CG:local\_sched\_alg=2 -HP -march=bdver1

465.tonto: -Ofast -OPT:alias=no\_f90\_pointer\_alias -LNO:blocking=off

-CG:load\_exe=1 -CG:local\_sched\_alg=3 -IPA:plimit=525

-HP:bd=2m:heap=2m -march=bdver1

Benchmarks using both Fortran and C:

435.gromacs: -Ofast -OPT:rsqrt=2 -HP:bd=2m:heap=2m

-CG:local\_sched\_alg=2 -CG:load\_exe=3 -GRA:unspill=on

-march=bdver1 -LNO:simd=3

436.cactusADM: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast

-LNO:blocking=off -LNO:prefetch=2 -LNO:pf2=0

-LNO:prefetch\_ahead=4 -HP -CG:locs\_shallow\_depth=1

-CG:load\_exe=0 -CG:dsched=on -WOPT:sib=on -march=bdver1

454.calculix: -Ofast -OPT:unroll\_size=256 -OPT:alias=disjoint

-GRA:optimize\_boundary=on -CG:dsched=on -HP:bd=2m:heap=2m

-march=bdver1

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Page 6



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## Peak Optimization Flags (Continued)

```
481.wrf: -Ofast -LNO:blocking=off -LANG:copyinout=off
        -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -HP
        -WOPT:sib=on -march=bdver1
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-I.html>

<http://www.spec.org/cpu2006/flags/amd1206-acer-platform-rate-revA-I.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-452-flags-rate-revA-I.xml>

<http://www.spec.org/cpu2006/flags/amd1206-acer-platform-rate-revA-I.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 13:36:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 4 December 2012.