# SPEC® CINT2006 Result

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## Itautec

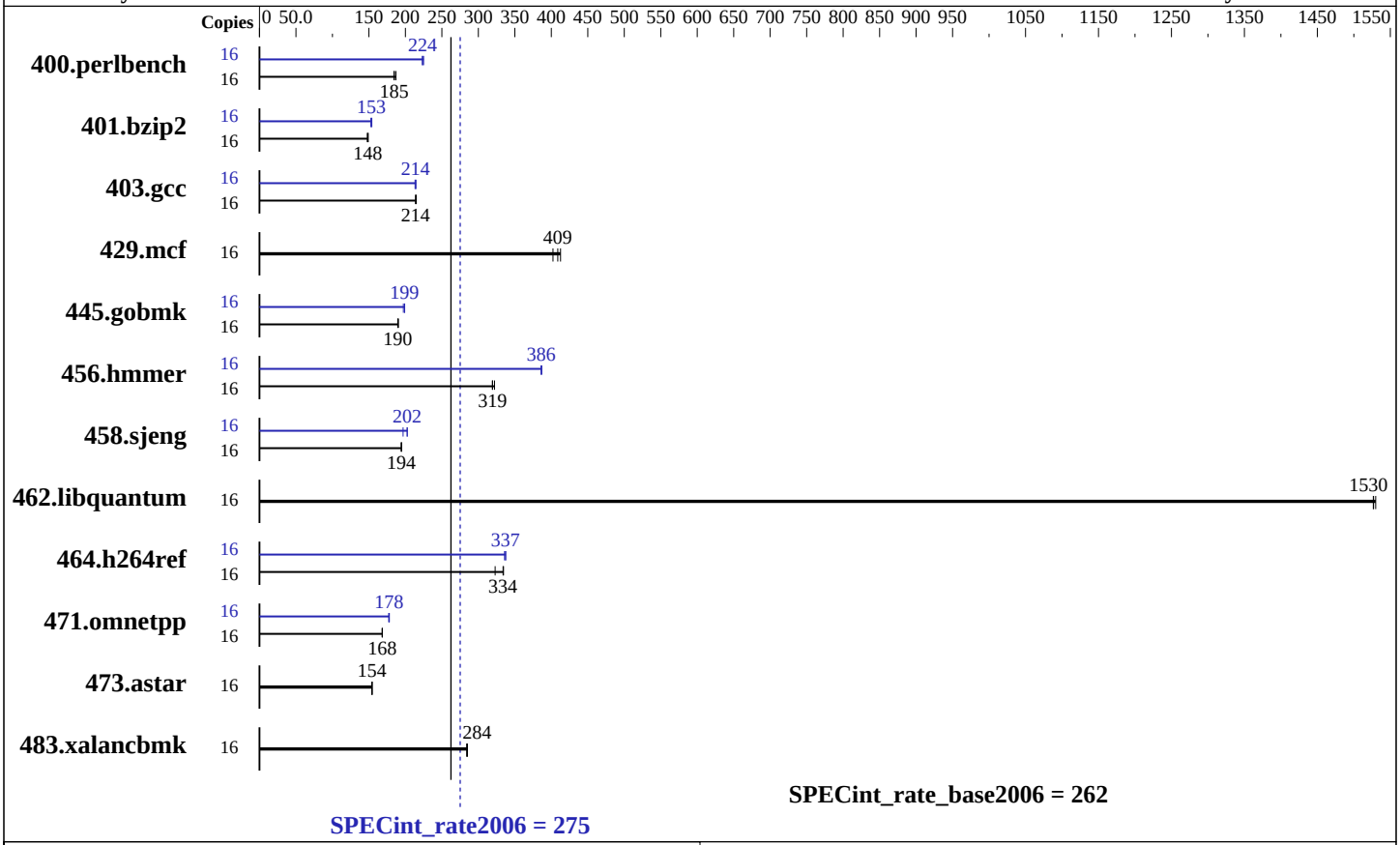
## Servidor Itautec MX205 (Intel Xeon E5-2650)

**SPECint®_rate2006 = 275**

**SPECint_rate_base2006 = 262**

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Apr-2012
**Hardware Availability:** Jun-2012
**Software Availability:** Dec-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 1 chip, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 32 GB (8 x 4 GB 1Rx4 PC3-12800R-11, ECC) |
| Disk Subsystem: | 500 GB, SATA-2, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server Release 6.3, 2.6.32-279.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011 |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 848 | 184 | **845** | **185** | 836 | 187 | 16 | 701 | 223 | 695 | 225 | **699** | **224** |
| 401.bzip2 | 16 | **1041** | **148** | 1045 | 148 | 1036 | 149 | 16 | 1003 | 154 | 1011 | 153 | **1007** | **153** |
| 403.gcc | 16 | 599 | 215 | **602** | **214** | 602 | 214 | 16 | **602** | **214** | 604 | 213 | 601 | 214 |
| 429.mcf | 16 | 363 | 402 | **357** | **409** | 354 | 413 | 16 | 363 | 402 | **357** | **409** | 354 | 413 |
| 445.gobmk | 16 | **884** | **190** | 886 | 190 | 881 | 190 | 16 | **844** | **199** | 851 | 197 | 843 | 199 |
| 456.hmmer | 16 | **467** | **319** | 463 | 322 | 468 | 319 | 16 | 387 | 386 | 386 | 387 | **386** | **386** |
| 458.sjeng | 16 | **995** | **194** | 995 | 195 | 995 | 194 | 16 | 984 | 197 | **957** | **202** | 956 | 203 |
| 462.libquantum | 16 | 217 | 1530 | **217** | **1530** | 217 | 1530 | 16 | 217 | 1530 | **217** | **1530** | 217 | 1530 |
| 464.h264ref | 16 | 1096 | 323 | 1059 | 334 | **1061** | **334** | 16 | 1054 | 336 | 1049 | 337 | **1050** | **337** |
| 471.omnetpp | 16 | 594 | 168 | 595 | 168 | **594** | **168** | 16 | 562 | 178 | 565 | 177 | **562** | **178** |
| 473.astar | 16 | **728** | **154** | 730 | 154 | 727 | 154 | 16 | **728** | **154** | 730 | 154 | 727 | 154 |
| 483.xalancbmk | 16 | 387 | 285 | 389 | 284 | **388** | **284** | 16 | 387 | 285 | 389 | 284 | **388** | **284** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

## Platform Notes

```
Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on localhost Thu Apr  5 17:41:34 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
      1 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
```

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## Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3 4 5 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       32850352 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.3 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux localhost 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Apr 5 17:35

  SPEC is set to: /home/rcaneca/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_fam5-lv_home
                    ext4    394G  2.1G  372G   1% /home

  (End of data from sysinfo program)
```

## General Notes

```
This result was measured on the Servidor Itautec MX205.
The Servidor Itautec LX205, the Servidor Itautec MX205 and the Servidor Itautec MX225+
are electronically equivalent.
```

## Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page

## Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`


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