



# SPEC® CFP2006 Result

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## IBM Corporation

IBM System x3250 M4  
(Intel Pentium G640T, 2.40 GHz)

**SPECfp®2006 = 36.0**

**SPECfp\_base2006 = 35.3**

CPU2006 license: 11

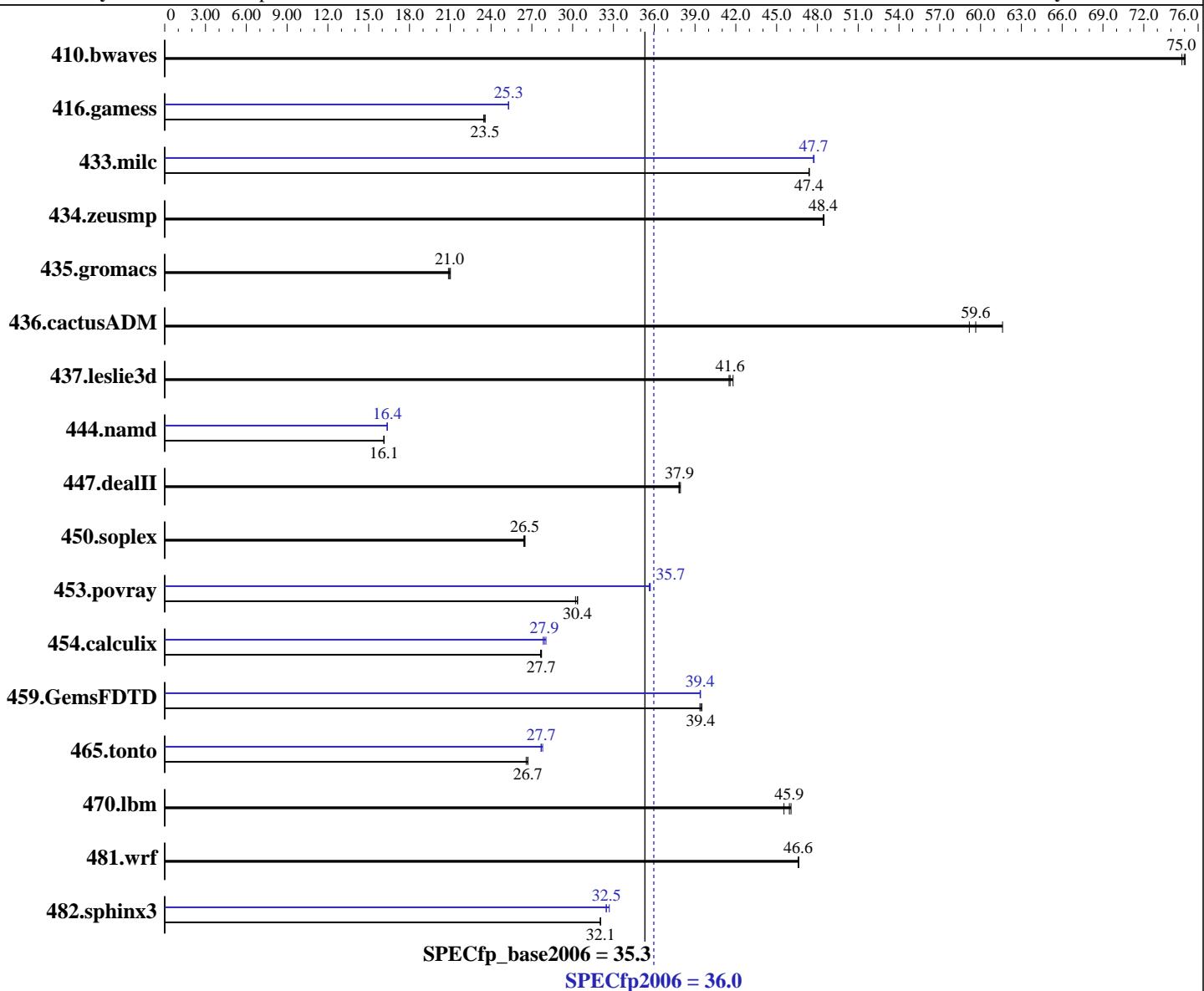
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Pentium G640T  
CPU Characteristics:  
CPU MHz: 2400  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
Compiler: 2.6.32-220.el6.x86\_64  
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 3 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1066 MHz)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>181</b>	<b>75.0</b>	181	75.1	182	74.8	<b>181</b>	<b>75.0</b>	181	75.1	182	74.8
416.gamess	831	23.6	835	23.5	<b>832</b>	<b>23.5</b>	774	25.3	<b>774</b>	<b>25.3</b>	774	25.3
433.milc	194	47.4	<b>194</b>	<b>47.4</b>	194	47.4	<b>192</b>	<b>47.8</b>	<b>192</b>	<b>47.7</b>	192	47.7
434.zeusmp	188	48.5	188	48.4	<b>188</b>	<b>48.4</b>	188	48.5	188	48.4	<b>188</b>	<b>48.4</b>
435.gromacs	<b>341</b>	<b>21.0</b>	342	20.9	340	21.0	<b>341</b>	<b>21.0</b>	342	20.9	340	21.0
436.cactusADM	202	59.2	194	61.6	<b>200</b>	<b>59.6</b>	202	59.2	194	61.6	<b>200</b>	<b>59.6</b>
437.leslie3d	225	41.8	<b>226</b>	<b>41.6</b>	226	41.5	225	41.8	<b>226</b>	<b>41.6</b>	226	41.5
444.namd	497	16.1	<b>497</b>	<b>16.1</b>	497	16.1	490	16.4	490	16.4	<b>490</b>	<b>16.4</b>
447.dealII	302	37.9	303	37.8	<b>302</b>	<b>37.9</b>	302	37.9	303	37.8	<b>302</b>	<b>37.9</b>
450.soplex	315	26.5	316	26.4	<b>315</b>	<b>26.5</b>	315	26.5	316	26.4	<b>315</b>	<b>26.5</b>
453.povray	<b>175</b>	<b>30.4</b>	175	30.4	176	30.2	<b>149</b>	<b>35.7</b>	149	35.6	149	35.7
454.calculix	298	27.7	<b>298</b>	<b>27.7</b>	298	27.6	<b>295</b>	<b>27.9</b>	296	27.8	294	28.0
459.GemsFDTD	269	39.5	270	39.4	<b>269</b>	<b>39.4</b>	269	39.4	<b>269</b>	<b>39.4</b>	269	39.4
465.tonto	368	26.7	<b>369</b>	<b>26.7</b>	370	26.6	354	27.8	355	27.7	<b>355</b>	<b>27.7</b>
470.lbm	<b>299</b>	<b>45.9</b>	302	45.5	298	46.1	<b>299</b>	<b>45.9</b>	302	45.5	298	46.1
481.wrf	240	46.6	240	46.6	<b>240</b>	<b>46.6</b>	240	46.6	240	46.6	<b>240</b>	<b>46.6</b>
482.sphinx3	608	32.0	608	32.1	<b>608</b>	<b>32.1</b>	<b>600</b>	<b>32.5</b>	596	32.7	600	32.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Turbo Mode enabled in BIOS

C-State enabled in BIOS

Sysinfo program /root/SPECcpul.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on localhost.localdomain Sun Sep 30 09:02:02 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Pentium(R) CPU G640T @ 2.40GHz
  1 "physical id"s (chips)
  2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings : 2
  physical 0: cores 0 1
cache size : 3072 KB
```

```
From /proc/meminfo
MemTotal:      16322724 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 28 14:54
```

```
SPEC is set to: /root/SPECcpul.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                  ext4   50G   17G   31G  35%  /
```

Additional information from dmidecode:

```
Memory:
2x Micron 18JSF1G72AZ-1G6D1 8 GB 1067 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/SPECcpul.2/lib32:/root/SPECcpul.2/lib64"

OMP\_NUM\_THREADS = "2"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enable

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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