SPEC® CFP2006 Result

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NEC Corporation

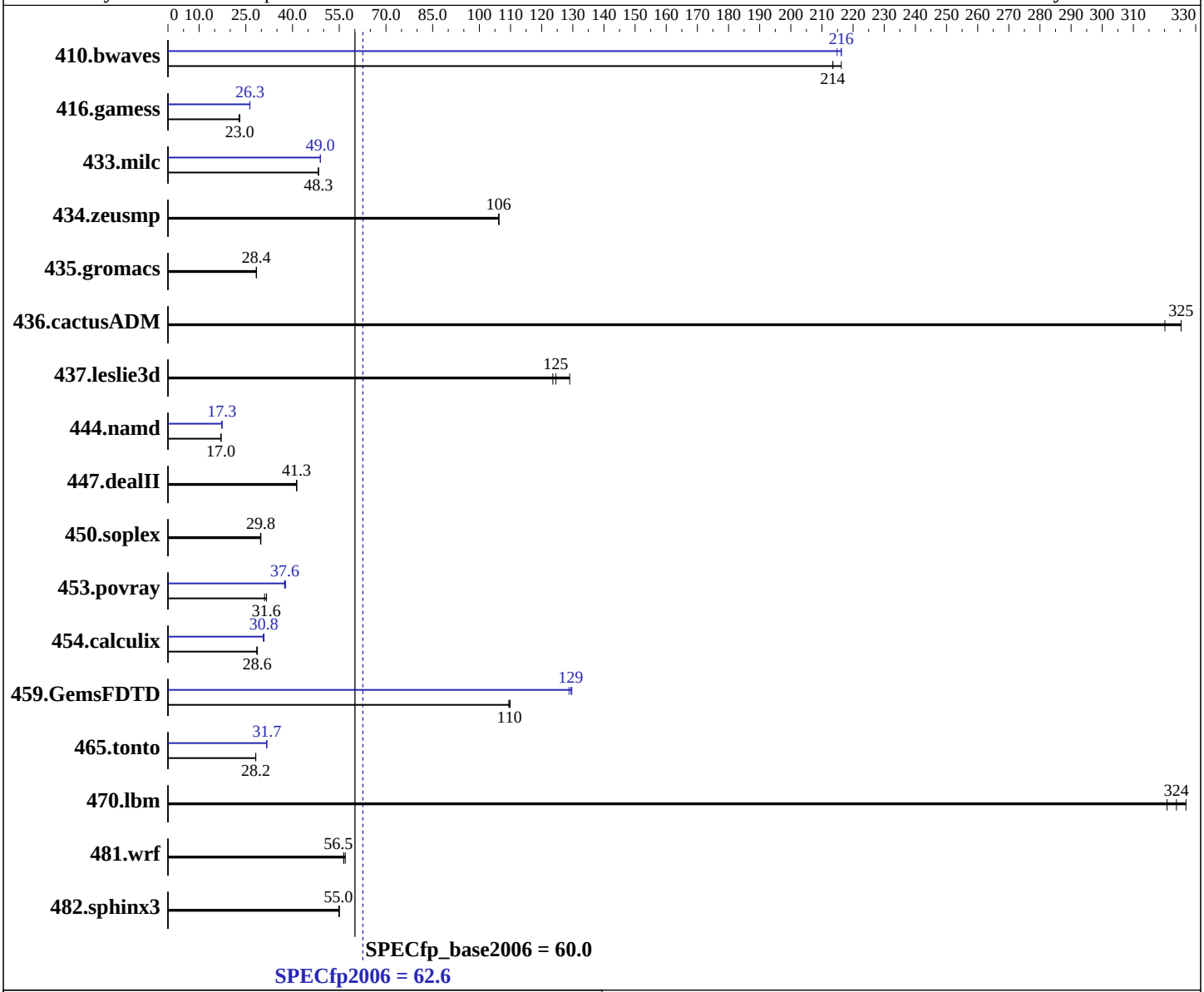
Express5800/E120d-1 (Intel Xeon E5-2430L)

SPECfp®2006 = 62.6

SPECfp_base2006 = 60.0

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Aug-2012
Hardware Availability: Sep-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-2430L
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.3.293 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**63.7**	**214**	62.9	216	63.7	213	**62.9**	**216**	63.3	215	62.9	216
416.gamess	**850**	**23.0**	859	22.8	849	23.1	744	26.3	744	26.3	**744**	**26.3**
433.milc	190	48.3	**190**	**48.3**	190	48.3	**188**	**49.0**	188	49.0	188	48.9
434.zeusmp	**85.7**	**106**	85.7	106	85.7	106	**85.7**	**106**	85.7	106	85.7	106
435.gromacs	251	28.4	**251**	**28.4**	252	28.4	251	28.4	**251**	**28.4**	252	28.4
436.cactusADM	**36.7**	**325**	37.3	320	36.7	325	**36.7**	**325**	37.3	320	36.7	325
437.leslie3d	72.9	129	**75.5**	**125**	76.1	124	72.9	129	**75.5**	**125**	76.1	124
444.namd	471	17.0	**471**	**17.0**	471	17.0	**463**	**17.3**	463	17.3	463	17.3
447.dealII	277	41.3	**277**	**41.3**	277	41.3	277	41.3	**277**	**41.3**	277	41.3
450.soplex	**280**	**29.8**	279	29.8	281	29.7	**280**	**29.8**	279	29.8	281	29.7
453.povray	**168**	**31.6**	171	31.0	168	31.7	142	37.4	141	37.8	**142**	**37.6**
454.calculix	287	28.7	**288**	**28.6**	289	28.5	270	30.6	**268**	**30.8**	267	30.9
459.GemsFDTD	96.9	109	**96.7**	**110**	96.5	110	81.8	130	**82.0**	**129**	82.4	129
465.tonto	349	28.2	**349**	**28.2**	349	28.2	311	31.6	**310**	**31.7**	310	31.8
470.lbm	42.8	321	42.0	327	**42.4**	**324**	42.8	321	42.0	327	**42.4**	**324**
481.wrf	196	56.9	198	56.4	**198**	**56.5**	196	56.9	198	56.4	**198**	**56.5**
482.sphinx3	355	54.9	**355**	**55.0**	354	55.0	355	54.9	**355**	**55.0**	354	55.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
```

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General Notes (Continued)

```
OMP_NUM_THREADS = "12"

 Added glibc-static-2.12-1.47.el6.x86_64.rpm
 to enable static linking

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 12:49:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 October 2012.