



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint®_rate2006 = 536

Servidor Itautec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001

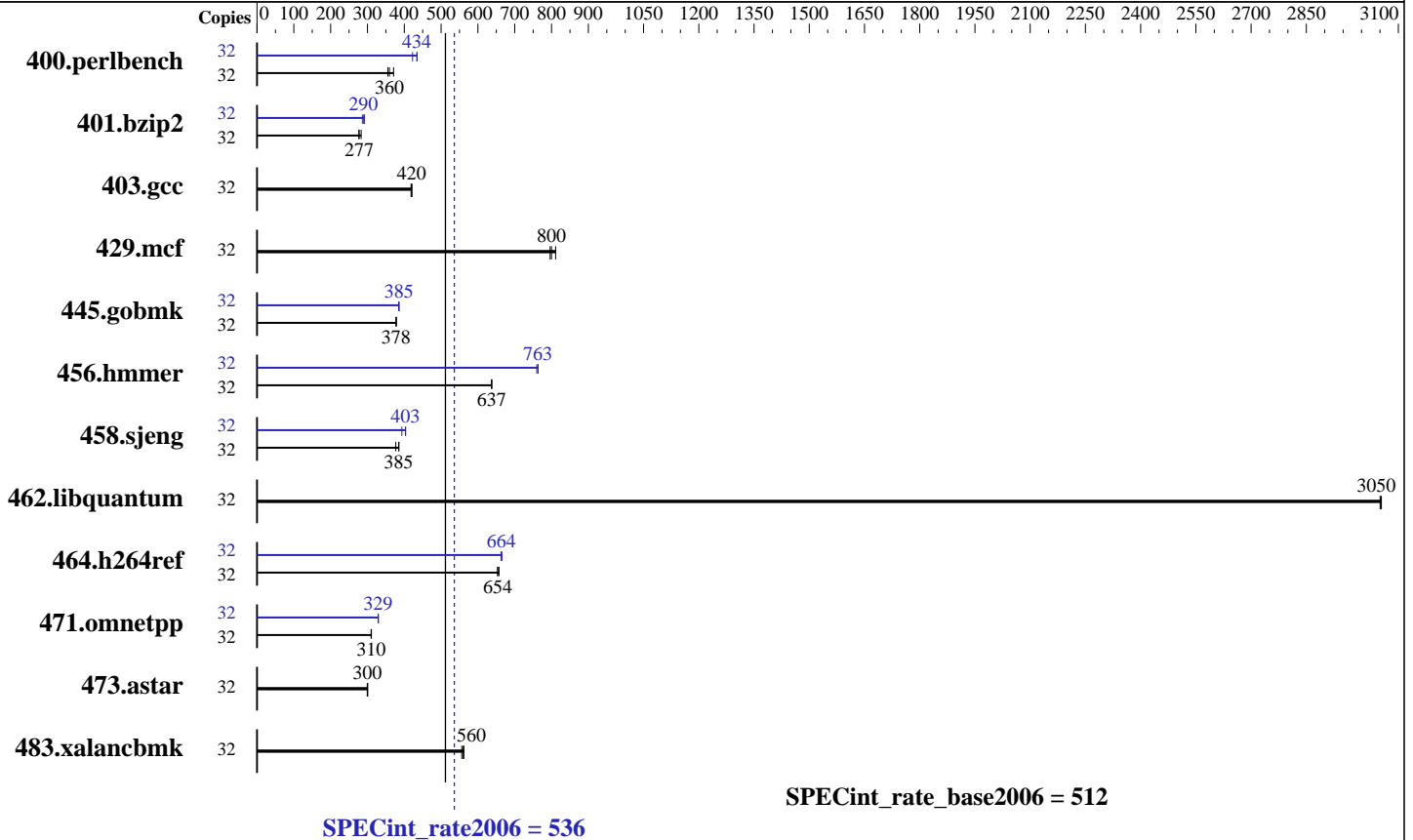
Test date: Dec-2011

Test sponsor: Itautec

Hardware Availability: Jun-2012

Tested by: Itautec

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2650
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (16 x 4 GB 1Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 536

Servidor Itaotec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	843	371	880	355	<u>869</u>	<u>360</u>	32	740	422	719	435	<u>720</u>	<u>434</u>
401.bzip2	32	1092	283	<u>1113</u>	<u>277</u>	1115	277	32	1079	286	1057	292	<u>1066</u>	<u>290</u>
403.gcc	32	614	420	612	421	616	418	32	614	420	612	421	616	418
429.mcf	32	360	811	367	796	<u>365</u>	<u>800</u>	32	360	811	367	796	<u>365</u>	<u>800</u>
445.gobmk	32	888	378	888	378	888	378	32	870	386	<u>871</u>	<u>385</u>	872	385
456.hammer	32	468	637	469	637	<u>468</u>	<u>637</u>	32	<u>391</u>	<u>763</u>	391	763	393	760
458.sjeng	32	1005	385	<u>1006</u>	<u>385</u>	1027	377	32	959	404	984	393	<u>960</u>	<u>403</u>
462.libquantum	32	217	3050	<u>217</u>	<u>3050</u>	217	3050	32	217	3050	<u>217</u>	<u>3050</u>	217	3050
464.h264ref	32	1084	654	<u>1083</u>	<u>654</u>	1077	657	32	1069	663	<u>1067</u>	<u>664</u>	1065	665
471.omnetpp	32	644	311	<u>645</u>	<u>310</u>	645	310	32	607	330	608	329	<u>607</u>	<u>329</u>
473.astar	32	750	300	<u>750</u>	<u>300</u>	747	301	32	750	300	<u>750</u>	<u>300</u>	747	301
483.xalancbmk	32	393	562	397	556	<u>394</u>	<u>560</u>	32	393	562	397	556	<u>394</u>	<u>560</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on localhost Sat Dec 24 16:40:43 2011

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 536

Servidor Itaotec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      65942260 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 24 16:28
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_it5rh-lv_home
                ext4      193G  2.0G  181G   2% /home
```

(End of data from sysinfo program)

General Notes

This result was measured on the Servidor Itaotec MX205.
The Servidor Itaotec MX205, the Servidor Itaotec LX205 and the Servidor Itaotec MX225+ are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 536

Servidor Itautec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 536

Servidor Itaotec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 536

Servidor Itautec MX205 (Intel Xeon E5-2650)

SPECint_rate_base2006 = 512

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2011
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 12:52:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 October 2012.