



SPEC® CFP2006 Result

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HITACHI

SPECfp®2006 = **58.5**

BladeSymphony BS520H (Intel Xeon E5-2609)

SPECfp_base2006 = **56.5**

CPU2006 license: 35

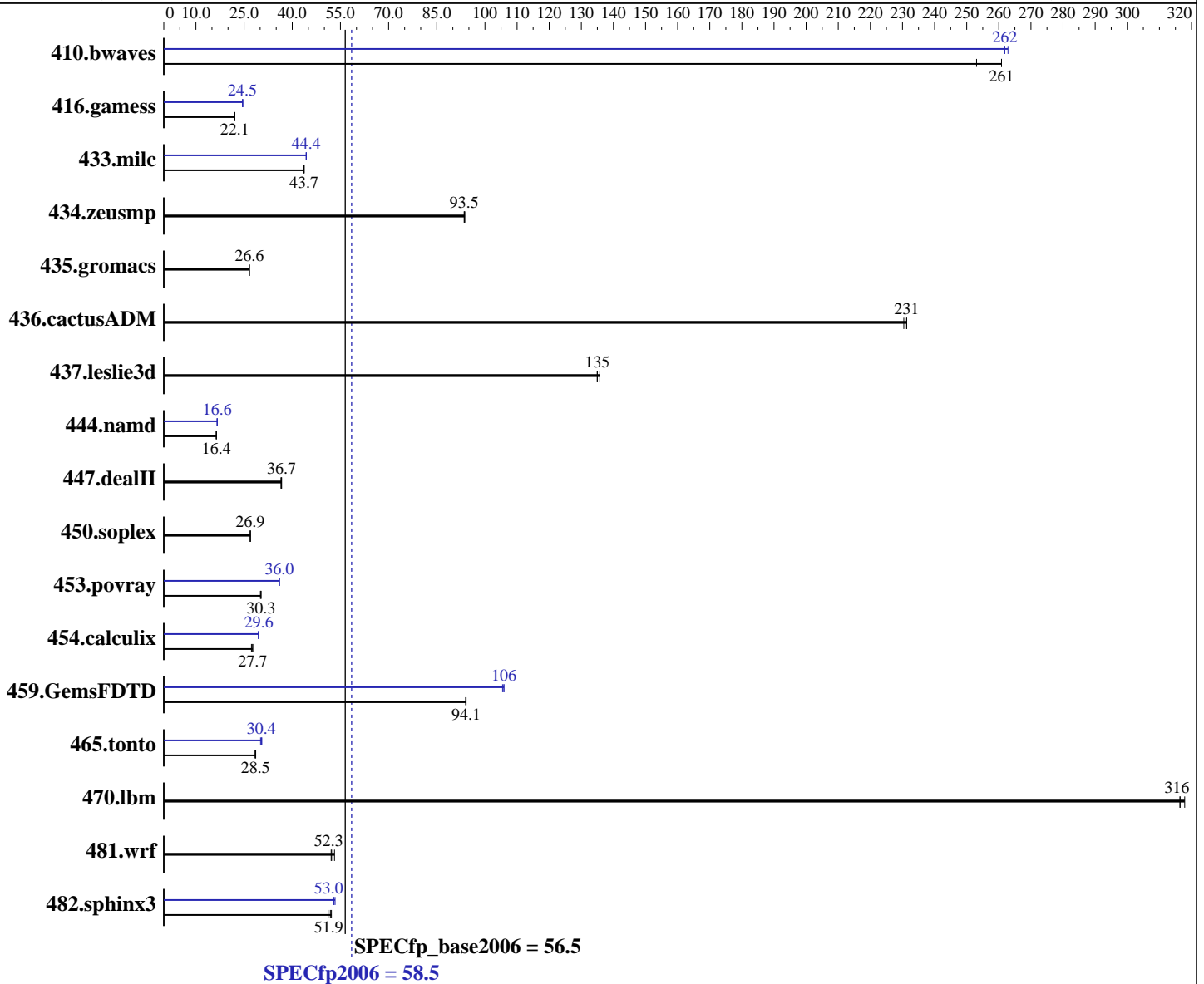
Test date: Aug-2012

Test sponsor: HITACHI

Hardware Availability: Aug-2012

Tested by: HITACHI

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2609
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.4.2.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 53.7 | 253 | 52.1 | 261 | 52.1 | 261 | 51.9 | 262 | 51.7 | 263 | 51.9 | 262 |
| 416.gamess | 888 | 22.1 | 886 | 22.1 | 887 | 22.1 | 798 | 24.5 | 798 | 24.5 | 796 | 24.6 |
| 433.milc | 210 | 43.6 | 210 | 43.7 | 210 | 43.7 | 207 | 44.3 | 207 | 44.4 | 207 | 44.4 |
| 434.zeusmp | 97.3 | 93.5 | 97.1 | 93.7 | 97.3 | 93.5 | 97.3 | 93.5 | 97.1 | 93.7 | 97.3 | 93.5 |
| 435.gromacs | 268 | 26.6 | 268 | 26.6 | 268 | 26.6 | 268 | 26.6 | 268 | 26.6 | 268 | 26.6 |
| 436.cactusADM | 51.7 | 231 | 51.9 | 230 | 51.7 | 231 | 51.7 | 231 | 51.9 | 230 | 51.7 | 231 |
| 437.leslie3d | 69.6 | 135 | 69.6 | 135 | 69.2 | 136 | 69.6 | 135 | 69.6 | 135 | 69.2 | 136 |
| 444.namd | 490 | 16.4 | 490 | 16.4 | 490 | 16.4 | 483 | 16.6 | 482 | 16.6 | 482 | 16.6 |
| 447.dealII | 312 | 36.7 | 312 | 36.7 | 314 | 36.5 | 312 | 36.7 | 312 | 36.7 | 314 | 36.5 |
| 450.soplex | 309 | 27.0 | 310 | 26.9 | 310 | 26.9 | 309 | 27.0 | 310 | 26.9 | 310 | 26.9 |
| 453.povray | 176 | 30.3 | 177 | 30.1 | 176 | 30.3 | 148 | 36.0 | 148 | 35.9 | 148 | 36.0 |
| 454.calculix | 302 | 27.3 | 298 | 27.7 | 298 | 27.7 | 278 | 29.6 | 278 | 29.6 | 281 | 29.4 |
| 459.GemsFDTD | 113 | 94.1 | 113 | 94.0 | 113 | 94.1 | 101 | 105 | 100 | 106 | 100 | 106 |
| 465.tonto | 344 | 28.6 | 345 | 28.5 | 346 | 28.4 | 327 | 30.1 | 323 | 30.4 | 322 | 30.5 |
| 470.lbm | 43.2 | 318 | 43.4 | 316 | 43.4 | 316 | 43.2 | 318 | 43.4 | 316 | 43.4 | 316 |
| 481.wrf | 210 | 53.2 | 214 | 52.3 | 214 | 52.1 | 210 | 53.2 | 214 | 52.3 | 214 | 52.1 |
| 482.sphinx3 | 373 | 52.2 | 376 | 51.9 | 381 | 51.2 | 365 | 53.3 | 368 | 52.9 | 368 | 53.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sat Aug 4 02:42:15 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
  2 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
cache size : 10240 KB

```

```

From /proc/meminfo
MemTotal:      132140028 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-220.4.2.el6.x86_64 #1 SMP Mon Feb 6
16:39:28 EST 2012 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 4 02:41

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "8"

```

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled

```

HITACHI BladeSymphony BS520H and HITACHI Compute Blade 520H are electronically equivalent. The results have been measured on a HITACHI BladeSymphony BS520H.



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>

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