



# SPEC<sup>®</sup> CINT2006 Result

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Itautec

SPECint<sup>®</sup>\_rate2006 = 309

Servidor Itautec MX205 (Intel Xeon E5-2665)

SPECint\_rate\_base2006 = 296

CPU2006 license: 9001

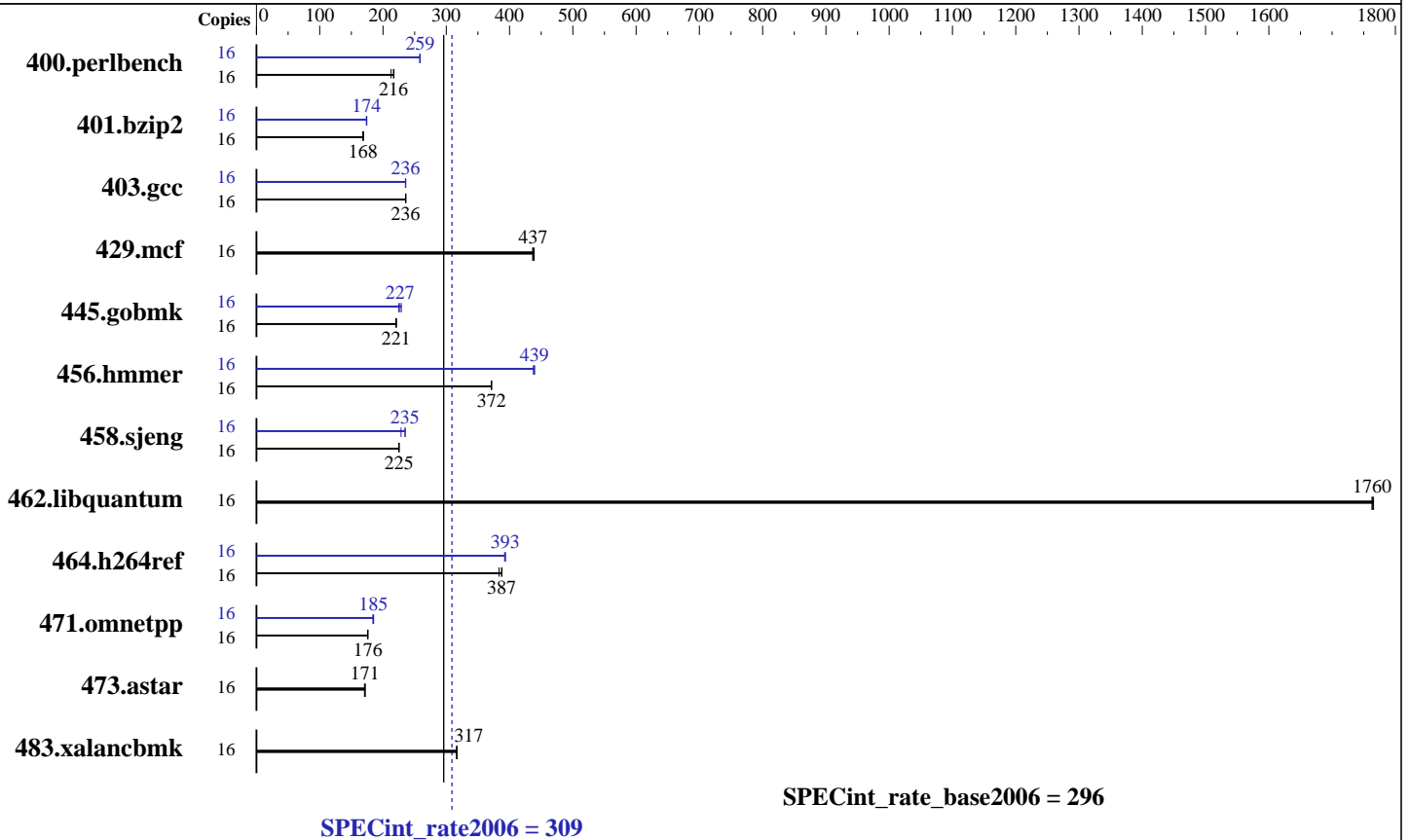
Test date: Dec-2011

Test sponsor: Itautec

Hardware Availability: Jun-2012

Tested by: Itautec

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon E5-2665  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (8 x 4 GB 1Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	719	217	735	213	<u>722</u>	<u>216</u>	16	606	258	<u>605</u>	<u>259</u>	604	259
401.bzip2	16	918	168	<u>917</u>	<u>168</u>	911	169	16	<u>887</u>	<u>174</u>	885	174	889	174
403.gcc	16	545	236	547	236	<u>546</u>	<u>236</u>	16	<u>546</u>	<u>236</u>	546	236	547	235
429.mcf	16	334	436	<u>334</u>	<u>437</u>	333	439	16	334	436	<u>334</u>	<u>437</u>	333	439
445.gobmk	16	<u>761</u>	<u>221</u>	759	221	761	221	16	<u>740</u>	<u>227</u>	747	225	733	229
456.hammer	16	402	371	402	372	<u>402</u>	<u>372</u>	16	340	439	341	438	<u>340</u>	<u>439</u>
458.sjeng	16	859	225	<u>859</u>	<u>225</u>	860	225	16	823	235	847	228	<u>825</u>	<u>235</u>
462.libquantum	16	188	1760	<u>188</u>	<u>1760</u>	188	1770	16	188	1760	<u>188</u>	<u>1760</u>	188	1770
464.h264ref	16	913	388	924	383	<u>914</u>	<u>387</u>	16	<u>900</u>	<u>393</u>	903	392	900	393
471.omnetpp	16	569	176	<u>568</u>	<u>176</u>	568	176	16	<u>541</u>	<u>185</u>	544	184	540	185
473.astar	16	<u>655</u>	<u>171</u>	658	171	653	172	16	<u>655</u>	<u>171</u>	658	171	653	172
483.xalancbmk	16	<u>349</u>	<u>317</u>	349	316	349	317	16	<u>349</u>	<u>317</u>	349	316	349	317

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on itaotecfam5 Sun Dec 4 17:23:56 2011

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Genuine Intel(R) CPU @ 2.40GHz  
1 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The  
following excerpts from /proc/cpuinfo might not be reliable. Use with  
caution.)  
cpu cores : 8  
siblings : 16

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5 6 7  
cache size : 20480 KB

From /proc/meminfo  
MemTotal: 32851432 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux itaotecfam5 2.6.32-220.el6.x86\_64 #1 SMP Wed Nov 9 08:03:13 EST 2011  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Dec 4 17:21

SPEC is set to: /home/rcaneca/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/vg\_itaotecfam5-lv\_home  
ext4 280G 4.5G 262G 2% /home

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itaotec MX205.  
The Servidor Itaotec MX205, the Servidor Itaotec MX225+ and the Servidor Itaotec LX205 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 10:31:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 11 September 2012.