



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint[®]_rate2006 = 235

Servidor Itaotec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001

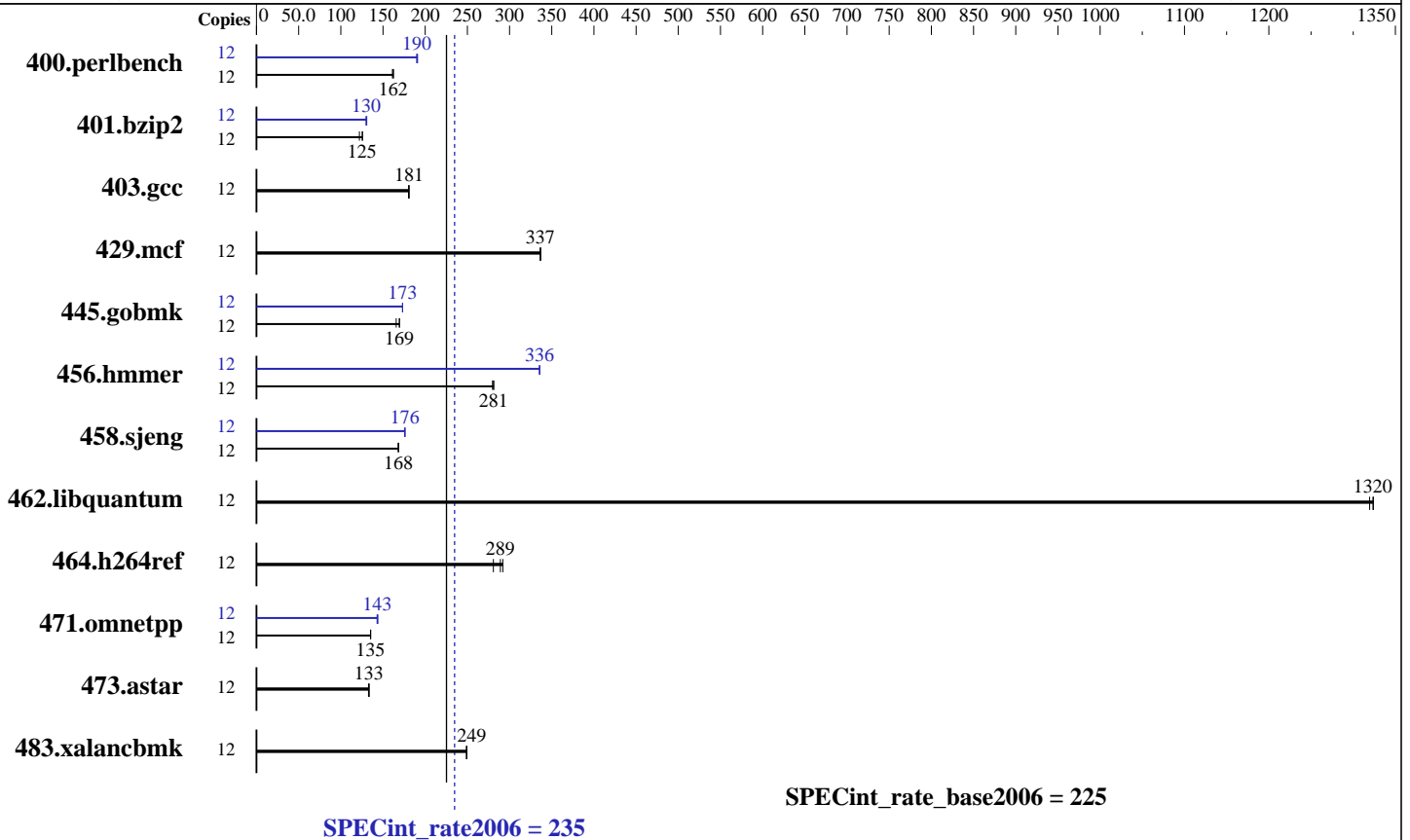
Test date: Aug-2012

Test sponsor: Itaotec

Hardware Availability: Jun-2012

Tested by: Itaotec

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2640
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 235

Servidor Itaotec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Aug-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	727	161	<u>723</u>	<u>162</u>	723	162	12	617	190	614	191	<u>616</u>	<u>190</u>
401.bzip2	12	921	126	949	122	<u>924</u>	<u>125</u>	12	<u>890</u>	<u>130</u>	891	130	886	131
403.gcc	12	536	180	534	181	<u>534</u>	<u>181</u>	12	536	180	534	181	<u>534</u>	<u>181</u>
429.mcf	12	<u>325</u>	<u>337</u>	325	336	325	337	12	<u>325</u>	<u>337</u>	325	336	325	337
445.gobmk	12	<u>743</u>	<u>169</u>	743	169	760	166	12	<u>727</u>	<u>173</u>	727	173	727	173
456.hammer	12	398	281	<u>398</u>	<u>281</u>	400	280	12	334	335	334	336	<u>334</u>	<u>336</u>
458.sjeng	12	862	168	864	168	<u>863</u>	<u>168</u>	12	826	176	<u>825</u>	<u>176</u>	825	176
462.libquantum	12	188	1320	<u>188</u>	<u>1320</u>	188	1320	12	188	1320	<u>188</u>	<u>1320</u>	188	1320
464.h264ref	12	909	292	<u>918</u>	<u>289</u>	946	281	12	909	292	<u>918</u>	<u>289</u>	946	281
471.omnetpp	12	555	135	<u>554</u>	<u>135</u>	554	135	12	<u>523</u>	<u>143</u>	525	143	521	144
473.astar	12	<u>631</u>	<u>133</u>	631	133	634	133	12	<u>631</u>	<u>133</u>	631	133	634	133
483.xalancbmk	12	332	249	333	249	<u>332</u>	<u>249</u>	12	332	249	333	249	<u>332</u>	<u>249</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on localhost Mon Aug 20 09:21:49 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 0 @ 2.50GHz
1 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 235

Servidor Itautec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Aug-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal: 32851040 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 20 09:20

SPEC is set to: /home/rcaneca/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/vg_it5rh-lv_home
ext4 193G 1.9G 181G 2% /home

(End of data from sysinfo program)

General Notes

This result was measured on the Servidor Itautec MX225.
The Servidor Itautec MX215 and the Servidor Itautec MX225
are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 235

Servidor Itaotec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Aug-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 235

Servidor Itaotec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Aug-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 235

Servidor Itaotec MX225 (Intel Xeon E5-2640)

SPECint_rate_base2006 = 225

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Aug-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 10:26:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 September 2012.