



# SPEC® CINT2006 Result

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Itaotec

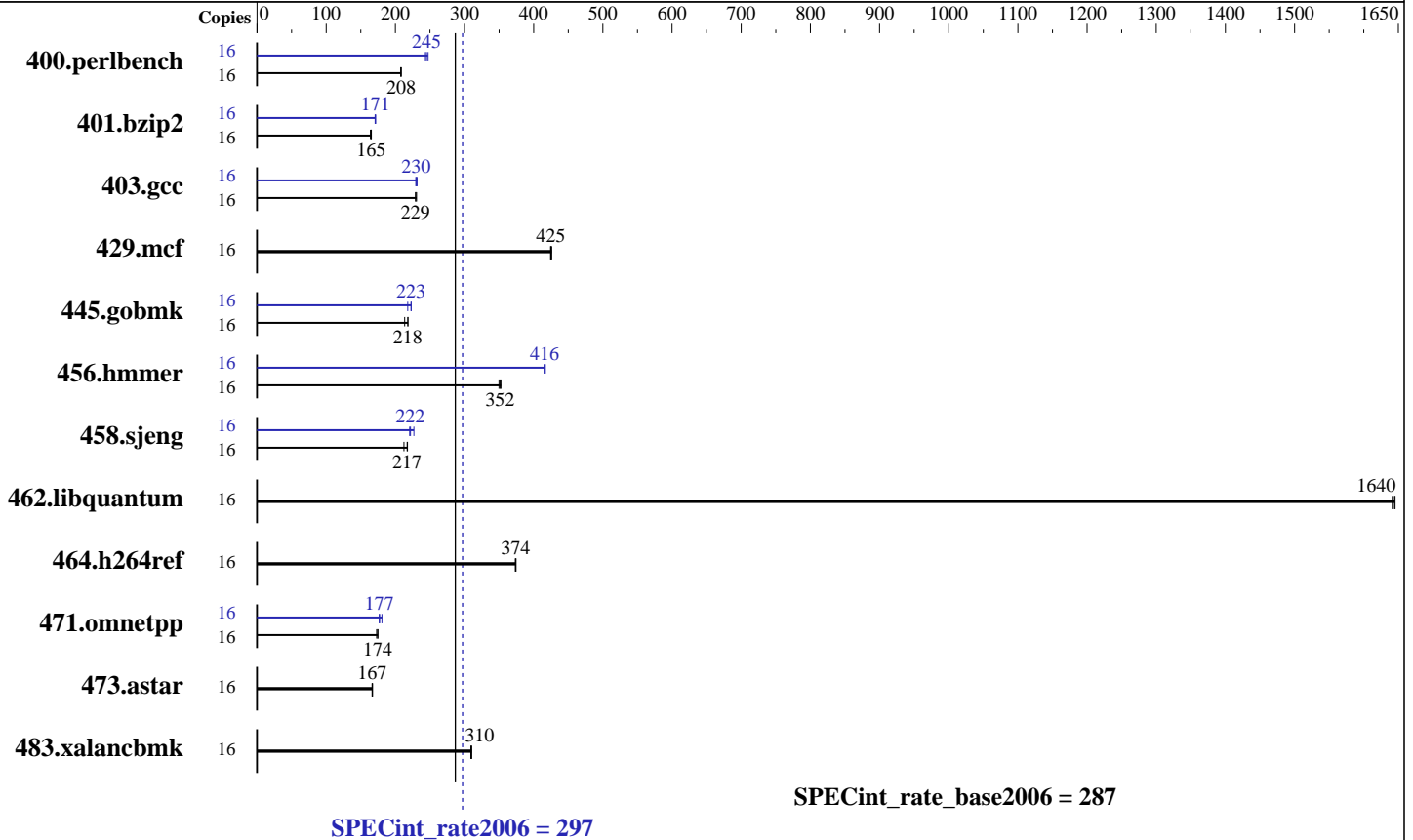
SPECint®\_rate2006 = 297

Servidor Itaotec MX215 (Intel Xeon E5-2660)

SPECint\_rate\_base2006 = 287

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Aug-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon E5-2660  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (8 x 4 GB 1Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 3 x 300 GB, SAS, 15000 RPM, RAID 0  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	753	208	<b><u>750</u></b>	<b><u>208</u></b>	750	208	16	632	247	642	243	<b><u>638</u></b>	<b><u>245</u></b>
401.bzip2	16	942	164	934	165	<b><u>935</u></b>	<b><u>165</u></b>	16	<b><u>902</u></b>	<b><u>171</u></b>	901	171	905	171
403.gcc	16	562	229	559	231	<b><u>562</u></b>	<b><u>229</u></b>	16	<b><u>559</u></b>	<b><u>230</u></b>	561	230	557	231
429.mcf	16	343	426	344	424	<b><u>344</u></b>	<b><u>425</u></b>	16	343	426	344	424	<b><u>344</u></b>	<b><u>425</u></b>
445.gobmk	16	786	213	769	218	<b><u>770</u></b>	<b><u>218</u></b>	16	770	218	<b><u>754</u></b>	<b><u>223</u></b>	753	223
456.hammer	16	427	350	423	353	<b><u>425</u></b>	<b><u>352</u></b>	16	358	417	<b><u>359</u></b>	<b><u>416</u></b>	360	415
458.sjeng	16	912	212	<b><u>892</u></b>	<b><u>217</u></b>	890	218	16	<b><u>873</u></b>	<b><u>222</u></b>	854	227	877	221
462.libquantum	16	202	1640	<b><u>202</u></b>	<b><u>1640</u></b>	202	1650	16	202	1640	<b><u>202</u></b>	<b><u>1640</u></b>	202	1650
464.h264ref	16	948	374	<b><u>947</u></b>	<b><u>374</u></b>	946	374	16	948	374	<b><u>947</u></b>	<b><u>374</u></b>	946	374
471.omnetpp	16	578	173	573	175	<b><u>573</u></b>	<b><u>174</u></b>	16	566	177	554	181	<b><u>564</u></b>	<b><u>177</u></b>
473.astar	16	675	166	<b><u>674</u></b>	<b><u>167</u></b>	673	167	16	675	166	<b><u>674</u></b>	<b><u>167</u></b>	673	167
483.xalancbmk	16	357	309	356	310	<b><u>356</u></b>	<b><u>310</u></b>	16	357	309	356	310	<b><u>356</u></b>	<b><u>310</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on itaotec.fam5 Fri Aug 19 09:31:40 2011

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Genuine Intel(R) CPU @ 2.20GHz  
1 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The  
following excerpts from /proc/cpuinfo might not be reliable. Use with  
caution.)  
cpu cores : 8  
siblings : 16

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5 6 7  
cache size : 20480 KB

From /proc/meminfo  
MemTotal: 32824916 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux itaotec.fam5 2.6.32-220.el6.x86\_64 #1 SMP Wed Nov 9 08:03:13 EST 2011  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 17 17:57

SPEC is set to: /home/rcaneca/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/vg\_itaotec-lv\_home  
ext4 710G 1.9G 672G 1% /home

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itaotec MX225.  
The Servidor Itaotec MX215 and the Servidor Itaotec MX225  
are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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