# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## NEC Corporation

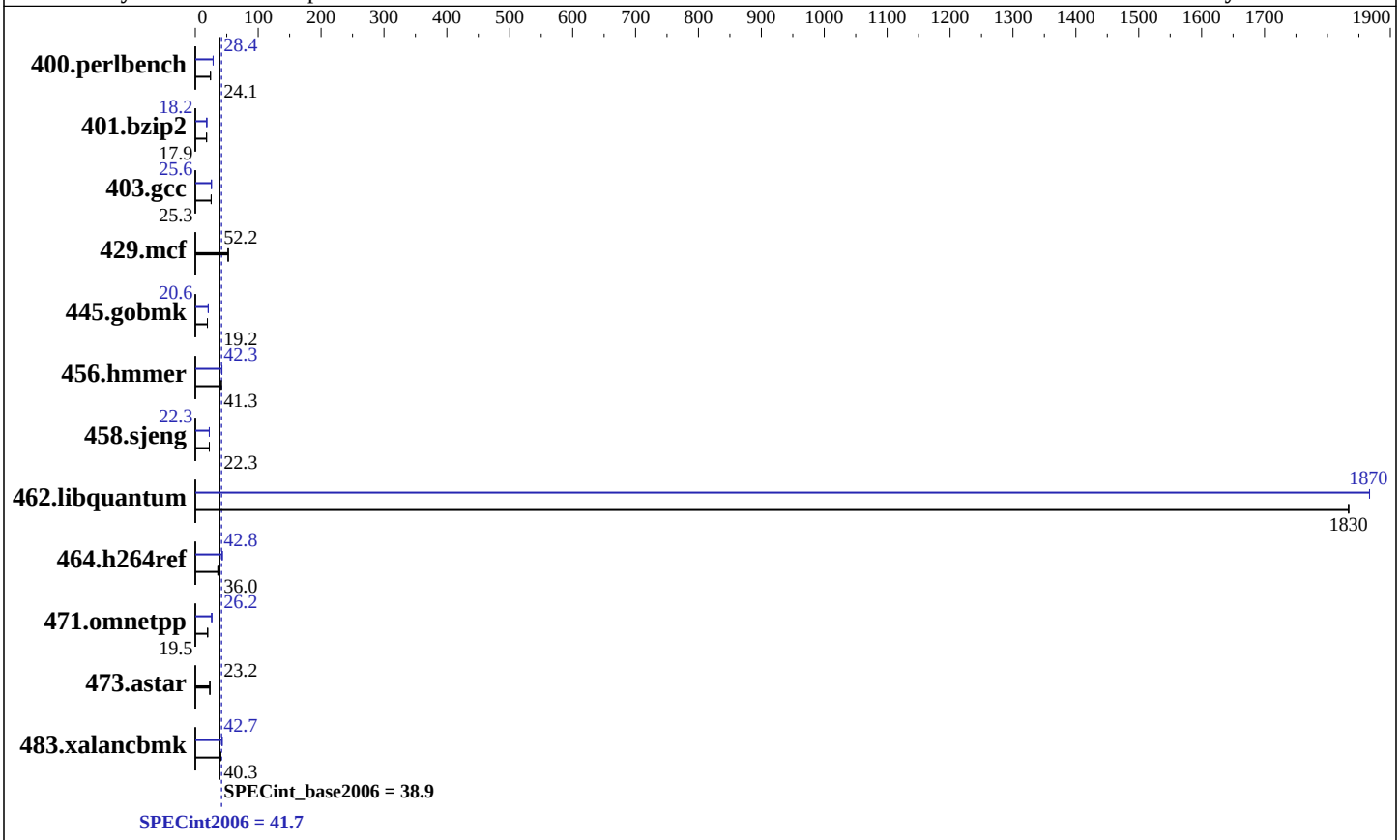
## Express5800/E120d-M (Intel Xeon E5-2430)

SPECint®2006 = 41.7

SPECint_base2006 = 38.9

**CPU2006 license:** 9006
**Test sponsor:** NEC Corporation
**Tested by:** NEC Corporation

**Test date:** Jul-2012
**Hardware Availability:** Jul-2012
**Software Availability:** Feb-2012




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2430 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.70 GHz |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9) |
| Disk Subsystem: | 1 x 250 GB SATA, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>Kernel 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.3.293 of Intel C++ Studio XE for Linux; |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## NEC Corporation

Express5800/E120d-M (Intel Xeon E5-2430)

SPECint2006 = 41.7

SPECint_base2006 = 38.9

**CPU2006 license:** 9006
**Test sponsor:** NEC Corporation
**Tested by:** NEC Corporation

**Test date:** Jul-2012
**Hardware Availability:** Jul-2012
**Software Availability:** Feb-2012

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 406 | 24.1 | **406** | **24.1** | 406 | 24.0 | 344 | 28.4 | **344** | **28.4** | 344 | 28.4 |
| 401.bzip2 | 541 | 17.8 | 540 | 17.9 | **540** | **17.9** | 530 | 18.2 | **530** | **18.2** | 530 | 18.2 |
| 403.gcc | 318 | 25.3 | **318** | **25.3** | 319 | 25.3 | **314** | **25.6** | 314 | 25.6 | 314 | 25.6 |
| 429.mcf | **175** | **52.2** | 175 | 52.0 | 173 | 52.6 | **175** | **52.2** | 175 | 52.0 | 173 | 52.6 |
| 445.gobmk | 545 | 19.3 | **545** | **19.2** | 546 | 19.2 | 509 | 20.6 | 509 | 20.6 | **509** | **20.6** |
| 456.hmmer | 226 | 41.3 | 226 | 41.3 | **226** | **41.3** | **220** | **42.3** | 221 | 42.2 | 220 | 42.4 |
| 458.sjeng | 543 | 22.3 | **543** | **22.3** | 544 | 22.3 | 544 | 22.2 | 543 | 22.3 | **544** | **22.3** |
| 462.libquantum | **11.3** | **1830** | 11.3 | 1830 | 11.3 | 1830 | 11.1 | 1870 | **11.1** | **1870** | 11.1 | 1870 |
| 464.h264ref | 615 | 36.0 | 614 | 36.0 | **615** | **36.0** | 516 | 42.8 | 519 | 42.7 | **517** | **42.8** |
| 471.omnetpp | 319 | 19.6 | 320 | 19.5 | **320** | **19.5** | 240 | 26.0 | 238 | 26.3 | **238** | **26.2** |
| 473.astar | **303** | **23.2** | 302 | 23.2 | 303 | 23.1 | **303** | **23.2** | 302 | 23.2 | 303 | 23.1 |
| 483.xalancbmk | 171 | 40.3 | **171** | **40.3** | 171 | 40.3 | 163 | 42.4 | **162** | **42.7** | 162 | 42.7 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Settings:
 Energy Performance: Performance
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "12"

 Added glibc-static-2.12-1.47.el6.x86_64.rpm
 to enable static linking

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

Continued on next page

## Peak Compiler Invocation (Continued)

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-unroll4`

Continued on next page

## Peak Optimization Flags (Continued)

462.libquantum: `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 11:04:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 August 2012.