SPEC® CINT2006 Result
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Dell Inc.

PowerEdge M820 (Intel Xeon E5-4603, 2.00 GHz)

$SPECint®2006 =$ 30.8

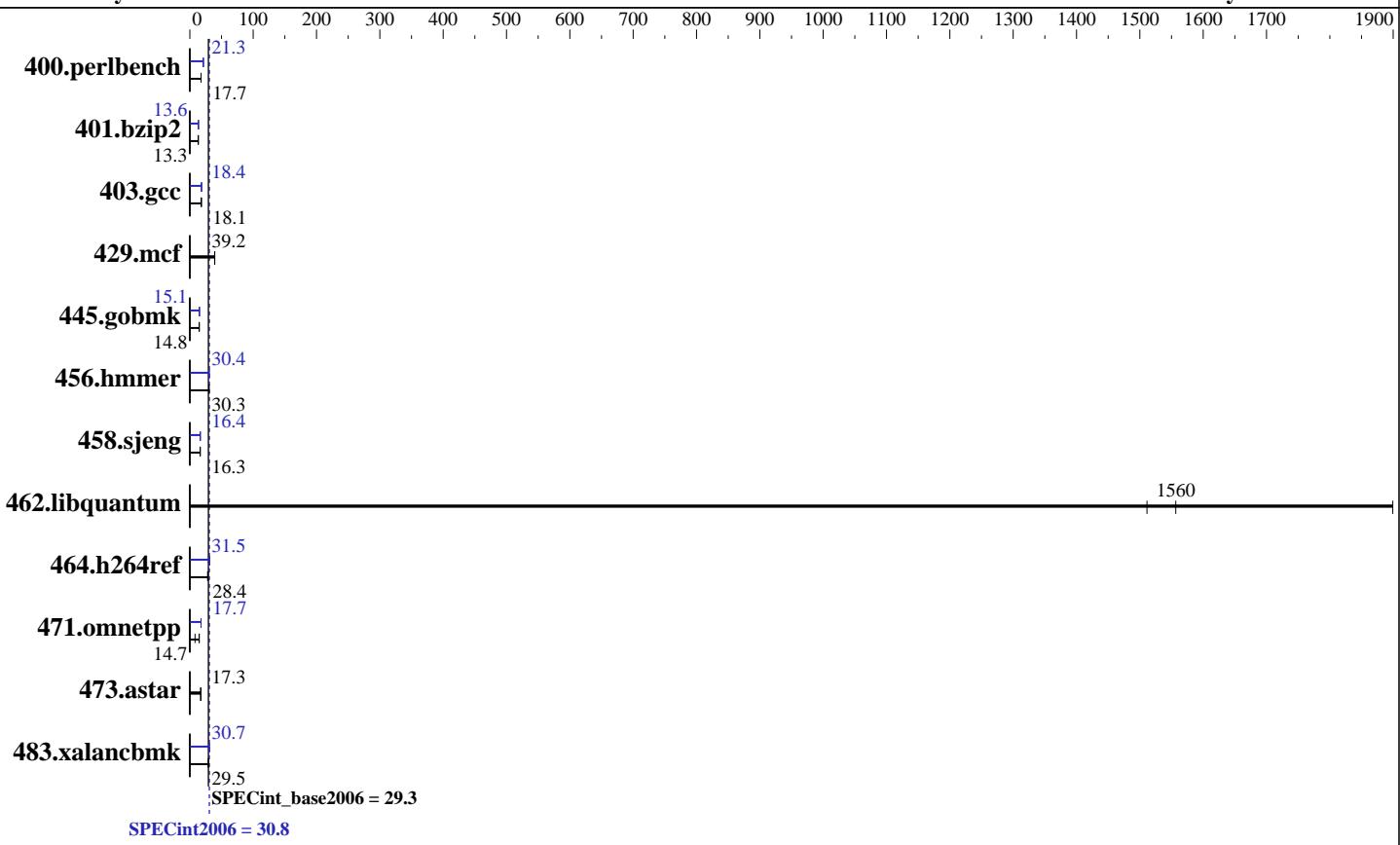$SPECint\_base2006 =$ 29.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2012
Hardware Availability: Aug-2012
Software Availability: Feb-2012

400.perlbench 21.3 / 17.7
401.bzip2 13.6 / 13.3
403.gcc 18.4 / 18.1
429.mcf 39.2
445.gobmk 15.1 / 14.8
456.hmmer 30.4 / 30.3
458.sjeng 16.4 / 16.3
462.libquantum 1560
464.h264ref 31.5 / 28.4
471.omnetpp 17.7 / 14.7
473.astar 17.3
483.xalancbmk 30.7 / 29.5

SPECint_base2006 = 29.3

SPECint2006 = 30.8

Hardware

CPU Name:	Intel Xeon E5-4603
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1067 MHz)
Disk Subsystem:	1 x 900 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	552	17.7	**553**	**17.7**	554	17.6	460	21.2	459	21.3	**460**	**21.3**
401.bzip2	**727**	**13.3**	727	13.3	726	13.3	712	13.5	712	13.6	**712**	**13.6**
403.gcc	447	18.0	**445**	**18.1**	444	18.1	**437**	**18.4**	438	18.4	437	18.4
429.mcf	233	39.2	**233**	**39.2**	232	39.4	233	39.2	**233**	**39.2**	232	39.4
445.gobmk	713	14.7	**711**	**14.8**	707	14.8	**694**	**15.1**	695	15.1	694	15.1
456.hmmer	308	30.3	**308**	**30.3**	310	30.1	307	30.4	306	30.5	**306**	**30.4**
458.sjeng	746	16.2	**740**	**16.3**	738	16.4	**736**	**16.4**	737	16.4	736	16.4
462.libquantum	13.7	1510	**13.3**	**1560**	10.9	1900	13.7	1510	**13.3**	**1560**	10.9	1900
464.h264ref	774	28.6	**780**	**28.4**	788	28.1	716	30.9	702	31.5	**703**	**31.5**
471.omnetpp	**425**	**14.7**	421	14.9	764	8.18	354	17.7	**354**	**17.7**	354	17.6
473.astar	**406**	**17.3**	406	17.3	407	17.2	**406**	**17.3**	406	17.3	407	17.2
483.xalancbmk	235	29.4	**234**	**29.5**	232	29.7	225	30.7	**225**	**30.7**	224	30.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-0j3r Mon May 14 05:53:04 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4603 0 @ 2.00GHz
      4 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
```

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Platform Notes (Continued)

```
      physical 2: cores 0 1 2 3
      physical 3: cores 0 1 2 3
   cache size : 10240 KB

 From /proc/meminfo
    MemTotal:        264501520 kB
    HugePages_Total:         0
    Hugepagesize:         2048 kB

 /usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 11 (x86_64)

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 11 (x86_64)
       VERSION = 11
       PATCHLEVEL = 2

 uname -a:
    Linux linux-0j3r 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
    (d73692b) x86_64 x86_64 x86_64 GNU/Linux

 run-level 3 May 14 05:40 last=5

 SPEC is set to: /root/CPU2006-1.2
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda1       ext3  886G  160G  682G  19% /

 Additional information from dmidecode:

 (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
 OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
```

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Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
          403.gcc: -DSPEC_CPU_LP64
          429.mcf: -DSPEC_CPU_LP64
        445.gobmk: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       464.h264ref: -DSPEC_CPU_LP64
       471.omnetpp: -DSPEC_CPU_LP64
         473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64
```

Base Other Flags

C benchmarks:
```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
  401.bzip2: -DSPEC_CPU_LP64
    403.gcc: -DSPEC_CPU_LP64
    429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
  473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch`
`-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch`
`-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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