



# SPEC® CFP2006 Result

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## NEC Corporation

SPECfp®2006 = 74.7

Express5800/R120d-1E (Intel Xeon E5-2450)

SPECfp\_base2006 = 71.5

CPU2006 license: 9006

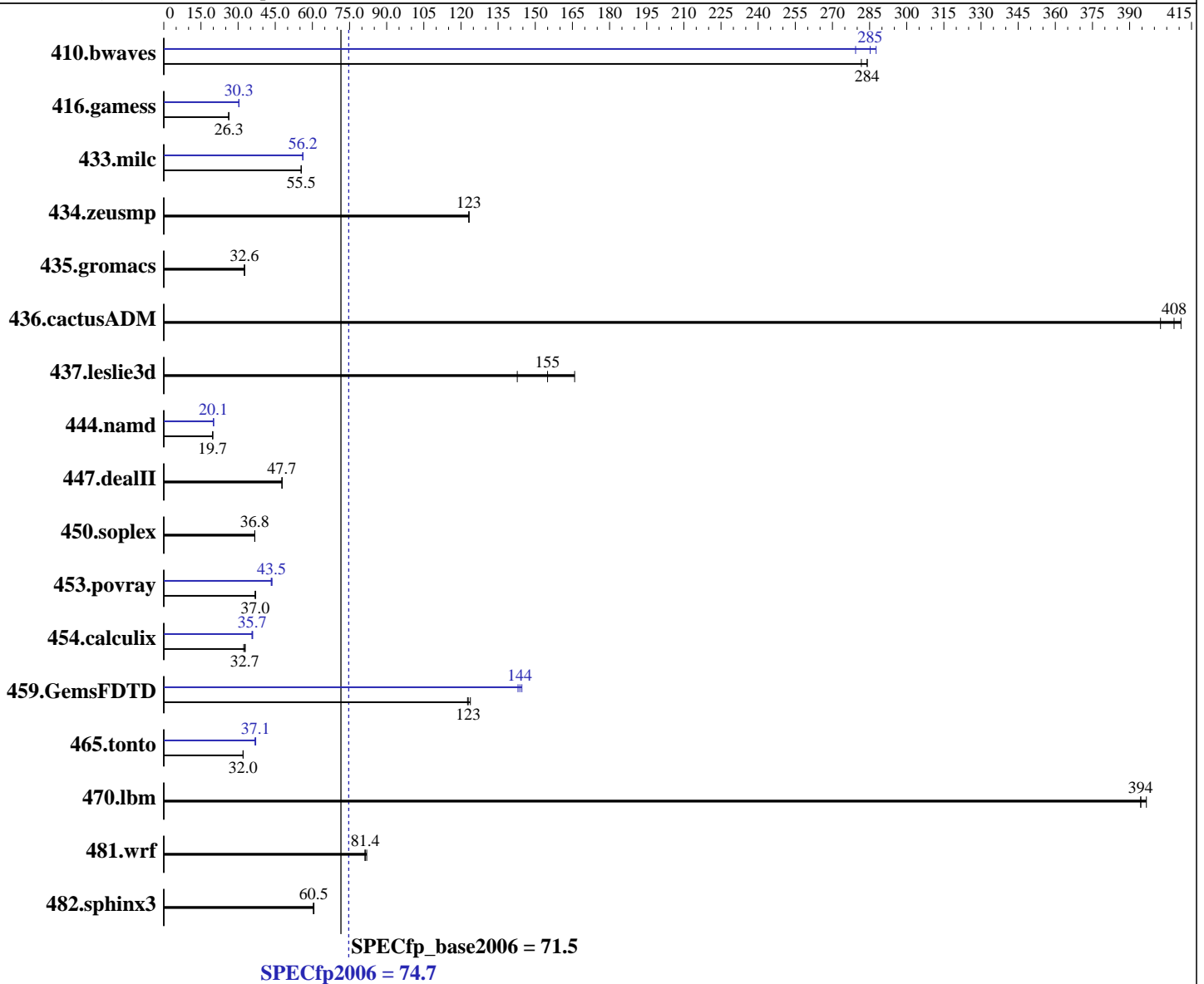
Test date: Jun-2012

Test sponsor: NEC Corporation

Hardware Availability: May-2012

Tested by: NEC Corporation

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2450  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	47.8	284	48.2	282	<b>47.8</b>	<b>284</b>	48.6	279	<b>47.6</b>	<b>285</b>	47.2	288
416.gamess	745	26.3	<b>746</b>	<b>26.3</b>	747	26.2	645	30.4	646	30.3	<b>646</b>	<b>30.3</b>
433.milc	166	55.5	<b>165</b>	<b>55.5</b>	165	55.6	<b>163</b>	<b>56.2</b>	163	56.2	164	56.1
434.zeusmp	<b>73.9</b>	<b>123</b>	73.9	123	73.9	123	<b>73.9</b>	<b>123</b>	73.9	123	73.9	123
435.gromacs	219	32.7	<b>219</b>	<b>32.6</b>	220	32.5	219	32.7	<b>219</b>	<b>32.6</b>	220	32.5
436.cactusADM	<b>29.3</b>	<b>408</b>	29.7	402	29.1	411	<b>29.3</b>	<b>408</b>	29.7	402	29.1	411
437.leslie3d	56.7	166	65.9	143	<b>60.7</b>	<b>155</b>	56.7	166	65.9	143	<b>60.7</b>	<b>155</b>
444.namd	406	19.7	406	19.7	<b>406</b>	<b>19.7</b>	<b>400</b>	<b>20.1</b>	399	20.1	400	20.1
447.dealII	<b>240</b>	<b>47.7</b>	239	47.8	240	47.7	<b>240</b>	<b>47.7</b>	239	47.8	240	47.7
450.soplex	<b>227</b>	<b>36.8</b>	227	36.8	227	36.7	<b>227</b>	<b>36.8</b>	227	36.8	227	36.7
453.povray	144	36.9	143	37.1	<b>144</b>	<b>37.0</b>	123	43.4	122	43.7	<b>122</b>	<b>43.5</b>
454.calculix	256	32.3	251	32.9	<b>252</b>	<b>32.7</b>	<b>231</b>	<b>35.7</b>	230	35.9	231	35.7
459.GemsFDTD	86.5	123	85.7	124	<b>86.3</b>	<b>123</b>	74.2	143	<b>73.8</b>	<b>144</b>	73.4	145
465.tonto	308	32.0	<b>307</b>	<b>32.0</b>	307	32.1	267	36.8	265	37.1	<b>265</b>	<b>37.1</b>
470.lbm	34.8	394	<b>34.8</b>	<b>394</b>	34.6	397	34.8	394	<b>34.8</b>	<b>394</b>	34.6	397
481.wrf	<b>137</b>	<b>81.4</b>	138	81.2	136	81.9	<b>137</b>	<b>81.4</b>	138	81.2	136	81.9
482.sphinx3	<b>322</b>	<b>60.5</b>	321	60.6	323	60.4	<b>322</b>	<b>60.5</b>	321	60.6	323	60.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Energy Performance: Performance  
 Memory Voltage: 1.5 V

## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,compact,1,0"  
 LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

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## General Notes (Continued)

OMP\_NUM\_THREADS = "16"

The Express5800/R120d-1E and the Express5800/R120d-2E models are electronically equivalent. The results have been measured on the Express5800/R120d-1E model.

Added glibc-static-2.12-1.47.el6.x86\_64.rpm to enable static linking

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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