SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX270 S1, Intel Xeon E5-2620, 2.0 GHz

SPECint®_rate2006 = 393

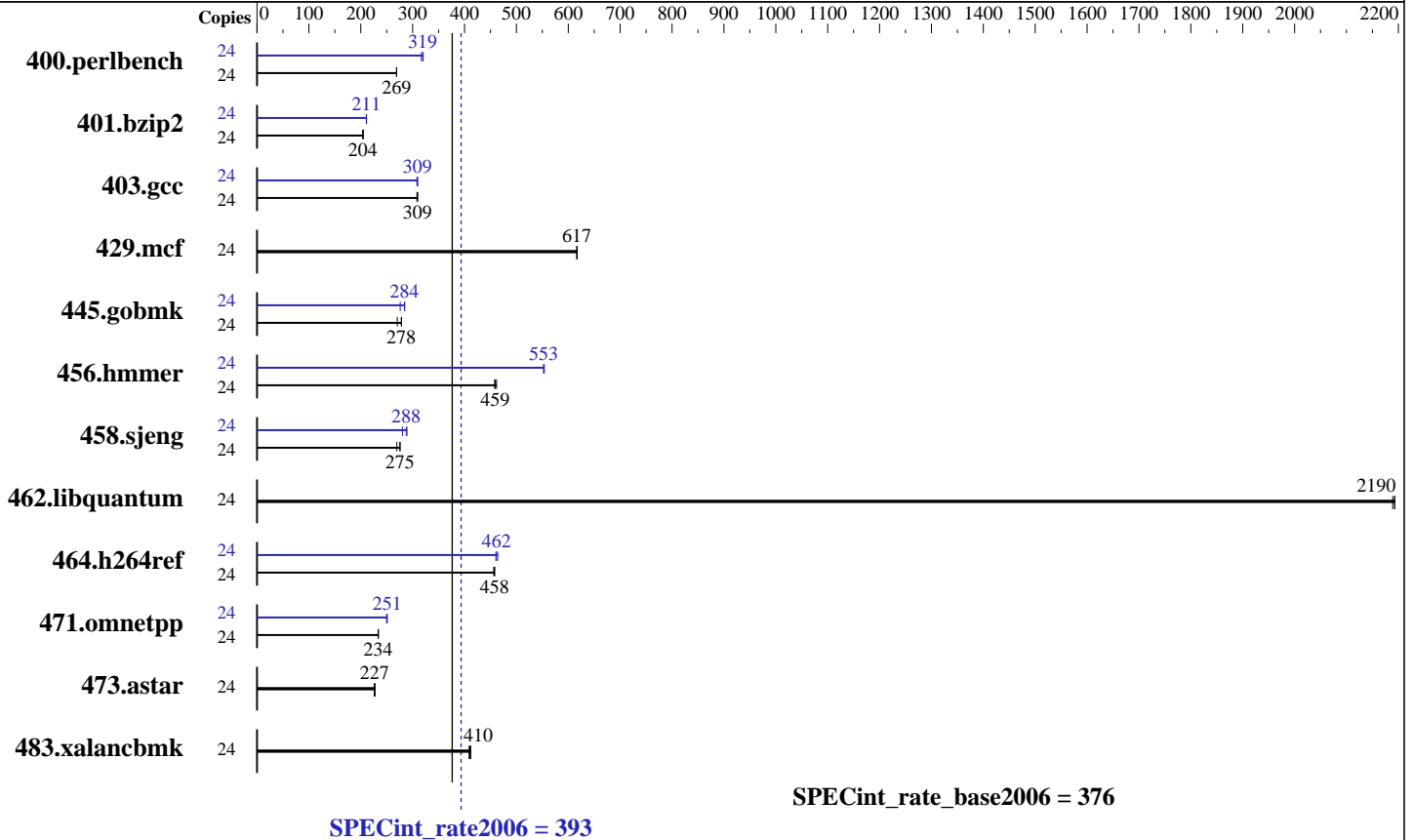SPECint_rate_base2006 = 376

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

SPECint_rate_base2006 = 376

SPECint_rate2006 = 393

Hardware

CPU Name: Intel Xeon E5-2620
CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX270 S1, Intel Xeon E5-2620, 2.0 GHz

SPECint_rate2006 = 393

SPECint_rate_base2006 = 376

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	871	269	**871**	**269**	872	269	24	740	317	**734**	**319**	732	320
401.bzip2	24	1131	205	**1134**	**204**	1134	204	24	1097	211	1099	211	**1098**	**211**
403.gcc	24	**625**	**309**	627	308	624	310	24	**624**	**309**	627	308	624	310
429.mcf	24	**355**	**617**	355	616	355	617	24	**355**	**617**	355	616	355	617
445.gobmk	24	931	270	**905**	**278**	904	278	24	**885**	**284**	885	285	912	276
456.hmmer	24	489	458	**487**	**459**	485	462	24	405	552	**405**	**553**	404	554
458.sjeng	24	1053	276	**1054**	**275**	1077	270	24	1005	289	**1009**	**288**	1036	280
462.libquantum	24	227	2190	**227**	**2190**	227	2190	24	227	2190	**227**	**2190**	227	2190
464.h264ref	24	1159	458	1164	456	**1161**	**458**	24	**1151**	**462**	1144	464	1154	460
471.omnetpp	24	642	234	642	234	**642**	**234**	24	601	250	**598**	**251**	598	251
473.astar	24	**743**	**227**	745	226	741	227	24	**743**	**227**	745	226	741	227
483.xalancbmk	24	402	412	405	409	**404**	**410**	24	402	412	405	409	**404**	**410**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

   Stack size set to unlimited using "ulimit -s unlimited"
   Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
   runspec command invoked through numactl i.e.:
   numactl --interleave=all runspec <etc>

General Notes

   Environment variables set by runspec before the start of the run:
   LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

   Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
   This result was measured on the PRIMERGY CX250 S1. The PRIMERGY CX250 S1 and the PRIMERGY CX270 S1 are electronically equivalent.
   For information about Fujitsu please visit: http://www.fujitsu.com

SPEC CINT2006 Result

Fujitsu

PRIMERGY CX270 S1, Intel Xeon E5-2620, 2.0 GHz

SPECint_rate2006 =   393

SPECint_rate_base2006 = 376

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

        401.bzip2: icc -m64

      456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

Fujitsu

PRIMERGY CX270 S1, Intel Xeon E5-2620, 2.0 GHz

SPECint_rate2006 =　　393

SPECint_rate_base2006 =　376

CPU2006 license: 19		**Test date:**	Mar-2012
Test sponsor: Fujitsu		**Hardware Availability:**	Jun-2012
Tested by: Fujitsu		**Software Availability:**	Dec-2011

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

      429.mcf: basepeak = yes

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap

     473.astar: basepeak = yes
```

Continued on next page

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX270 S1, Intel Xeon E5-2620, 2.0 GHz

SPECint_rate2006 =　393

SPECint_rate_base2006 = 376

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 12:32:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 July 2012.