



# SPEC® CINT2006 Result

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## IBM Corporation

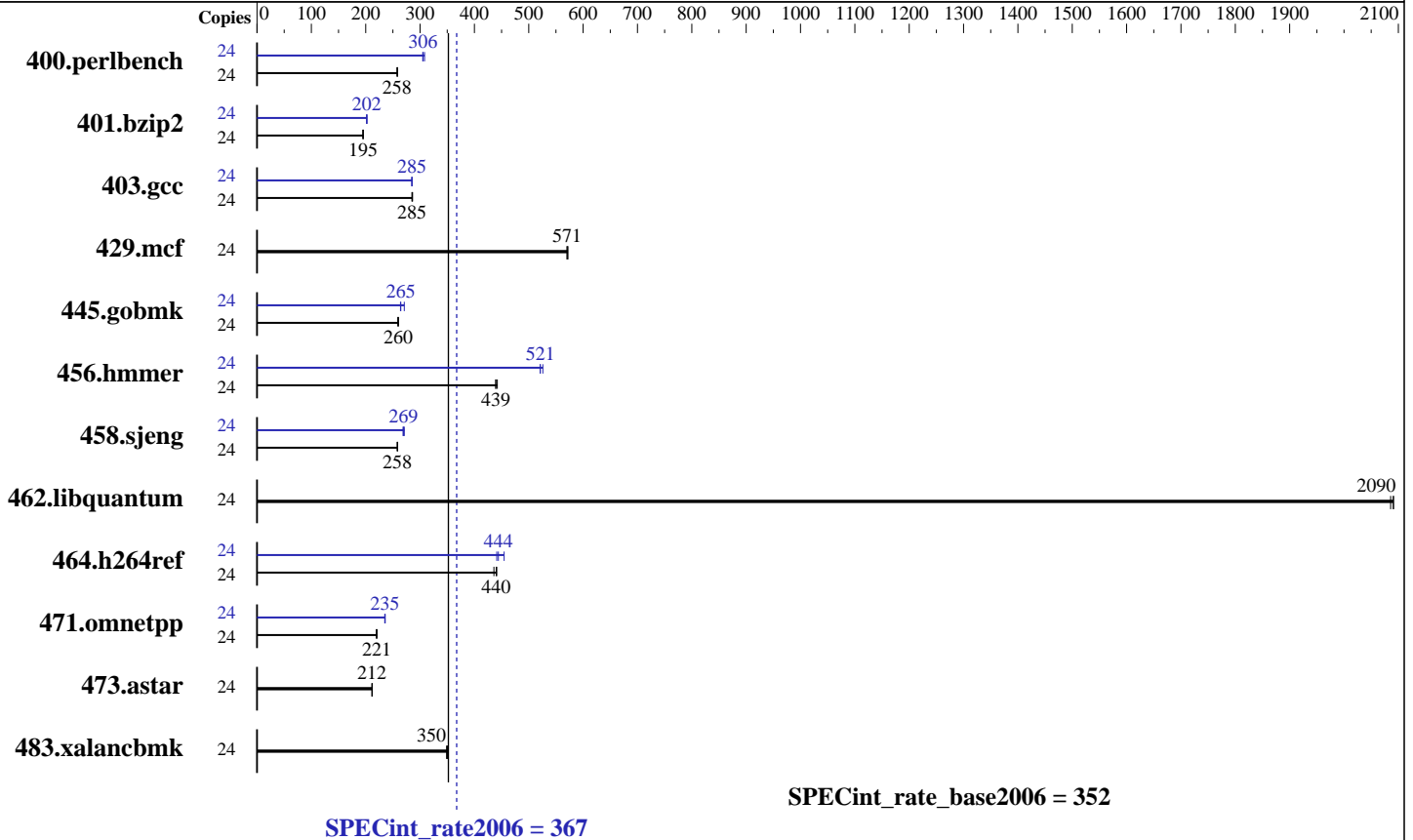
IBM BladeCenter HS23E  
(Intel Xeon E5-2420, 1.90 GHz)

SPECint®\_rate2006 = 367

SPECint\_rate\_base2006 = 352

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Jun-2012  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2420  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 1900  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	24	910	258	<u>909</u>	<u>258</u>	906	259	24	761	308	<u>767</u>	<u>306</u>	769	305		
401.bzip2	24	1185	195	<u>1185</u>	<u>195</u>	1190	195	24	1144	202	1148	202	<u>1147</u>	<u>202</u>		
403.gcc	24	675	286	677	285	<u>677</u>	<u>285</u>	24	678	285	<u>677</u>	<u>285</u>	677	285		
429.mcf	24	<u>383</u>	<u>571</u>	383	572	384	570	24	<u>383</u>	<u>571</u>	383	572	384	570		
445.gobmk	24	<u>970</u>	<u>260</u>	967	260	972	259	24	<u>952</u>	<u>265</u>	954	264	929	271		
456.hammer	24	510	439	<u>510</u>	<u>439</u>	506	442	24	430	521	426	526	<u>430</u>	<u>521</u>		
458.sjeng	24	<u>1124</u>	<u>258</u>	1122	259	1127	258	24	1081	269	1072	271	<u>1078</u>	<u>269</u>		
462.libquantum	24	<u>238</u>	<u>2090</u>	238	2090	238	2090	24	<u>238</u>	<u>2090</u>	238	2090	238	2090		
464.h264ref	24	1219	436	<u>1207</u>	<u>440</u>	1204	441	24	<u>1196</u>	<u>444</u>	1168	455	1204	441		
471.omnetpp	24	680	221	<u>680</u>	<u>221</u>	684	219	24	636	236	638	235	<u>637</u>	<u>235</u>		
473.astar	24	795	212	796	212	<u>796</u>	<u>212</u>	24	795	212	796	212	<u>796</u>	<u>212</u>		
483.xalancbmk	24	<u>473</u>	<u>350</u>	475	349	473	350	24	<u>473</u>	<u>350</u>	475	349	473	350		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on angleshark-pete Sat Jun 2 20:39:01 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2420 0 @ 1.90GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6

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### Platform Notes (Continued)

```
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:          99042356 kB
HugePages_Total:   0
Hugepagesize:      2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux angleshark-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST
2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 2 19:42
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_angledsharkpet-lv_root
ext4            265G     36G  217G  14% /
```

```
Additional information from dmidecode:
Memory:
6x Micron 36JDYS1G72PZ-1G6M1 8 GB 1600 MHz 2 rank
6x Samsung M392B1K70DM0-CK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64  
  
456.hmmer: icc -m64  
  
458.sjeng: icc -m64  
  
C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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