SPEC® CINT2006 Result

Hewlett-Packard Company

ProLiant BL660c Gen8
(2.70 GHz, Intel Xeon E5-4650)

SPECint®_rate2006 =　　1230

SPECint_rate_base2006 =　1180

CPU2006 license: 3　　　　　　　　　　　　　　　　　　　　　　　**Test date:**　　　　May-2012
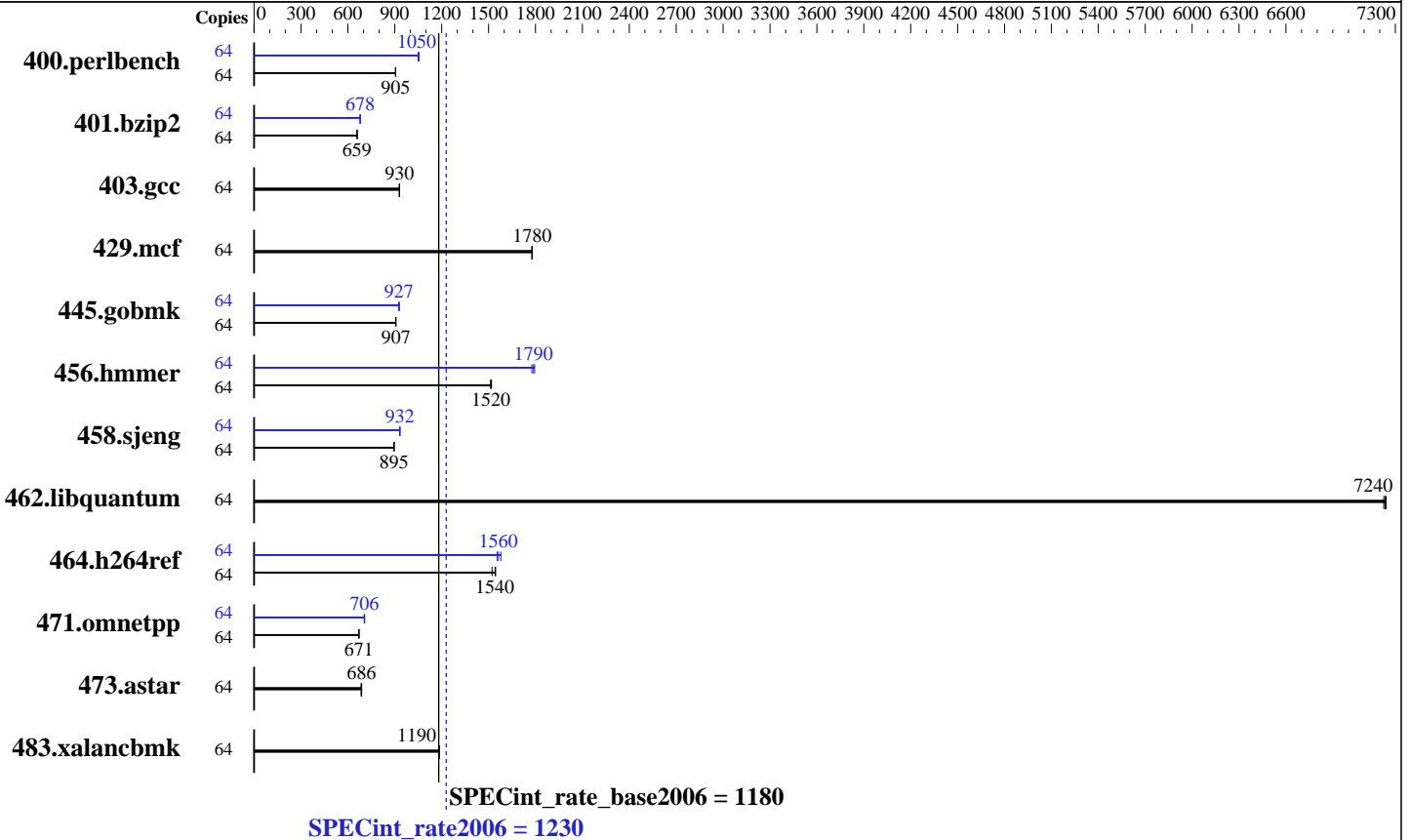
Test sponsor:　Hewlett-Packard Company　　　　　　　　　　**Hardware Availability:** Sep-2012

Tested by:　　Hewlett-Packard Company　　　　　　　　　　**Software Availability:**　Mar-2012

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Hardware

CPU Name:	Intel Xeon E5-4650
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	2 x 146 GB 15 K SAS, RAID 1
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2, (Santiago) Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	HP Array Configuration Utility, CLI version

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	691	905	**_691_**	**_905_**	691	905	64	596	1050	**_593_**	**_1050_**	593	1060
401.bzip2	64	**_938_**	**_659_**	936	660	938	659	64	911	678	**_910_**	**_678_**	910	679
403.gcc	64	555	928	**_554_**	**_930_**	553	931	64	555	928	**_554_**	**_930_**	553	931
429.mcf	64	328	1780	**_328_**	**_1780_**	329	1780	64	328	1780	**_328_**	**_1780_**	329	1780
445.gobmk	64	740	907	741	906	**_740_**	**_907_**	64	**_724_**	**_927_**	725	926	723	929
456.hmmer	64	**_394_**	**_1520_**	395	1510	393	1520	64	**_334_**	**_1790_**	336	1780	333	1790
458.sjeng	64	865	895	**_865_**	**_895_**	865	896	64	832	931	**_830_**	**_932_**	829	934
462.libquantum	64	184	7230	**_183_**	**_7240_**	183	7240	64	184	7230	**_183_**	**_7240_**	183	7240
464.h264ref	64	917	1550	930	1520	**_919_**	**_1540_**	64	897	1580	911	1550	**_906_**	**_1560_**
471.omnetpp	64	596	671	**_596_**	**_671_**	597	670	64	568	704	**_566_**	**_706_**	565	707
473.astar	64	**_655_**	**_686_**	653	688	656	685	64	**_655_**	**_686_**	653	688	656	685
483.xalancbmk	64	372	1190	373	1180	**_372_**	**_1190_**	64	372	1190	373	1180	**_372_**	**_1190_**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
    Filesystem page cache cleared with:
    echo 1 >   /proc/sys/vm/drop_caches
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
    Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version
    Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version

Platform Notes

    BIOS Configuration:
     HP Power Profile set to Custom
     Energy/Performance Bias is set to Maximum Performance
     Thermal Configuration set to Maximum Cooling
     Collaborative Power Control set to Disabled
     Processor Power and Utilization Monitoring set to Disabled
    Sysinfo program /cpu2006/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on BL660G8 Fri May 18 10:37:05 2012

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4650 0 @ 2.70GHz
      4 "physical id"s (chips)
      64 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
      physical 2: cores 0 1 2 3 4 5 6 7
      physical 3: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       264634472 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux BL660G8 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
   x86_64 x86_64 GNU/Linux

run-level 3 May 18 09:59

SPEC is set to: /cpu2006
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/sda3      ext4    133G  8.7G  118G   7% /

Additional information from dmidecode:
  BIOS HP I32 04/11/2012
  Memory:
   32x HP Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs2/32:/cpu2006/libs2/64"

 Binaries compiled on a system with 2x Xeon E5-2677 CPU + 256GB
 memory using SLES11 SP2,RC3
```

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/spec/libs2/32 -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/spec/libs2/32 -lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120605.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120605.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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