SPEC® CFP2006 Result

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Dell Inc.

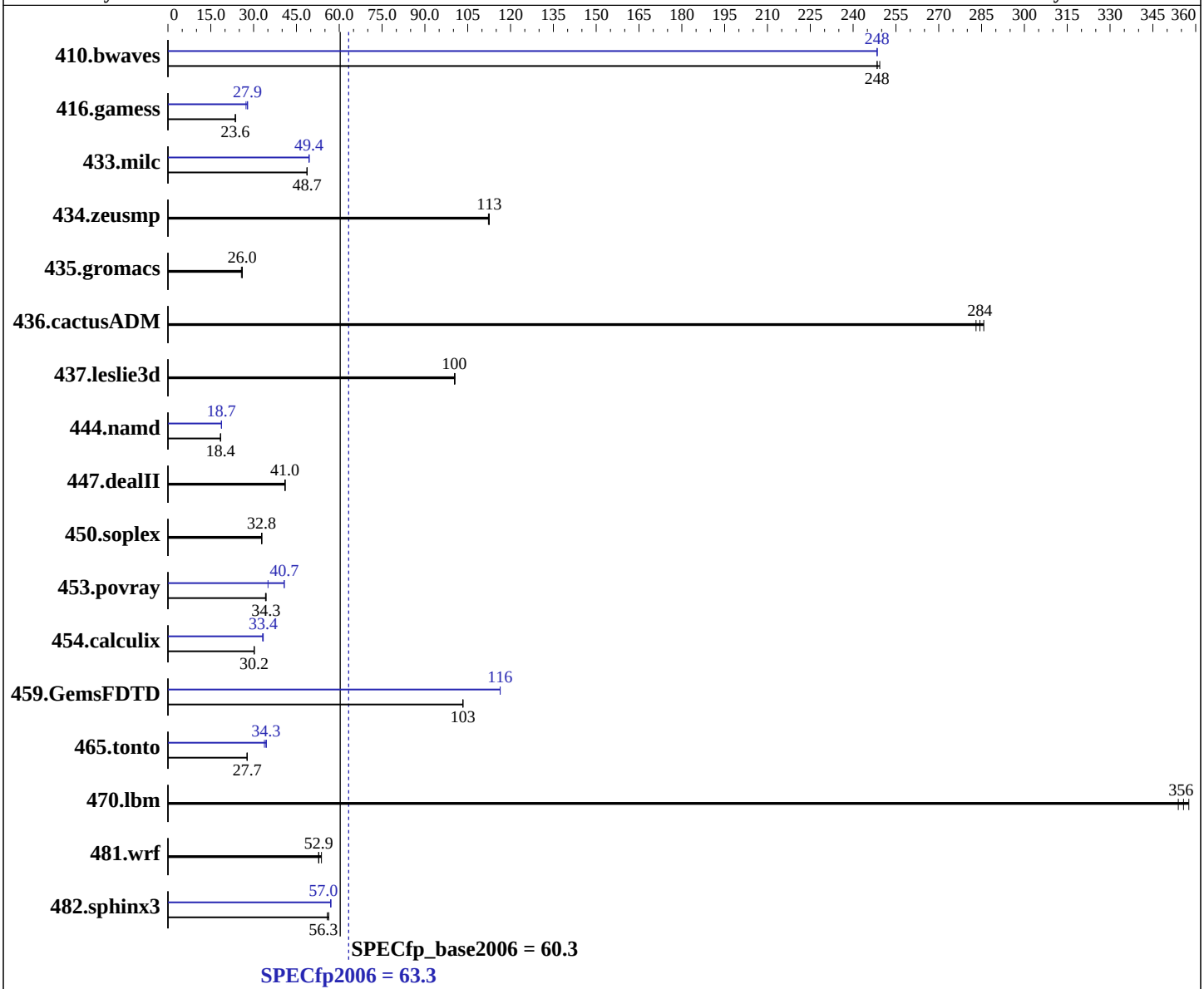
PowerEdge R520 (Intel Xeon E5-2430, 2.20 GHz)

SPECfp®2006 = 63.3

SPECfp_base2006 = 60.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2012
Hardware Availability: May-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-2430
CPU Characteristics:	Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (add definition here)

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**54.7**	**248**	54.7	248	54.5	249	54.7	248	**54.7**	**248**	54.7	248
416.gamess	828	23.6	829	23.6	**829**	**23.6**	**703**	**27.9**	702	27.9	716	27.4
433.milc	**188**	**48.7**	188	48.7	188	48.7	186	49.4	186	49.5	**186**	**49.4**
434.zeusmp	80.9	113	**80.9**	**113**	81.1	112	80.9	113	**80.9**	**113**	81.1	112
435.gromacs	**275**	**26.0**	274	26.0	278	25.7	**275**	**26.0**	274	26.0	278	25.7
436.cactusADM	**42.0**	**284**	41.8	286	42.2	283	**42.0**	**284**	41.8	286	42.2	283
437.leslie3d	**93.7**	**100**	93.7	100	93.5	101	**93.7**	**100**	93.7	100	93.5	101
444.namd	435	18.4	**435**	**18.4**	435	18.4	428	18.7	428	18.7	**428**	**18.7**
447.dealII	279	41.1	279	41.0	**279**	**41.0**	279	41.1	279	41.0	**279**	**41.0**
450.soplex	254	32.9	254	32.8	**254**	**32.8**	254	32.9	254	32.8	**254**	**32.8**
453.povray	**155**	**34.3**	156	34.2	155	34.3	131	40.7	**131**	**40.7**	152	35.0
454.calculix	**273**	**30.2**	274	30.2	273	30.3	247	33.4	**247**	**33.4**	249	33.1
459.GemsFDTD	**103**	**103**	103	103	103	103	**91.2**	**116**	91.2	116	91.2	116
465.tonto	355	27.7	354	27.8	**355**	**27.7**	291	33.8	**287**	**34.3**	285	34.5
470.lbm	38.4	358	**38.6**	**356**	38.8	354	38.4	358	**38.6**	**356**	38.8	354
481.wrf	208	53.8	**211**	**52.9**	212	52.8	208	53.8	**211**	**52.9**	212	52.8
482.sphinx3	346	56.3	**346**	**56.3**	349	55.8	341	57.1	342	57.0	**342**	**57.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Silk-2P Fri Mar  9 16:19:45 2012

This section contains SUT (System Under Test) info as seen by
```

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Platform Notes (Continued)

```
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2430 0 @ 2.20GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       49381468 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 2

uname -a:
   Linux Silk-2P 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
   (54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 9 08:51 last=S

SPEC is set to: /root/CPU2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext3  266G   53G  201G  21% /

Additional information from dmidecode:

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "12"
```

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General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
The Dell PowerEdge R520 and
the Bull NovaScale R450 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R520 model
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml`

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