# SPEC® CINT2006 Result

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## Dell Inc.

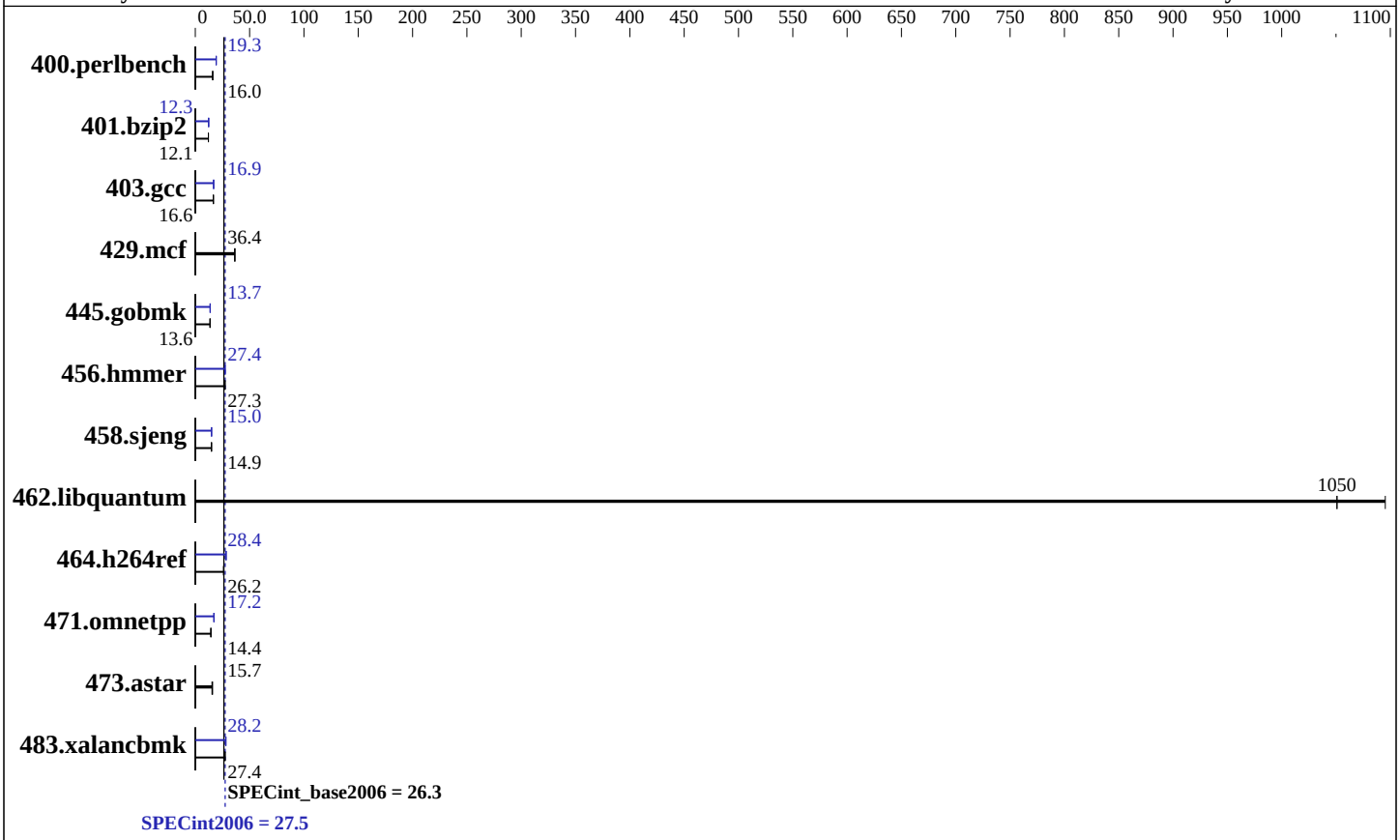
PowerEdge M520 (Intel Xeon E5-2403, 1.80 GHz)

SPECint®2006 = 27.5

SPECint_base2006 = 26.3

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Mar-2012
**Hardware Availability:** May-2012
**Software Availability:** Feb-2012




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2403 |
| CPU Characteristics: | |
| CPU MHz: | 1800 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 10 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz) |
| Disk Subsystem: | 1 x 1 TB 7200 RPM SAS |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run level 3 (add definition here) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **610** | **16.0** | 609 | 16.0 | 611 | 16.0 | 507 | 19.3 | **507** | **19.3** | 508 | 19.2 |
| 401.bzip2 | 802 | 12.0 | **800** | **12.1** | 800 | 12.1 | **785** | **12.3** | 784 | 12.3 | 786 | 12.3 |
| 403.gcc | 485 | 16.6 | **484** | **16.6** | 483 | 16.7 | 477 | 16.9 | **477** | **16.9** | 477 | 16.9 |
| 429.mcf | 251 | 36.3 | 250 | 36.4 | **251** | **36.4** | 251 | 36.3 | 250 | 36.4 | **251** | **36.4** |
| 445.gobmk | 773 | 13.6 | 771 | 13.6 | **773** | **13.6** | 767 | 13.7 | **767** | **13.7** | 767 | 13.7 |
| 456.hmmer | 341 | 27.4 | 341 | 27.3 | **341** | **27.3** | 340 | 27.5 | 342 | 27.3 | **340** | **27.4** |
| 458.sjeng | 811 | 14.9 | **812** | **14.9** | 814 | 14.9 | **808** | **15.0** | 808 | 15.0 | 810 | 14.9 |
| 462.libquantum | 18.9 | 1100 | 19.7 | 1050 | **19.7** | **1050** | 18.9 | 1100 | 19.7 | 1050 | **19.7** | **1050** |
| 464.h264ref | **846** | **26.2** | 851 | 26.0 | 844 | 26.2 | 780 | 28.4 | **780** | **28.4** | 784 | 28.2 |
| 471.omnetpp | 435 | 14.4 | 436 | 14.3 | **435** | **14.4** | 363 | 17.2 | **364** | **17.2** | 365 | 17.1 |
| 473.astar | 444 | 15.8 | **446** | **15.7** | 457 | 15.4 | 444 | 15.8 | **446** | **15.7** | 457 | 15.4 |
| 483.xalancbmk | 252 | 27.4 | 253 | 27.3 | **252** | **27.4** | **245** | **28.2** | 246 | 28.0 | 245 | 28.2 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Blur-2P Tue Mar 20 01:33:22 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
      2 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 4
      physical 0: cores 0 1 2 3
      physical 1: cores 0 1 2 3
```

Continued on next page

## Platform Notes (Continued)

```
      cache size : 10240 KB

   From /proc/meminfo
      MemTotal:       99092560 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      SUSE Linux Enterprise Server 11 (x86_64)

   From /etc/*release* /etc/*version*
      SuSE-release:
         SUSE Linux Enterprise Server 11 (x86_64)
         VERSION = 11
         PATCHLEVEL = 2

   uname -a:
      Linux Blur-2P 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
      (d73692b) x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Mar 20 01:23 last=S

   SPEC is set to: /root/CPU2006-1.2
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda1      ext3  913G   20G  847G   3% /

   Additional information from dmidecode:

   (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -ansi-alias`

401.bzip2: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

458.sjeng: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml


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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 06:12:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 June 2012.