# SPEC® CINT2006 Result

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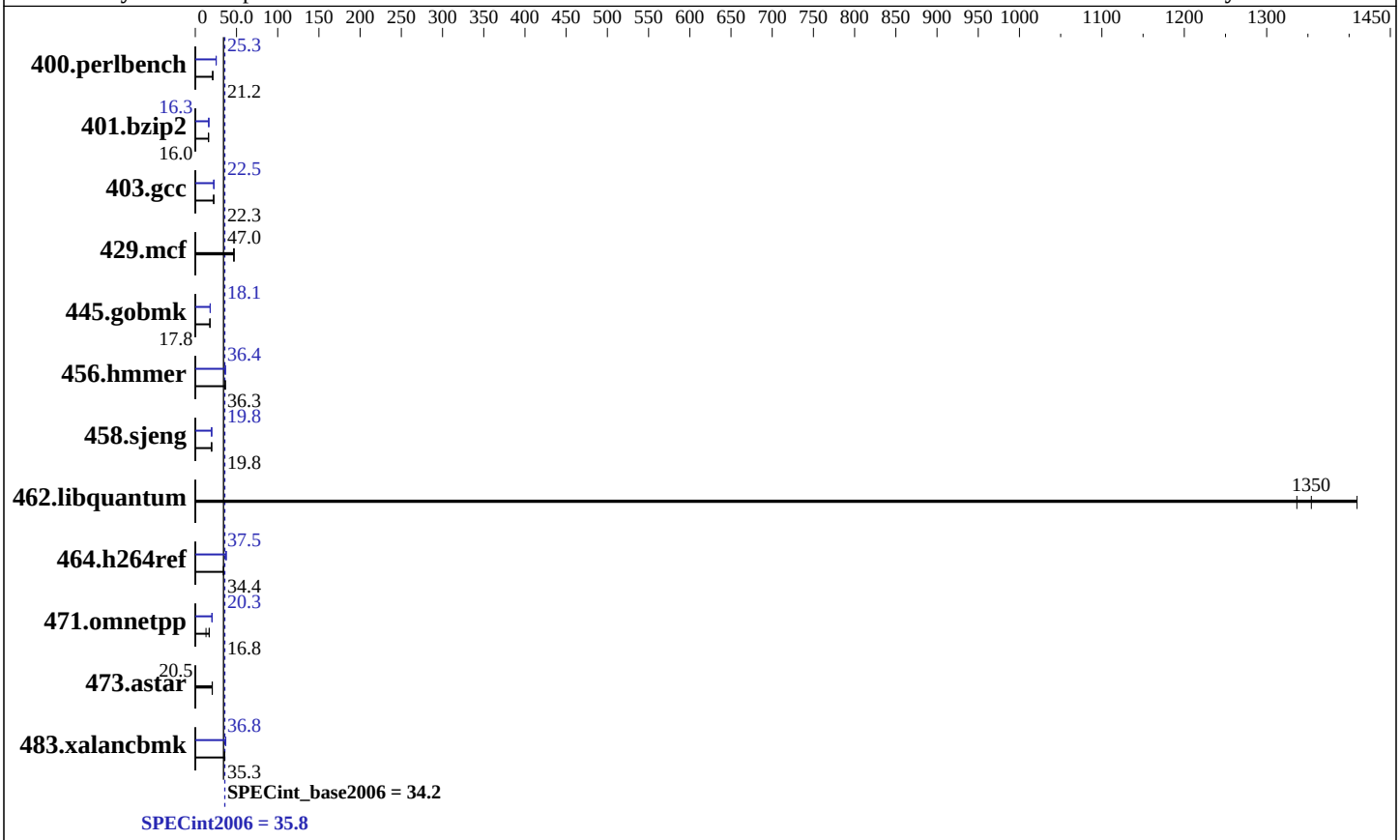
## Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2609)

| | |
|---|---|
| SPECint®2006 = | 35.8 |
| SPECint_base2006 = | 34.2 |

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** May-2012
**Hardware Availability:** Mar-2012
**Software Availability:** Dec-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2609 |
| CPU Characteristics: | |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 10 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 1Rx4 PC3-12800R-11, ECC, running at 1066 MHz and CL7) |
| Disk Subsystem: | 1 x 2 TB SATA II, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 464 | 21.1 | **462** | **21.2** | 461 | 21.2 | 386 | 25.3 | **386** | **25.3** | 386 | 25.3 |
| 401.bzip2 | **602** | **16.0** | 601 | 16.1 | 604 | 16.0 | 590 | 16.4 | **590** | **16.3** | 591 | 16.3 |
| 403.gcc | 361 | 22.3 | **361** | **22.3** | 360 | 22.3 | 357 | 22.5 | **358** | **22.5** | 358 | 22.5 |
| 429.mcf | **194** | **47.0** | 194 | 47.1 | 196 | 46.5 | **194** | **47.0** | 194 | 47.1 | 196 | 46.5 |
| 445.gobmk | **589** | **17.8** | 589 | 17.8 | 590 | 17.8 | **580** | **18.1** | 580 | 18.1 | 580 | 18.1 |
| 456.hmmer | 256 | 36.4 | **257** | **36.3** | 258 | 36.2 | 256 | 36.4 | **256** | **36.4** | 256 | 36.4 |
| 458.sjeng | 612 | 19.8 | 612 | 19.8 | **612** | **19.8** | 610 | 19.8 | **610** | **19.8** | 612 | 19.8 |
| 462.libquantum | 14.7 | 1410 | 15.5 | 1340 | **15.3** | **1350** | 14.7 | 1410 | 15.5 | 1340 | **15.3** | **1350** |
| 464.h264ref | **642** | **34.4** | 648 | 34.2 | 641 | 34.5 | **590** | **37.5** | 602 | 36.7 | 590 | 37.5 |
| 471.omnetpp | 371 | 16.9 | 481 | 13.0 | **371** | **16.8** | 308 | 20.3 | 307 | 20.4 | **308** | **20.3** |
| 473.astar | 339 | 20.7 | **343** | **20.5** | 343 | 20.5 | 339 | 20.7 | **343** | **20.5** | 343 | 20.5 |
| 483.xalancbmk | **196** | **35.3** | 196 | 35.2 | 195 | 35.5 | **188** | **36.8** | 188 | 36.8 | 186 | 37.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

C++ benchmarks:

```
icpc -m64
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

Continued on next page

## Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`


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For other inquiries, please contact webmaster@spec.org.

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