



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2650L)

SPECfp®2006 = **62.7**

SPECfp_base2006 = **58.8**

CPU2006 license: 001176

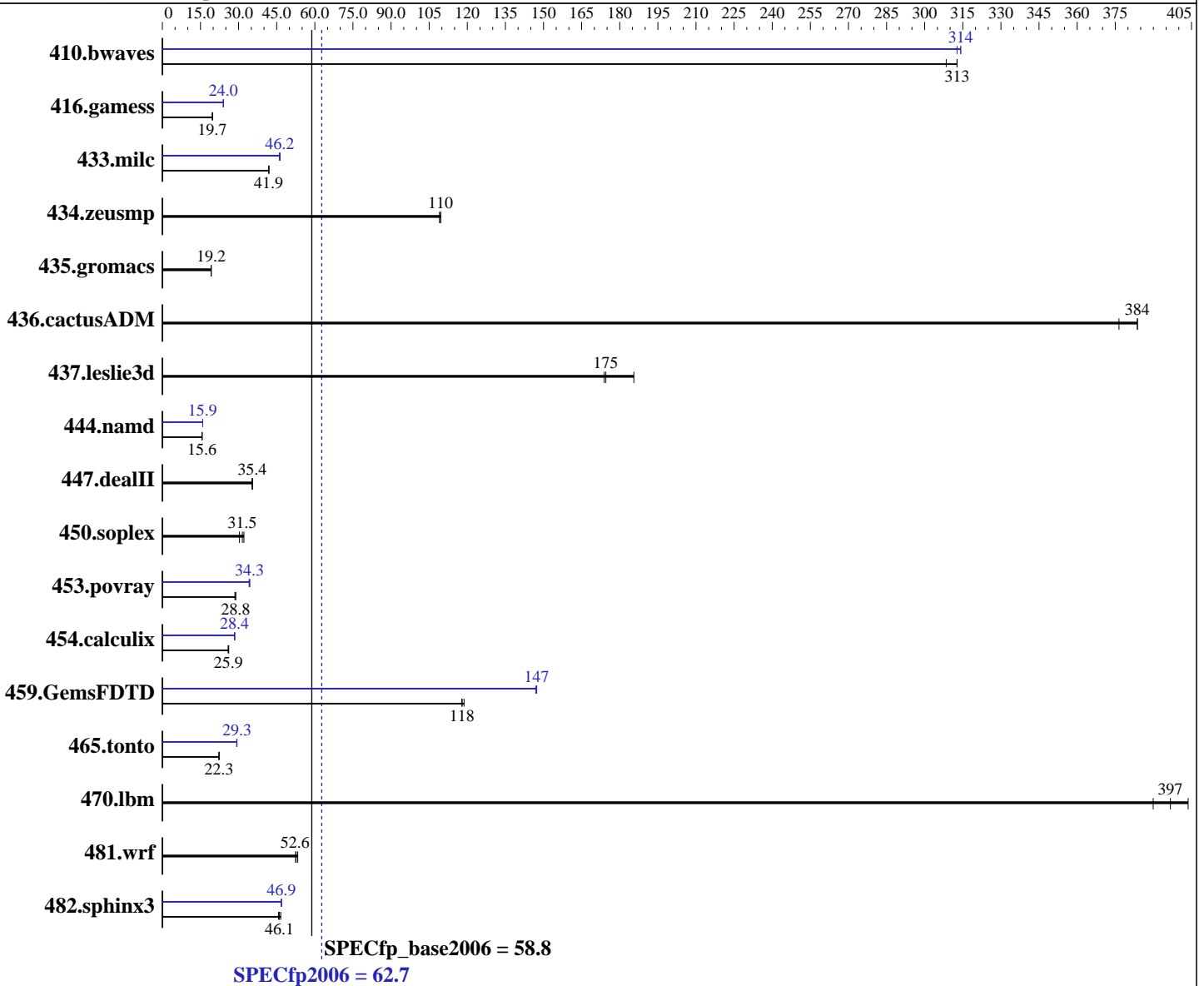
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2650L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2650L)

SPECfp2006 = **62.7**

SPECfp_base2006 = **58.8**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 1Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 2 TB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	43.5	313	43.5	313	44.1	308	43.3	314	43.3	314	43.5	313
416.gamess	994	19.7	995	19.7	993	19.7	817	24.0	815	24.0	814	24.1
433.milc	219	41.9	219	42.0	219	41.8	198	46.3	199	46.2	199	46.0
434.zeusmp	83.1	110	83.1	110	83.5	109	83.1	110	83.1	110	83.5	109
435.gromacs	373	19.2	372	19.2	371	19.3	373	19.2	372	19.2	371	19.3
436.cactusADM	31.1	384	31.7	376	31.1	384	31.1	384	31.7	376	31.1	384
437.leslie3d	53.9	175	54.1	174	50.7	186	53.9	175	54.1	174	50.7	186
444.namd	513	15.6	513	15.6	513	15.6	505	15.9	505	15.9	505	15.9
447.dealII	323	35.4	323	35.4	323	35.4	323	35.4	323	35.4	323	35.4
450.soplex	275	30.3	265	31.5	260	32.1	275	30.3	265	31.5	260	32.1
453.povray	184	28.9	185	28.8	186	28.5	156	34.1	155	34.3	155	34.4
454.calculix	318	25.9	316	26.1	318	25.9	289	28.5	290	28.4	290	28.4
459.GemsFDTD	90.0	118	89.4	119	90.0	118	72.2	147	72.0	147	72.0	147
465.tonto	441	22.3	441	22.3	442	22.2	335	29.3	335	29.4	337	29.2
470.lbm	34.0	404	35.2	390	34.6	397	34.0	404	35.2	390	34.6	397
481.wrf	212	52.6	213	52.4	210	53.3	212	52.6	213	52.4	210	53.3
482.sphinx3	418	46.6	426	45.7	423	46.1	417	46.8	416	46.9	416	46.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2650L)

SPECfp2006 = 62.7

SPECfp_base2006 = 58.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2650L)

SPECfp2006 = 62.7

SPECfp_base2006 = 58.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2650L)

SPECfp2006 = 62.7

SPECfp_base2006 = 58.8

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 08:12:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 May 2012.