



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®2006 = **71.2**

### IBM System x3550 M4 (Intel Xeon E5-2630)

SPECfp\_base2006 = **67.9**

CPU2006 license: 11

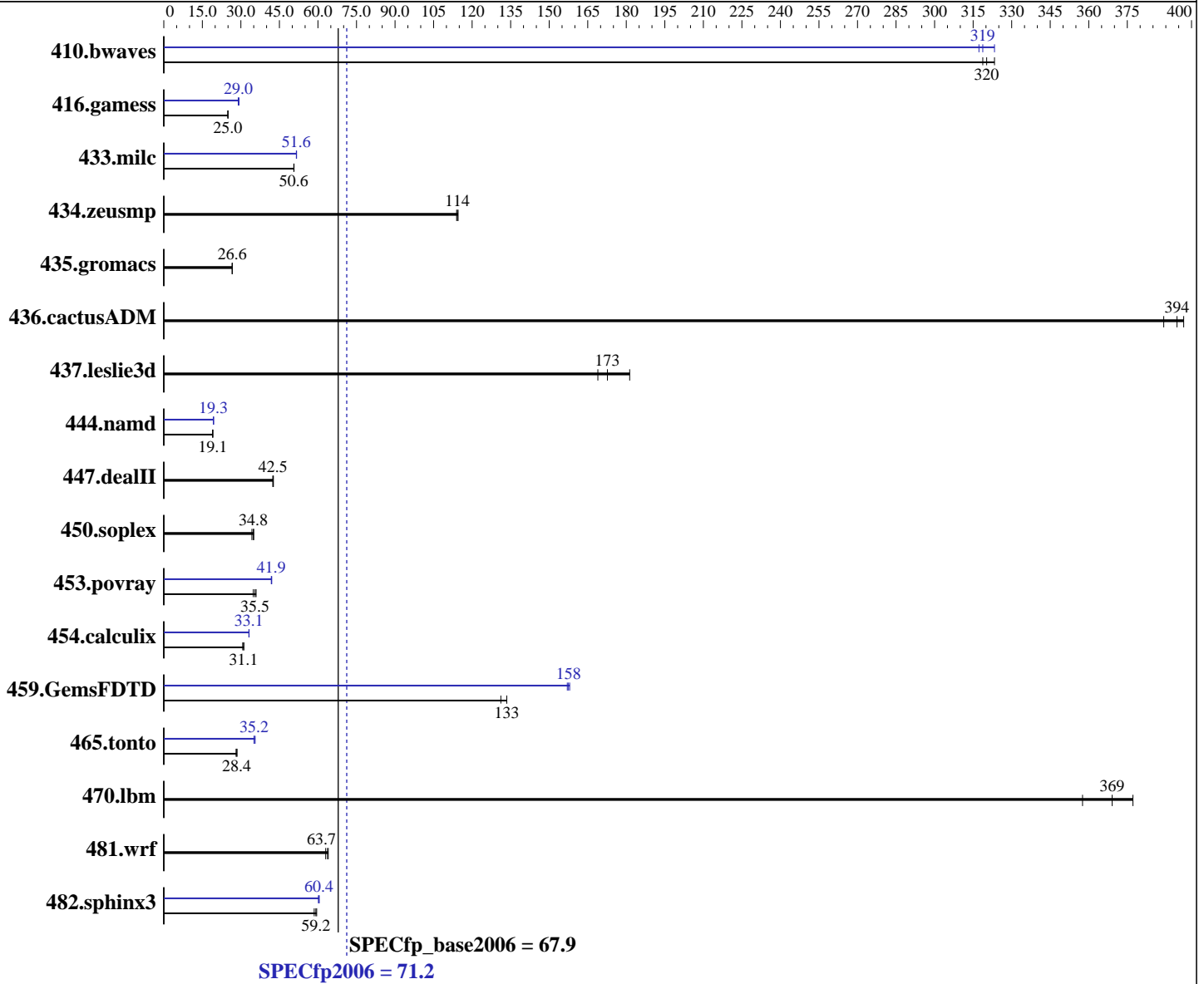
Test date: Apr-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Oct-2011



### Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 1 TB SAS, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	42.6	319	42.0	323	<b>42.4</b>	<b>320</b>	<b>42.6</b>	<b>319</b>	42.8	317	42.0	323
416.gamess	785	24.9	<b>784</b>	<b>25.0</b>	784	25.0	<b>675</b>	<b>29.0</b>	669	29.3	675	29.0
433.milc	181	50.6	<b>181</b>	<b>50.6</b>	181	50.7	178	51.7	178	51.6	<b>178</b>	<b>51.6</b>
434.zeusmp	79.4	115	79.8	114	<b>79.6</b>	<b>114</b>	79.4	115	79.8	114	<b>79.6</b>	<b>114</b>
435.gromacs	268	26.7	<b>268</b>	<b>26.6</b>	268	26.6	268	26.7	<b>268</b>	<b>26.6</b>	268	26.6
436.cactusADM	<b>30.3</b>	<b>394</b>	30.1	397	30.7	389	<b>30.3</b>	<b>394</b>	30.1	397	30.7	389
437.leslie3d	55.6	169	51.8	181	<b>54.4</b>	<b>173</b>	55.6	169	51.8	181	<b>54.4</b>	<b>173</b>
444.namd	421	19.1	421	19.0	<b>421</b>	<b>19.1</b>	<b>415</b>	<b>19.3</b>	415	19.3	415	19.3
447.dealII	<b>269</b>	<b>42.5</b>	268	42.7	270	42.4	<b>269</b>	<b>42.5</b>	268	42.7	270	42.4
450.soplex	243	34.3	<b>239</b>	<b>34.8</b>	238	35.0	243	34.3	<b>239</b>	<b>34.8</b>	238	35.0
453.povray	<b>150</b>	<b>35.5</b>	148	35.9	153	34.8	<b>127</b>	<b>41.9</b>	127	42.0	127	41.8
454.calculix	265	31.2	<b>266</b>	<b>31.1</b>	269	30.7	249	33.1	<b>249</b>	<b>33.1</b>	248	33.2
459.GemsFDTD	<b>79.5</b>	<b>133</b>	79.5	133	80.9	131	<b>67.4</b>	<b>158</b>	67.2	158	67.6	157
465.tonto	346	28.5	<b>346</b>	<b>28.4</b>	350	28.1	280	35.1	277	35.6	<b>280</b>	<b>35.2</b>
470.lbm	<b>37.2</b>	<b>369</b>	36.4	377	38.4	358	<b>37.2</b>	<b>369</b>	36.4	377	38.4	358
481.wrf	175	64.0	177	63.0	<b>175</b>	<b>63.7</b>	175	64.0	177	63.0	<b>175</b>	<b>63.7</b>
482.sphinx3	<b>329</b>	<b>59.2</b>	333	58.5	328	59.5	<b>323</b>	<b>60.4</b>	322	60.5	324	60.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Zone reclaim mode enabled with:  
 echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
 Operating Mode set to Maximum Performance  
 Sysinfo program /root/SPECcpu-v1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on x3550M4 Wed Apr 11 03:22:02 2012

This section contains SUT (System Under Test) info as seen by

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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores      : 6
  siblings       : 12
  physical 0:    cores 0 1 2 3 4 5
  physical 1:    cores 0 1 2 3 4 5
cache size      : 15360 KB

```

```

From /proc/meminfo
MemTotal:        132237084 kB
HugePages_Total: 0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux x3550M4 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10 15:42:40 EDT 2011
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Apr 9 17:04

```

SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_x3550m4-lv_root
ext4            790G    69G  681G  10% /

```

Additional information from dmidecode:

```

Memory:
16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

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## General Notes (Continued)

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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