SPEC® CFP2006 Result

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Fujitsu

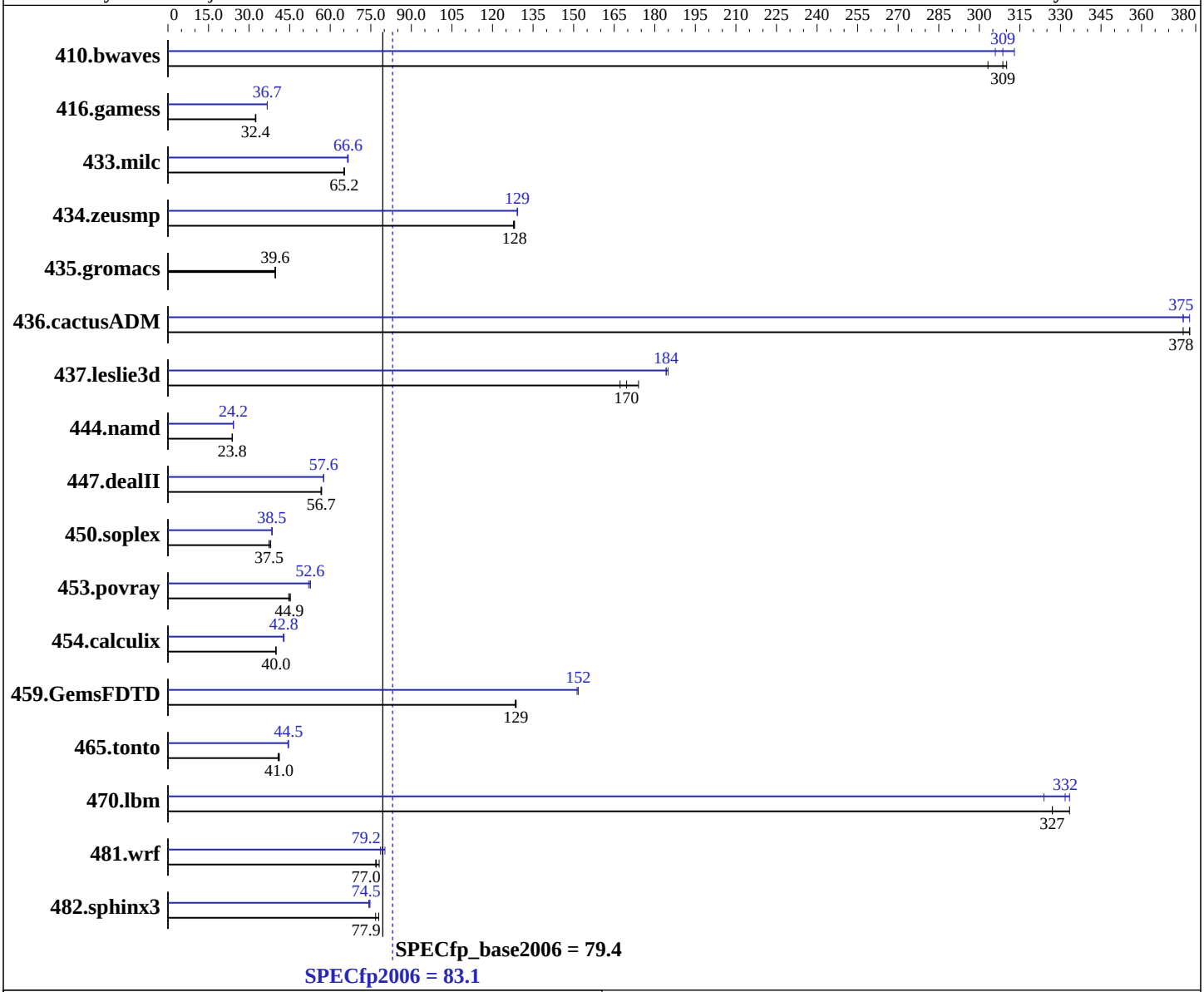
CELSIUS R920 (Intel Xeon E5-2643)

SPECfp®2006 = 83.1

SPECfp_base2006 = 79.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2643
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi - user)

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L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)
Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	44.8	303	**44.0**	**309**	43.8	310	**44.0**	**309**	44.4	306	43.4	313
416.gamess	602	32.5	606	32.3	**604**	**32.4**	533	36.7	533	36.7	**533**	**36.7**
433.milc	141	65.1	**141**	**65.2**	141	65.2	**138**	**66.6**	138	66.7	138	66.4
434.zeusmp	71.2	128	71.0	128	**71.0**	**128**	70.4	129	70.4	129	**70.4**	**129**
435.gromacs	**180**	**39.6**	180	39.8	180	39.6	**180**	**39.6**	180	39.8	180	39.6
436.cactusADM	31.8	375	31.6	378	**31.6**	**378**	**31.8**	**375**	31.6	378	31.8	375
437.leslie3d	54.0	174	**55.4**	**170**	56.2	167	50.8	185	**51.0**	**184**	51.0	184
444.namd	337	23.8	**337**	**23.8**	337	23.8	**332**	**24.2**	331	24.2	332	24.2
447.dealII	**202**	**56.7**	201	56.8	202	56.6	**199**	**57.6**	199	57.6	199	57.5
450.soplex	**223**	**37.5**	223	37.4	219	38.0	216	38.6	**217**	**38.5**	217	38.4
453.povray	119	44.7	**118**	**44.9**	118	45.3	101	52.7	**101**	**52.6**	102	52.0
454.calculix	206	40.0	**206**	**40.0**	207	39.9	192	42.9	193	42.7	**193**	**42.8**
459.GemsFDTD	82.6	128	**82.4**	**129**	82.4	129	**69.9**	**152**	69.9	152	70.1	151
465.tonto	242	40.7	**240**	**41.0**	239	41.2	221	44.6	**221**	**44.5**	222	44.4
470.lbm	42.0	327	**42.0**	**327**	41.2	333	42.4	324	41.2	333	**41.4**	**332**
481.wrf	143	78.1	145	76.8	**145**	**77.0**	**141**	**79.2**	142	78.5	139	80.2
482.sphinx3	254	76.8	250	77.9	**250**	**77.9**	**262**	**74.5**	261	74.7	262	74.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
For full details on using numactl, please refer to your Linux documentation, 'man numactl'
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Hyper-Threading Technology = Disabled
Frequency Floor Override = Enabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with
 2x Xeon E5-2690 CPU + 64 GB memory using
 Red Hat Enterprise Linux Server release 6.1 (Santiago)
 Added glibc-static-2.12-1.25.el6.x86_64.rpm
 to enable static linking

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -static

450.soplex: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

437.leslie3d: Same as 434.zeusmp

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

454.calculix: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `Same as 436.cactusADM`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml

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For other inquiries, please contact webmaster@spec.org.

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