**Supermicro**

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel i3-2120)

| Test date: | Mar-2012 |
| Test sponsor: | Supermicro |
| Test date: | Mar-2012 |
| Hardware Availability: | Aug-2011 |
| Software Availability: | Oct-2011 |
| SPECint_rate2006 = | 90.7 |
| SPECint_rate_base2006 = | 86.6 |

| Test sponsor: | Supermicro |
| Tested by: | Supermicro |

| Software Availability: | Oct-2011 |
| SPECint_rate_base2006 = | 86.6 |

| CPU2006 license: | 001176 |
| CPU Name: | Intel Core i3-2120 |
| CPU Characteristics: | Intel Core i3-2120 |
| CPU MHZ: | 3300 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 3 MB I+D on chip per core |
| Other Cache: | None |
| Memory: | 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC) |
| Disk Subsystem: | 1 x 500 GB SATA III, 7200 RPM |
| Other Hardware: | None |

| Operating System: | Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |
### SPEC CINT2006 Result

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
</tr>
<tr>
<td>400.perlbench</td>
<td>4</td>
<td>604</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td>860</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>459</td>
</tr>
<tr>
<td>429.mcf</td>
<td>4</td>
<td>282</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>4</td>
<td>656</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>368</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>739</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td>156</td>
</tr>
<tr>
<td>464.h264ref</td>
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<td>793</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>4</td>
<td>465</td>
</tr>
<tr>
<td>473.astar</td>
<td>4</td>
<td>530</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>4</td>
<td>288</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

### Base Compiler Invocation

C benchmarks:
- icc -m32

C++ benchmarks:
- icpc -m32
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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portatility Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-rr-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
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Peak Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

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