# SPEC® CINT2006 Result

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## Itautec

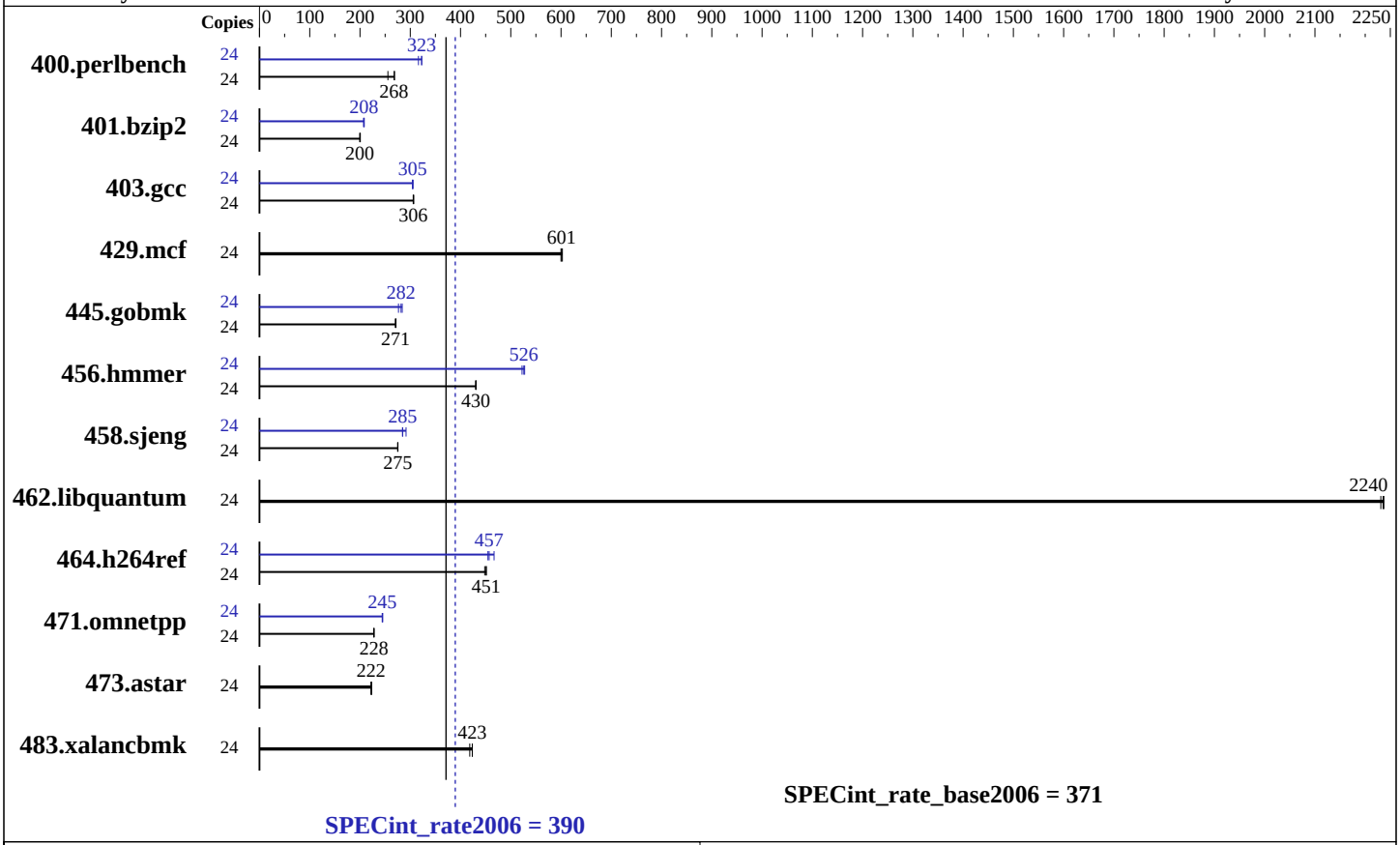
Servidor Itautec LX205 (Intel Xeon E5-2630L)

SPECint®_rate2006 = 390

SPECint_rate_base2006 = 371

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Mar-2012
**Hardware Availability:** Jun-2012
**Software Availability:** Dec-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2630L |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.50 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC) |
| Disk Subsystem: | 500 GB, SATA-2, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server Release 6.2, (Santiago) 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011 |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 872 | 269 | **875** | **268** | 918 | 256 | 24 | **727** | **323** | 741 | 316 | 726 | 323 |
| 401.bzip2 | 24 | **1159** | **200** | 1160 | 200 | 1154 | 201 | 24 | **1113** | **208** | 1119 | 207 | 1111 | 208 |
| 403.gcc | 24 | 632 | 306 | 629 | 307 | **631** | **306** | 24 | **634** | **305** | 635 | 304 | 632 | 306 |
| 429.mcf | 24 | 363 | 603 | **364** | **601** | 365 | 600 | 24 | 363 | 603 | **364** | **601** | 365 | 600 |
| 445.gobmk | 24 | **929** | **271** | 927 | 272 | 931 | 270 | 24 | **894** | **282** | 910 | 277 | 886 | 284 |
| 456.hmmer | 24 | 521 | 430 | 519 | 431 | **520** | **430** | 24 | **426** | **526** | 429 | 522 | 424 | 528 |
| 458.sjeng | 24 | 1054 | 276 | **1055** | **275** | 1056 | 275 | 24 | 996 | 292 | 1021 | 284 | **1019** | **285** |
| 462.libquantum | 24 | 223 | 2230 | **222** | **2240** | 222 | 2240 | 24 | 223 | 2230 | **222** | **2240** | 222 | 2240 |
| 464.h264ref | 24 | 1185 | 448 | **1178** | **451** | 1177 | 451 | 24 | **1162** | **457** | 1138 | 467 | 1168 | 455 |
| 471.omnetpp | 24 | 659 | 228 | 658 | 228 | **659** | **228** | 24 | 612 | 245 | **613** | **245** | 614 | 244 |
| 473.astar | 24 | 759 | 222 | 754 | 223 | **758** | **222** | 24 | 759 | 222 | 754 | 223 | **758** | **222** |
| 483.xalancbmk | 24 | **391** | **423** | 391 | 423 | 396 | 418 | 24 | **391** | **423** | 391 | 423 | 396 | 418 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

## General Notes

```
This result was measured on the Servidor Itautec MX205.
The Servidor Itautec LX205, Servidor Itautec MX205 and
the Servidor Itautec MX225+ are electronically equivalent.
```

# Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`
`-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

Continued on next page

## Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`


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For questions about this result, please contact the tester.
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