# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

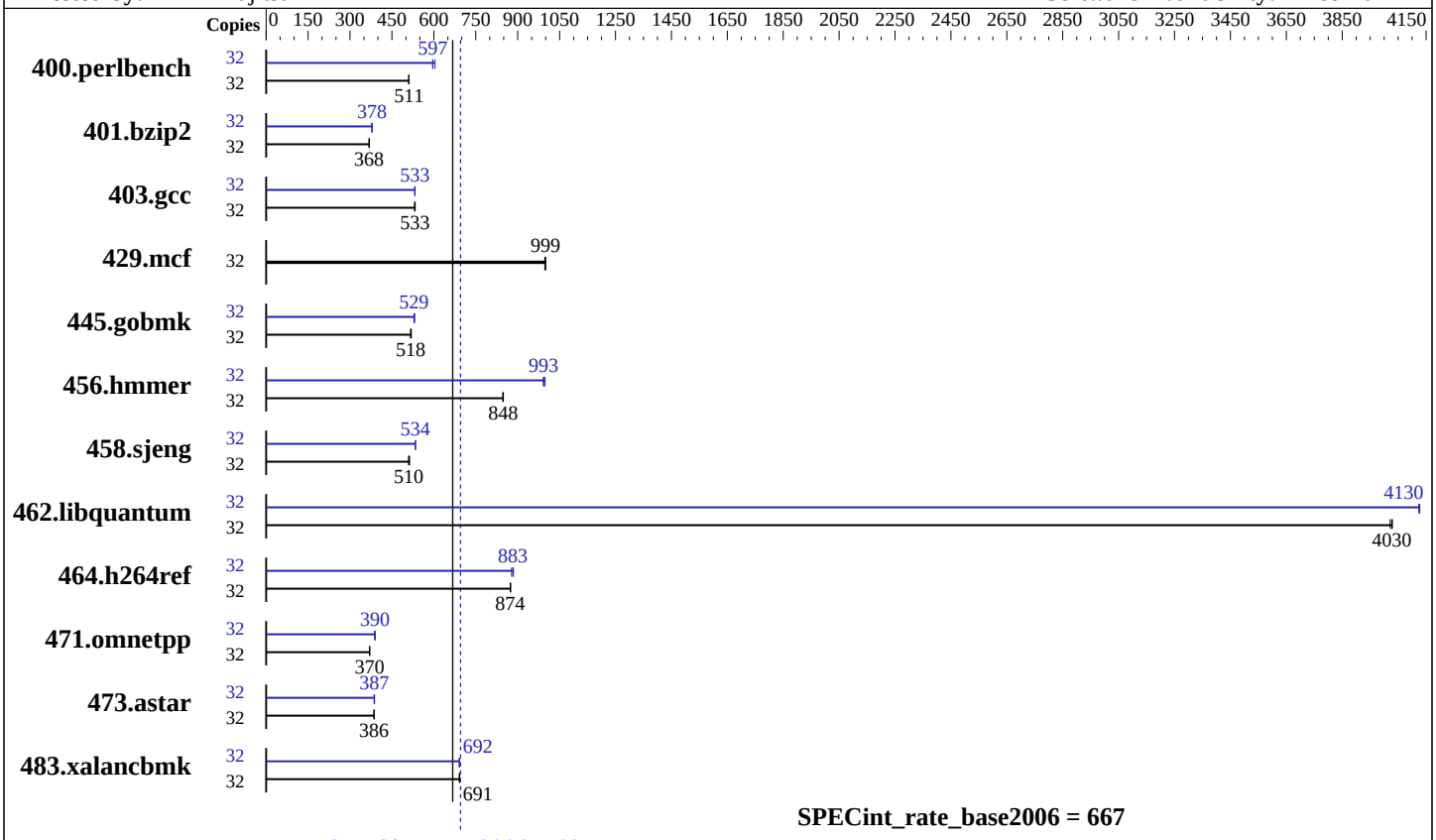
PRIMERGY RX200 S7, Intel Xeon E5-2690, 2.90 GHz

SPECint®_rate2006 = 695

SPECint_rate_base2006 = 667

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Mar-2012
**Hardware Availability:** Mar-2012
**Software Availability:** Feb-2012




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2690 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.80 GHz |
| CPU MHz: | 2900 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap 10 (Multi-Core) |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY RX200 S7, Intel Xeon E5-2690, 2.90 GHz

SPECint_rate2006 = 695

SPECint_rate_base2006 = 667

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Mar-2012
**Hardware Availability:** Mar-2012
**Software Availability:** Feb-2012

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 32 | **612** | **511** | 612 | 511 | 613 | 510 | 32 | 525 | 596 | **524** | **597** | 517 | 604 |
| 401.bzip2 | 32 | 836 | 369 | 839 | 368 | **838** | **368** | 32 | **816** | **378** | 814 | 380 | 816 | 378 |
| 403.gcc | 32 | 483 | 533 | **484** | **533** | 485 | 531 | 32 | 485 | 531 | **484** | **533** | 483 | 533 |
| 429.mcf | 32 | 293 | 997 | **292** | **999** | 292 | 1000 | 32 | 293 | 997 | **292** | **999** | 292 | 1000 |
| 445.gobmk | 32 | **648** | **518** | 650 | 517 | 646 | 519 | 32 | 635 | 529 | **634** | **529** | 631 | 532 |
| 456.hmmer | 32 | 352 | 849 | **352** | **848** | 353 | 846 | 32 | 299 | 998 | 301 | 991 | **301** | **993** |
| 458.sjeng | 32 | **759** | **510** | 755 | 513 | 759 | 510 | 32 | 725 | 534 | 726 | 534 | **725** | **534** |
| 462.libquantum | 32 | 165 | 4020 | 165 | 4030 | **165** | **4030** | 32 | **161** | **4130** | 161 | 4120 | 161 | 4130 |
| 464.h264ref | 32 | 810 | 875 | **810** | **874** | 811 | 874 | 32 | **802** | **883** | 800 | 885 | 807 | 878 |
| 471.omnetpp | 32 | 540 | 370 | **540** | **370** | 540 | 370 | 32 | 515 | 388 | 513 | 390 | **513** | **390** |
| 473.astar | 32 | 581 | 387 | 583 | 386 | **581** | **386** | 32 | **581** | **387** | 581 | 386 | 579 | 388 |
| 483.xalancbmk | 32 | **319** | **691** | 319 | 693 | 319 | 691 | 32 | 320 | 691 | **319** | **692** | 319 | 692 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

403.gcc: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

462.libquantum: icc -m32

464.h264ref: icc -m32

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Continued on next page

## Peak Optimization Flags (Continued)

483.xalancbmk: Same as 473.astar

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 06:52:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 April 2012.