



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

### SPECfp®\_rate2006 = 196

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

### SPECfp\_rate\_base2006 = 191

CPU2006 license: 19

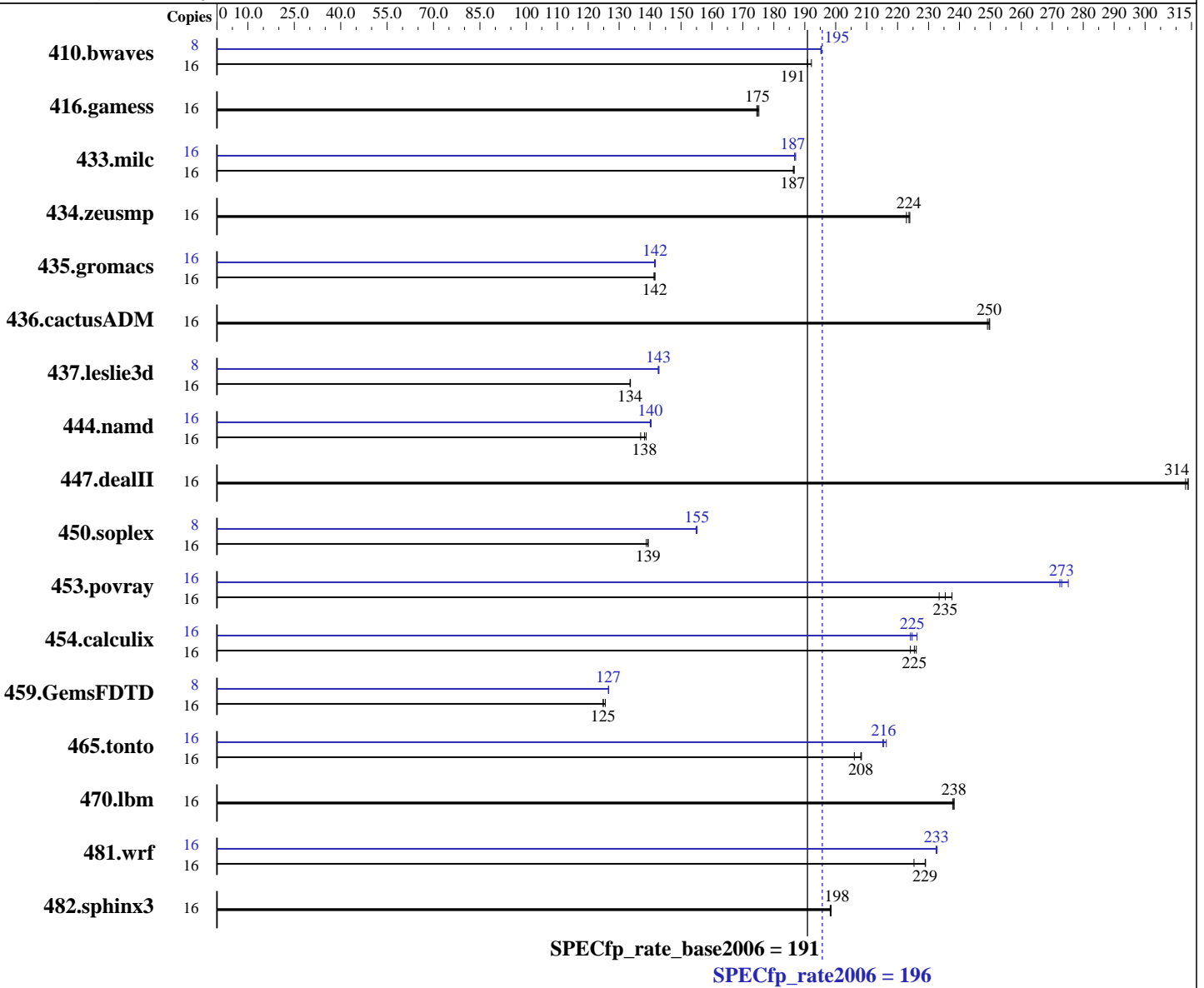
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2650L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECfp\_rate2006 = 196

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

SPECfp\_rate\_base2006 = 191

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 64 GB (8 x 8 GB 2Rx4 PC3L-12800R-11, ECC)  
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
Other Hardware: PRIMERGY SX940 S1 for hw\_disk

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base   |             |            |             |            |             |            | Peak   |             |            |             |            |            |            |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|------------|------------|
|               | Copies | Seconds     | Ratio      | Seconds     | Ratio      | Seconds     | Ratio      | Copies | Seconds     | Ratio      | Seconds     | Ratio      | Seconds    | Ratio      |
| 410.bwaves    | 16     | 1139        | 191        | <u>1139</u> | <u>191</u> | 1132        | 192        | 8      | <u>557</u>  | <u>195</u> | 556         | 195        | 557        | 195        |
| 416.gamess    | 16     | 1788        | 175        | <u>1793</u> | <u>175</u> | 1795        | 174        | 16     | 1788        | 175        | <u>1793</u> | <u>175</u> | 1795       | 174        |
| 433.milc      | 16     | 788         | 186        | <u>787</u>  | <u>187</u> | 787         | 187        | 16     | 787         | 187        | <u>786</u>  | <u>187</u> | 785        | 187        |
| 434.zeusmp    | 16     | <u>651</u>  | <u>224</u> | 650         | 224        | 653         | 223        | 16     | <u>651</u>  | <u>224</u> | 650         | 224        | 653        | 223        |
| 435.gromacs   | 16     | 809         | 141        | 807         | 142        | <u>807</u>  | <u>142</u> | 16     | 806         | 142        | 808         | 141        | <u>807</u> | <u>142</u> |
| 436.cactusADM | 16     | 768         | 249        | 765         | 250        | <u>766</u>  | <u>250</u> | 16     | 768         | 249        | 765         | 250        | <u>766</u> | <u>250</u> |
| 437.leslie3d  | 16     | 1125        | 134        | 1126        | 134        | <u>1126</u> | <u>134</u> | 8      | 527         | 143        | <u>527</u>  | <u>143</u> | 526        | 143        |
| 444.namd      | 16     | 937         | 137        | <u>929</u>  | <u>138</u> | 925         | 139        | 16     | 914         | 140        | <u>916</u>  | <u>140</u> | 916        | 140        |
| 447.dealII    | 16     | 585         | 313        | 583         | 314        | <u>583</u>  | <u>314</u> | 16     | 585         | 313        | 583         | 314        | <u>583</u> | <u>314</u> |
| 450.soplex    | 16     | <u>958</u>  | <u>139</u> | 961         | 139        | 957         | 139        | 8      | 430         | 155        | <u>430</u>  | <u>155</u> | 431        | 155        |
| 453.povray    | 16     | <u>362</u>  | <u>235</u> | 358         | 238        | 365         | 233        | 16     | 312         | 272        | 309         | 275        | <u>312</u> | <u>273</u> |
| 454.calculix  | 16     | <u>586</u>  | <u>225</u> | 589         | 224        | 584         | 226        | 16     | <u>587</u>  | <u>225</u> | 583         | 226        | 589        | 224        |
| 459.GemsFDTD  | 16     | <u>1360</u> | <u>125</u> | 1352        | 126        | 1360        | 125        | 8      | 670         | 127        | 671         | 127        | <u>671</u> | <u>127</u> |
| 465.tonto     | 16     | <u>757</u>  | <u>208</u> | 764         | 206        | 756         | 208        | 16     | <u>731</u>  | <u>216</u> | 728         | 216        | 732        | 215        |
| 470.lbm       | 16     | 924         | 238        | 923         | 238        | <u>923</u>  | <u>238</u> | 16     | 924         | 238        | 923         | 238        | <u>923</u> | <u>238</u> |
| 481.wrf       | 16     | 793         | 225        | 780         | 229        | <u>781</u>  | <u>229</u> | 16     | 768         | 233        | 769         | 232        | <u>768</u> | <u>233</u> |
| 482.sphinx3   | 16     | <u>1571</u> | <u>198</u> | 1571        | 198        | 1573        | 198        | 16     | <u>1571</u> | <u>198</u> | 1571        | 198        | 1573       | 198        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 196**

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

**SPECfp\_rate\_base2006 = 191**

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test date:** Mar-2012  
**Hardware Availability:** Mar-2012  
**Software Availability:** Dec-2011

## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 196**

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

**SPECfp\_rate\_base2006 = 191**

**CPU2006 license:** 19

**Test date:** Mar-2012

**Test sponsor:** Fujitsu

**Hardware Availability:** Mar-2012

**Tested by:** Fujitsu

**Software Availability:** Dec-2011

## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp\_rate2006 = 196

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

SPECfp\_rate\_base2006 = 191

CPU2006 license: 19

Test date: Mar-2012

Test sponsor: Fujitsu

Hardware Availability: Mar-2012

Tested by: Fujitsu

Software Availability: Dec-2011

## Peak Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 196**

PRIMERGY BX924 S3, Intel Xeon E5-2650L, 1.80 GHz

**SPECfp\_rate\_base2006 = 191**

**CPU2006 license:** 19

**Test sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test date:** Mar-2012

**Hardware Availability:** Mar-2012

**Software Availability:** Dec-2011

## Peak Optimization Flags (Continued)

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 06:43:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 10 April 2012.