



SPEC[®] CFP2006 Result

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Supermicro

SPECfp[®]_rate2006 = 104

SuperServer 1017C-TF (X9SCL-F, Intel E3-1260L)

SPECfp_rate_base2006 = 101

CPU2006 license: 001176

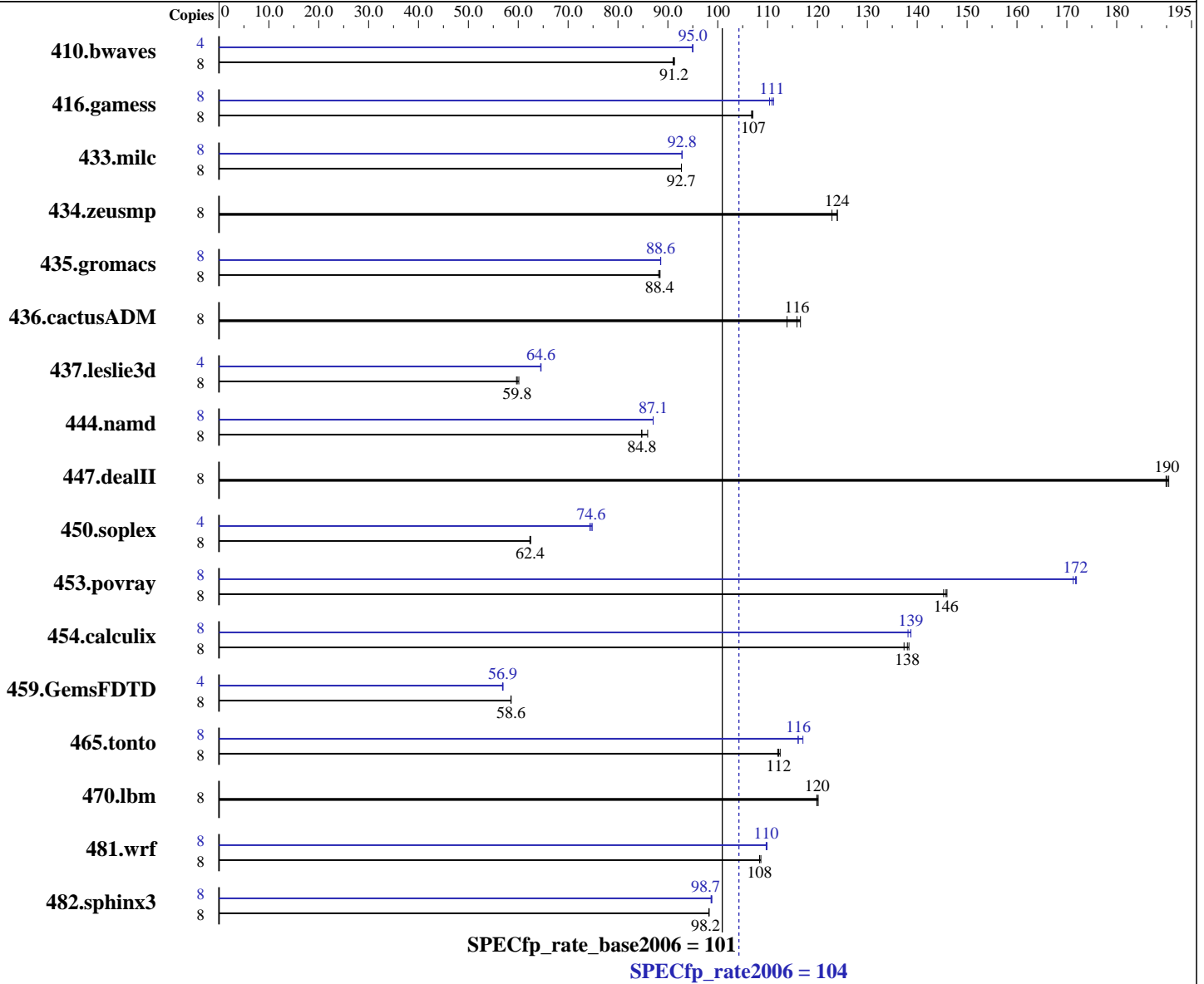
Test date: Mar-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 8 | 1194 | 91.1 | 1190 | 91.3 | <u>1192</u> | <u>91.2</u> | 4 | 572 | 95.0 | <u>572</u> | <u>95.0</u> | 573 | 94.9 | | |
| 416.gamess | 8 | 1467 | 107 | 1463 | 107 | <u>1465</u> | <u>107</u> | 8 | 1409 | 111 | 1419 | 110 | <u>1413</u> | <u>111</u> | | |
| 433.milc | 8 | 792 | 92.7 | 792 | 92.7 | <u>792</u> | <u>92.7</u> | 8 | 791 | 92.9 | <u>791</u> | <u>92.8</u> | 791 | 92.8 | | |
| 434.zeusmp | 8 | 587 | 124 | 592 | 123 | <u>587</u> | <u>124</u> | 8 | 587 | 124 | 592 | 123 | <u>587</u> | <u>124</u> | | |
| 435.gromacs | 8 | 648 | 88.2 | <u>646</u> | <u>88.4</u> | 646 | 88.4 | 8 | 645 | 88.5 | 645 | 88.6 | <u>645</u> | <u>88.6</u> | | |
| 436.cactusADM | 8 | 839 | 114 | 820 | 117 | <u>825</u> | <u>116</u> | 8 | 839 | 114 | 820 | 117 | <u>825</u> | <u>116</u> | | |
| 437.leslie3d | 8 | 1261 | 59.7 | <u>1257</u> | <u>59.8</u> | 1251 | 60.1 | 4 | 583 | 64.5 | 582 | 64.6 | <u>582</u> | <u>64.6</u> | | |
| 444.namd | 8 | 746 | 86.0 | <u>757</u> | <u>84.8</u> | 757 | 84.7 | 8 | 737 | 87.0 | 737 | 87.1 | <u>737</u> | <u>87.1</u> | | |
| 447.dealII | 8 | 481 | 190 | <u>481</u> | <u>190</u> | 482 | 190 | 8 | 481 | 190 | <u>481</u> | <u>190</u> | 482 | 190 | | |
| 450.soplex | 8 | 1070 | 62.3 | 1068 | 62.5 | <u>1069</u> | <u>62.4</u> | 4 | 449 | 74.4 | <u>447</u> | <u>74.6</u> | 446 | 74.8 | | |
| 453.povray | 8 | <u>292</u> | <u>146</u> | 293 | 145 | 292 | 146 | 8 | 248 | 172 | 248 | 171 | <u>248</u> | <u>172</u> | | |
| 454.calculix | 8 | 481 | 137 | <u>478</u> | <u>138</u> | 477 | 138 | 8 | <u>476</u> | <u>139</u> | 476 | 139 | 478 | 138 | | |
| 459.GemsFDTD | 8 | 1449 | 58.6 | <u>1450</u> | <u>58.6</u> | 1450 | 58.5 | 4 | <u>746</u> | <u>56.9</u> | 747 | 56.8 | 745 | 57.0 | | |
| 465.tonto | 8 | 700 | 113 | 703 | 112 | <u>701</u> | <u>112</u> | 8 | 672 | 117 | <u>678</u> | <u>116</u> | 678 | 116 | | |
| 470.lbm | 8 | 915 | 120 | 917 | 120 | <u>916</u> | <u>120</u> | 8 | 915 | 120 | 917 | 120 | <u>916</u> | <u>120</u> | | |
| 481.wrf | 8 | 825 | 108 | 822 | 109 | <u>824</u> | <u>108</u> | 8 | 815 | 110 | <u>814</u> | <u>110</u> | 813 | 110 | | |
| 482.sphinx3 | 8 | <u>1587</u> | <u>98.2</u> | 1587 | 98.2 | 1587 | 98.3 | 8 | 1580 | 98.7 | <u>1580</u> | <u>98.7</u> | 1578 | 98.8 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Fan speed set to Full Speed in BIOS Setup



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
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