# SPEC® CFP2006 Result

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## Dell Inc.

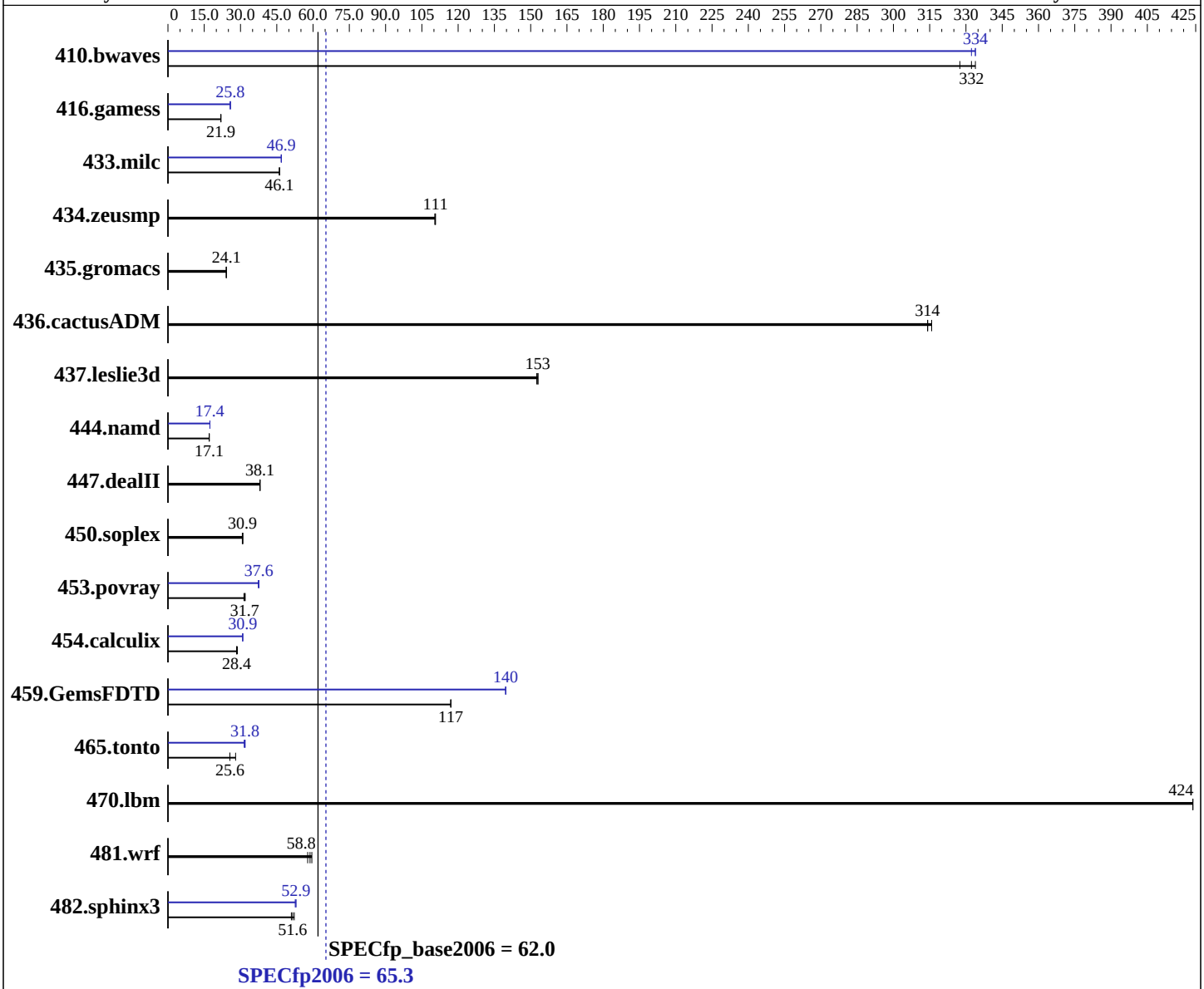
PowerEdge R720 (Intel Xeon E5-2630L, 2.00 GHz)

SPECfp®2006 = 65.3

SPECfp_base2006 = 62.0

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Feb-2012
**Hardware Availability:** Mar-2012
**Software Availability:** Feb-2012




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2630L |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.50 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run level 3 (add definition here) |

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
Disk Subsystem: 1 x 146 GB 10000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 41.5 | 327 | **40.9** | **332** | 40.7 | 334 | **40.7** | **334** | 40.7 | 334 | 40.9 | 332 |
| 416.gamess | 893 | 21.9 | 897 | 21.8 | **894** | **21.9** | 762 | 25.7 | 758 | 25.8 | **759** | **25.8** |
| 433.milc | **199** | **46.1** | 199 | 46.1 | 199 | 46.1 | **196** | **46.9** | 196 | 46.9 | 196 | 46.9 |
| 434.zeusmp | 82.3 | 111 | 82.5 | 110 | **82.3** | **111** | 82.3 | 111 | 82.5 | 110 | **82.3** | **111** |
| 435.gromacs | **296** | **24.1** | 296 | 24.1 | 296 | 24.2 | **296** | **24.1** | 296 | 24.1 | 296 | 24.2 |
| 436.cactusADM | 38.0 | 314 | 37.8 | 316 | **38.0** | **314** | 38.0 | 314 | 37.8 | 316 | **38.0** | **314** |
| 437.leslie3d | 61.6 | 153 | 61.4 | 153 | **61.4** | **153** | 61.6 | 153 | 61.4 | 153 | **61.4** | **153** |
| 444.namd | **470** | **17.1** | 470 | 17.1 | 470 | 17.0 | 462 | 17.4 | **462** | **17.4** | 462 | 17.3 |
| 447.dealII | **301** | **38.1** | 300 | 38.1 | 301 | 38.0 | **301** | **38.1** | 300 | 38.1 | 301 | 38.0 |
| 450.soplex | 269 | 31.0 | 271 | 30.7 | **270** | **30.9** | 269 | 31.0 | 271 | 30.7 | **270** | **30.9** |
| 453.povray | **168** | **31.7** | 169 | 31.4 | 167 | 31.9 | 141 | 37.6 | 142 | 37.4 | **142** | **37.6** |
| 454.calculix | **290** | **28.4** | 287 | 28.7 | 291 | 28.4 | 267 | 30.9 | 267 | 30.9 | **267** | **30.9** |
| 459.GemsFDTD | 90.7 | 117 | 90.7 | 117 | **90.7** | **117** | 76.0 | 140 | **76.0** | **140** | 76.0 | 140 |
| 465.tonto | 352 | 28.0 | **384** | **25.6** | 384 | 25.6 | **309** | **31.8** | 308 | 31.9 | 312 | 31.5 |
| 470.lbm | 32.4 | 424 | 32.4 | 424 | **32.4** | **424** | 32.4 | 424 | 32.4 | 424 | **32.4** | **424** |
| 481.wrf | 193 | 57.8 | 188 | 59.5 | **190** | **58.8** | 193 | 57.8 | 188 | 59.5 | **190** | **58.8** |
| 482.sphinx3 | **378** | **51.6** | 373 | 52.2 | 382 | 51.0 | **368** | **52.9** | 368 | 52.9 | 370 | 52.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-i51c Mon Feb 20 17:28:44 2012
```

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## Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2630L 0 @ 2.00GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5
      physical 1: cores 0 1 2 3 4 5
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       132089856 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 2

uname -a:
   Linux linux-i51c 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
   (54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 18 00:51 last=S

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext3  131G  7.8G  122G   6% /

Additional information from dmidecode:

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"
OMP_NUM_THREADS = "12"
```

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## General Notes (Continued)

```
The Dell PowerEdge R720 and
the Bull NovaScale R460 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R720 model
Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml


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For other inquiries, please contact webmaster@spec.org.

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