



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 84.9

PowerEdge R720 (Intel Xeon E5-2667, 2.90 GHz)

SPECfp\_base2006 = 80.7

CPU2006 license: 55

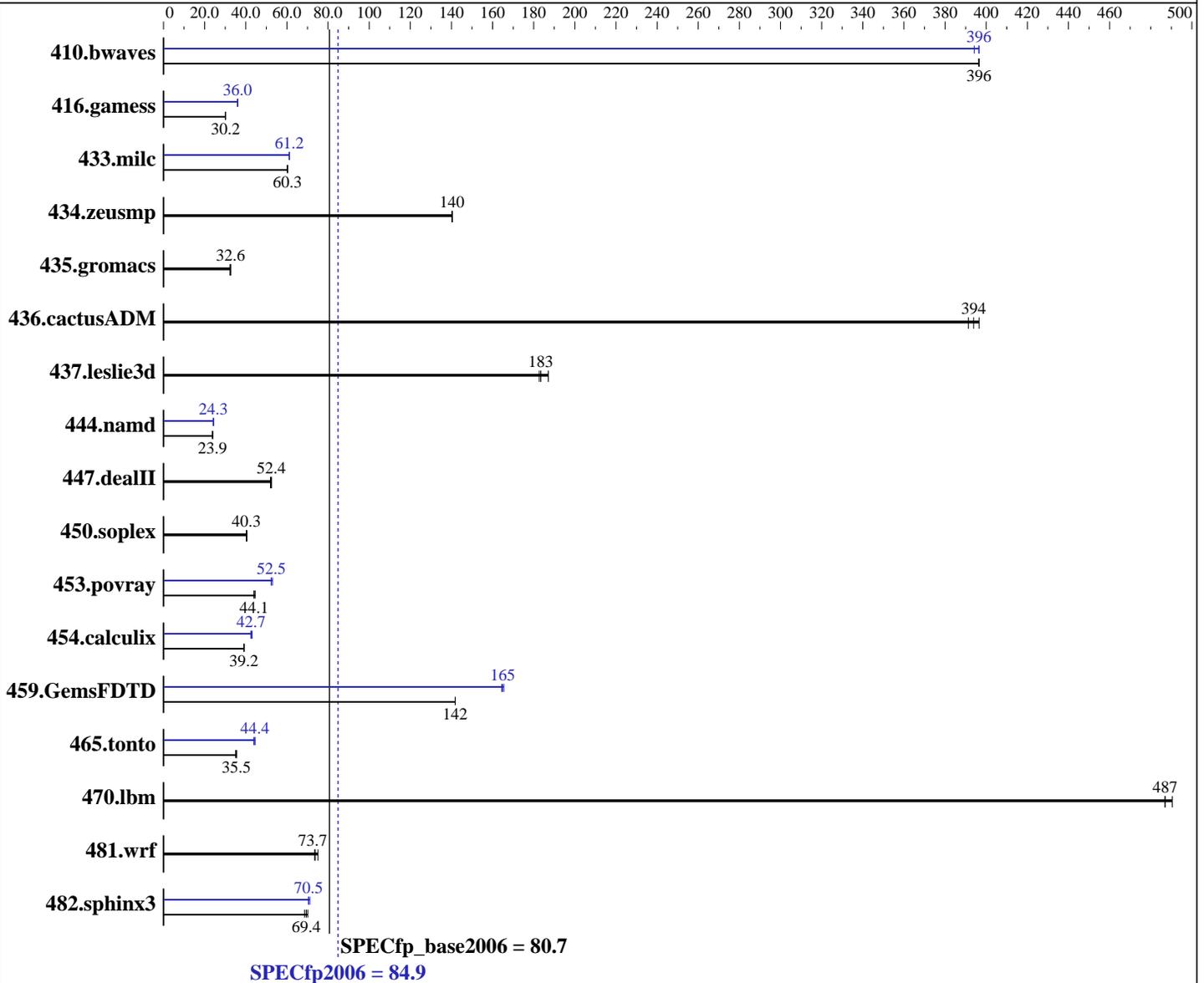
Test date: Feb-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



**Hardware**

CPU Name: Intel Xeon E5-2667  
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
CPU MHz: 2900  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext3  
System State: Run level 3 (add definition here)

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	34.3	396	<b><u>34.3</u></b>	<b><u>396</u></b>	34.3	396	34.5	394	34.3	396	<b><u>34.3</u></b>	<b><u>396</u></b>
416.gamess	648	30.2	652	30.0	<b><u>649</u></b>	<b><u>30.2</u></b>	542	36.1	544	36.0	<b><u>544</u></b>	<b><u>36.0</u></b>
433.milc	152	60.3	152	60.2	<b><u>152</u></b>	<b><u>60.3</u></b>	<b><u>150</u></b>	<b><u>61.2</u></b>	150	61.1	150	61.2
434.zeusmp	64.9	140	<b><u>64.9</u></b>	<b><u>140</u></b>	64.9	140	64.9	140	<b><u>64.9</u></b>	<b><u>140</u></b>	64.9	140
435.gromacs	221	32.3	218	32.7	<b><u>219</u></b>	<b><u>32.6</u></b>	221	32.3	218	32.7	<b><u>219</u></b>	<b><u>32.6</u></b>
436.cactusADM	30.5	391	30.1	397	<b><u>30.3</u></b>	<b><u>394</u></b>	30.5	391	30.1	397	<b><u>30.3</u></b>	<b><u>394</u></b>
437.leslie3d	51.4	183	50.2	187	<b><u>51.2</u></b>	<b><u>183</u></b>	51.4	183	50.2	187	<b><u>51.2</u></b>	<b><u>183</u></b>
444.namd	336	23.9	336	23.9	<b><u>336</u></b>	<b><u>23.9</u></b>	330	24.3	<b><u>330</u></b>	<b><u>24.3</u></b>	331	24.2
447.dealII	<b><u>218</u></b>	<b><u>52.4</u></b>	218	52.5	220	52.0	<b><u>218</u></b>	<b><u>52.4</u></b>	218	52.5	220	52.0
450.soplex	206	40.6	<b><u>207</u></b>	<b><u>40.3</u></b>	207	40.2	206	40.6	<b><u>207</u></b>	<b><u>40.3</u></b>	207	40.2
453.povray	121	44.0	<b><u>121</u></b>	<b><u>44.1</u></b>	119	44.6	<b><u>101</u></b>	<b><u>52.5</u></b>	100	53.0	102	52.3
454.calculix	<b><u>211</u></b>	<b><u>39.2</u></b>	210	39.2	211	39.1	191	43.1	<b><u>193</u></b>	<b><u>42.7</u></b>	194	42.5
459.GemsFDTD	74.8	142	<b><u>74.8</u></b>	<b><u>142</u></b>	74.8	142	64.1	165	<b><u>64.3</u></b>	<b><u>165</u></b>	64.5	164
465.tonto	277	35.5	<b><u>277</u></b>	<b><u>35.5</u></b>	281	35.1	224	43.9	<b><u>221</u></b>	<b><u>44.4</u></b>	221	44.5
470.lbm	<b><u>28.2</u></b>	<b><u>487</u></b>	28.2	487	28.0	490	<b><u>28.2</u></b>	<b><u>487</u></b>	28.2	487	28.0	490
481.wrf	<b><u>152</u></b>	<b><u>73.7</u></b>	149	75.1	152	73.6	<b><u>152</u></b>	<b><u>73.7</u></b>	149	75.1	152	73.6
482.sphinx3	278	70.1	284	68.6	<b><u>281</u></b>	<b><u>69.4</u></b>	274	71.2	276	70.5	<b><u>276</u></b>	<b><u>70.5</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

System Profile set to Custom  
 CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on linux-i51c Tue Feb 14 09:39:17 2012

This section contains SUT (System Under Test) info as seen by  
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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2667 0 @ 2.90GHz
  2 "physical id"s (chips)
  24 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 6
    siblings  : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
  cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      132089856 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux linux-i51c 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 14 03:43 last=S

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext3  131G  8.5G  121G   7% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,scatter"  
 LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"  
 OMP\_NUM\_THREADS = "12"  
 The Dell PowerEdge R720 and

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## General Notes (Continued)

the Bull NovaScale R460 F3 models are electronically equivalent.

The results have been measured on a Dell PowerEdge R720 model

Transparent Huge Pages disabled with:

```
echo never > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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