SPEC® CFP2006 Result

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Fujitsu

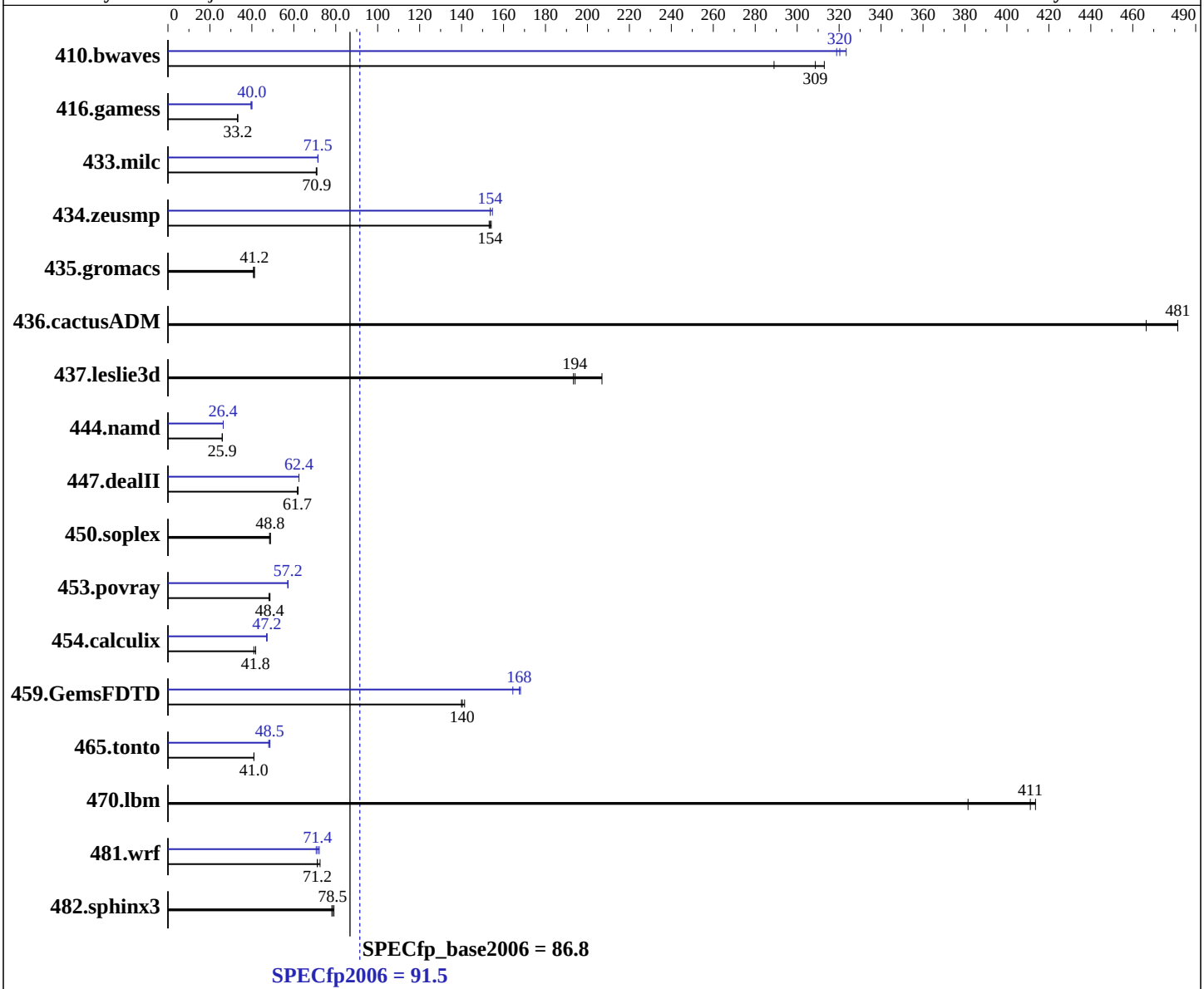
PRIMERGY RX300 S7, Intel Xeon E5-2690, 2.90 GHz

SPECfp®2006 = 91.5

SPECfp_base2006 = 86.8

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-2690
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	20 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	47.0	289	**44.0**	**309**	43.4	313	42.6	319	**42.4**	**320**	42.0	323
416.gamess	**589**	**33.2**	589	33.2	590	33.2	495	39.6	**490**	**40.0**	490	40.0
433.milc	129	71.0	**129**	**70.9**	130	70.7	**128**	**71.5**	128	71.5	128	71.6
434.zeusmp	59.4	153	59.0	154	**59.2**	**154**	58.8	155	**59.2**	**154**	59.2	154
435.gromacs	**173**	**41.2**	175	40.7	173	41.2	**173**	**41.2**	175	40.7	173	41.2
436.cactusADM	24.8	481	**24.8**	**481**	25.6	466	24.8	481	**24.8**	**481**	25.6	466
437.leslie3d	48.6	193	**48.4**	**194**	45.4	207	48.6	193	**48.4**	**194**	45.4	207
444.namd	310	25.9	309	25.9	**310**	**25.9**	304	26.4	**304**	**26.4**	304	26.4
447.dealII	184	62.1	**185**	**61.7**	185	61.7	183	62.5	183	62.4	**183**	**62.4**
450.soplex	171	48.8	**171**	**48.8**	172	48.4	171	48.8	**171**	**48.8**	172	48.4
453.povray	109	48.7	**110**	**48.4**	110	48.2	93.2	57.1	92.9	57.3	**92.9**	**57.2**
454.calculix	197	41.8	201	41.0	**197**	**41.8**	**175**	**47.2**	175	47.2	175	47.1
459.GemsFDTD	75.8	140	**75.6**	**140**	75.0	141	**63.3**	**168**	63.1	168	64.5	164
465.tonto	240	41.0	240	41.0	**240**	**41.0**	205	48.0	**203**	**48.5**	202	48.6
470.lbm	**33.4**	**411**	36.0	381	33.2	414	**33.4**	**411**	36.0	381	33.2	414
481.wrf	157	71.2	**157**	**71.2**	154	72.5	158	70.7	155	72.1	**156**	**71.4**
482.sphinx3	**248**	**78.5**	246	79.2	249	78.1	**248**	**78.5**	246	79.2	249	78.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Intel HT Technology = Disable
Frequency Floor Override = Enable
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
```

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General Notes (Continued)

```
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with
 Red Hat Enterprise Linux Server release 6.1 (Santiago)
 Added glibc-static-2.12-1.25.el6.x86_64.rpm
 to enable static linking
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -static

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.xml

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For other inquiries, please contact webmaster@spec.org.

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